

# C0510X6S0J474M030BC

TDK Item Description : C0510X6S0J474MT\*\*\*\*

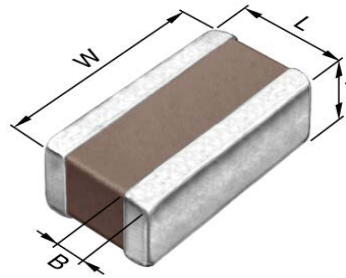


## Application & Main Feature

Commercial Grade ( Low ESL Reverse Geometry )

## Series

C0510 [EIA 0204]



## Dimensions

|   |                  |
|---|------------------|
| L | 0.52mm +/-0.05mm |
| W | 1.00mm +/-0.05mm |
| T | 0.30mm +/-0.05mm |
| B | 0.10mm Min.      |

## Temperature Characteristic

X6S (-55 to 105 degC ±22%)

## Rated Voltage

0J (6.3Vdc)

## Capacitance

470nF

## Capacitance Tolerance

M (+/-20%)

## Dissipation Factor

10% Max.

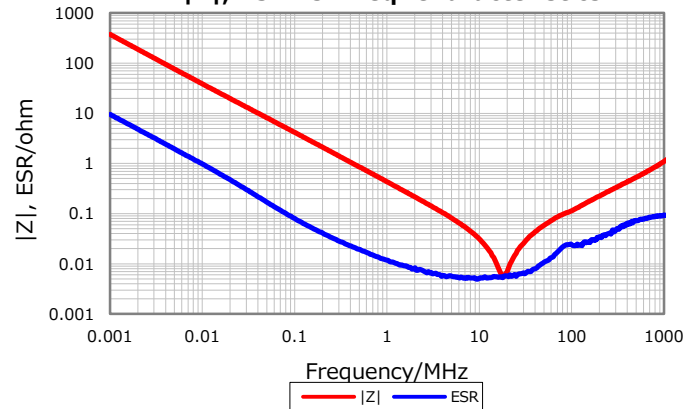
## Insulation Resistance

212Mohm Min.

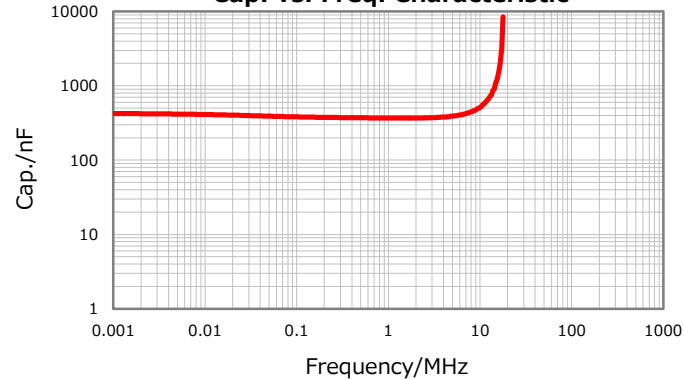
## AEC-Q200

Not Applicable

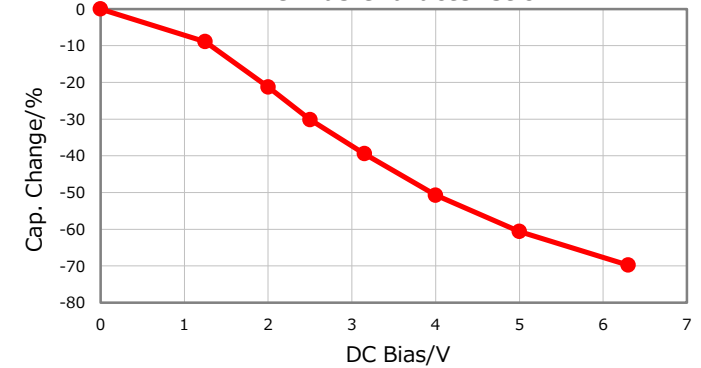
## |Z|, ESR vs. Freq. Characteristics



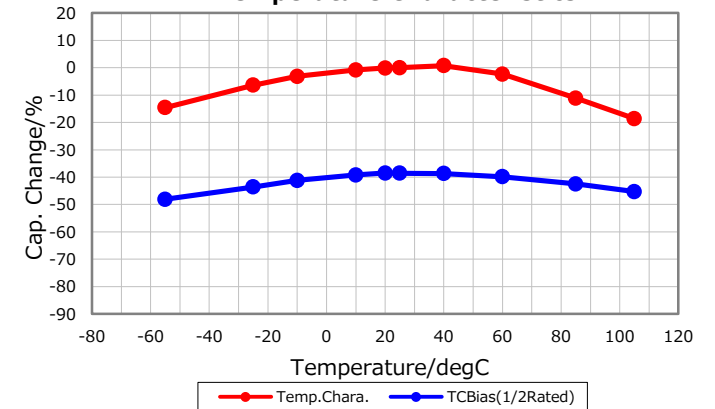
## Cap. vs. Freq. Characteristic



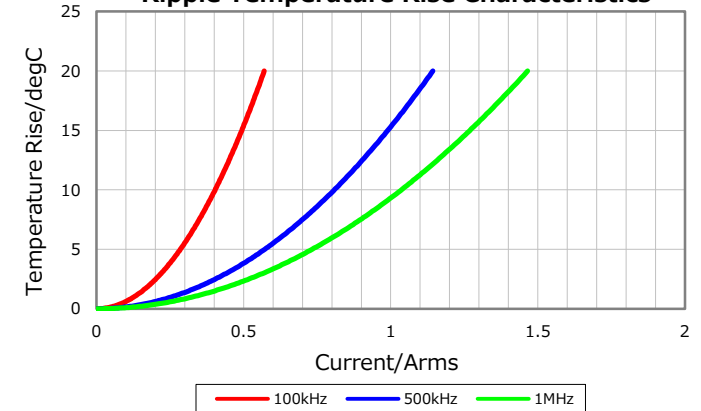
## DC Bias Characteristic



## Temperature Characteristics



## Ripple Temperature Rise Characteristics



Characterization Sheet ( Multilayer Ceramic Chip Capacitors )



All specifications are subject to change without notice.

November 27, 2017