

# KWD15

## RELIABILITY DATA

### 信頼性データ

No. RD-08T-743A		
承認	査閲	担当
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31. Oct. '08	30. Oct. '08	30. Oct. '08

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※信頼性試験は代表データであり、この値は実力値とお考え願います。

※本データに掲載しております内蔵部品の名称は、本製品を開発した当初のものです。

これらは改善等の為に変更されている可能性もありますが、ご了承下さい。

The following data are typical values and the data to be considered as ability values.  
 The built-in components names on this data are the things the time of Development.  
 Please understand that it may be changed for an improvement etc.

M . T . B . F

## 1. Method of calculation

This calculation is by the components count method laid down by the DC Stabilized Power Supplies (Switching mode) committee of EIAJ.

The MTBF is determined by means of a fixed component failure rate  $\lambda_c$  given to each component and the number of component count of each type of component.  $\lambda_c$  is determined based on MIL-HDBK-217D.

Please refer to the EIAJ handbook no. RCF-9021 for detail.

Formula:

$$\text{MTBF} = \frac{1}{\lambda_{\text{equip}}} = \frac{1}{\sum_{i=1}^n N_i(\lambda_c)_i} \times 10^6 \quad (\text{Hrs})$$

$\lambda_{\text{equip}}$  = Total equipment failure rate (Failures/10<sup>6</sup>hrs)

$\lambda_c$  = Failure rate of the  $i^{th}$  component

$N_i$  = Number of  $i^{th}$  component

$n$  = Number of categories of components

## 2. MTBF Value

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MTBF = 80866 hrs.

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Components Derating Data(At Nominal Line and Rated Load, Ambient Temperature 50°C)Calculation MethodA. Semiconductors

The derating factor is taken as the ratio of the actual operating junction temperature taking into consideration operating ambient temperature, power loss and thermal resistance to the maximum rated junction temperature specifications of the components.

B. IC, Resistors, Capacitors etc.

Operating ambient temperature, operating condition, power loss for each individual component are all designed to meet the requirements of Nemic-Lambda's design standard.

C. Thermal Resistance Calculation

$$\theta_{jc} = \frac{T_j(\max) - T_c}{P_c(\max)}$$

$$\theta_{ja} = \frac{T_j(\max) - T_a}{P_c(\max)}$$

Tc : Case Temperature (Normally 25°C)

Ta : Ambient Temperature (Normally 25°C)

Pc(max) : Maximum Power Loss

Tj(max) : Maximum Junction Temperature

θjc : Junction to Case Thermal Resistance

θja : Junction to ambient Thermal Resistance

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SEMICONDUCTOR DERATING

DWG. NO. : PA775-56-03

DATE : 6-DEC-92

MODEL : KWD15-1212

VIN = AC 100V

LOAD = 100%

Ta = 50°C

Q1 2SK1510 FUJI	Tchmax = 150 °C	θch-c = 1.563 °C/W	Pdmax = 80.0 W
	Pd = 0.64 W	Δ Tc = 41.2 °C	Tc = 91.2 °C
	Tj = Tc + (θch-c) * Pd =	92.2 °C	
	D.F. = 61.5 %		
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Q2 2SC2873-Y TOSHIBA	Tjmax = 150 °C	θj-c = 125 °C/W	Pdmax = 1 W
	Pd = 0 W	Δ Tc = 33.0 °C	Tc = 83.0 °C
	Tj = Tc + (θj-c) * Pd =	83.0 °C	
	D.F. = 55.3 %		
<hr/>			
A1 UC2842ADW UNITRODE	Tjmax = 150 °C	θj-c = 70 °C/W	Pdmax = 0.725 W
	Pd = 0.435 W	Δ Tc = 36.3 °C	Tc = 86.3 °C
	Tj = Tc + (θj-c) * Pd =	116.8 °C	
	D.F. = 77.9 %		
<hr/>			
A2 HA17431UA HITACHI	Tjmax = 125 °C	θj-c = 259.7 °C/W	Pdmax = 0.39 W
	Pd = 17.77 mW	Δ Tc = 32.8 °C	Tc = 82.8 °C
	Tj = Tc + (θj-c) * Pd =	87.4 °C	
	D.F. = 69.9 %		
<hr/>			
PC1 (LED) TLP121GR TOSHIBA	Tjmax = 125 °C	θj-c = 400 °C/W	Pdmax = 50 mW
	If = 1.3 mA	Δ Tc = 33.8 °C	Tc = 83.8 °C
	ALLOWABLE If (max) =	29.0 mA (at 83.8 °C)	
	D.F. = 4.5 %		
<hr/>			
PC1 (TRANSISTOR) TLP121GR TOSHIBA	Tjmax = 125 °C	θj-c = 400 °C/W	Pdmax = 150 mW
	Pd = 5.2 mW	Δ Tc = 33.8 °C	Tc = 83.8 °C
	Tj = Tc + (θj-c) * Pd =	85.9 °C	
	D.F. = 68.7 %		
<hr/>			
D1 S1WB(A)60B SHINDENGEN	Tjmax = 150 °C	θj-l = 10 °C/W	Pdmax = 12.5 W
	Pd = 0.286 W	Δ TI = 41.4 °C	T(lead) = 91.4 °C
	Tj = TI + (θj-l) * Pd =	94.3 °C	
	D.F. = 62.9 %		

SEMICONDUCTOR DERATING

DWG. NO. : PA775-56-04

DATE : 6-DEC-92

MODEL : KWD15-1212

VIN = AC 100V      LOAD = 100%      Ta = 50°C

D2 EC8FS6 NIHON-INTER	T <sub>jmax</sub> = 150 °C	Θ <sub>j-l</sub> = 23 °C/W	P <sub>dmax</sub> = 5.43 W
	P <sub>d</sub> = 37 mW	Δ T <sub>I</sub> = 41.0 °C	T(lead) = 91.0 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		91.9 °C
	D.F. = 61.3 %		
D3 1SS184TE85L TOSHIBA	T <sub>jmax</sub> = 125 °C	Θ <sub>j-l</sub> = 100 °C/W	P <sub>dmax</sub> = 150 mW
	P <sub>d</sub> = 0 W	Δ T <sub>I</sub> = 40.2 °C	T(lead) = 90.2 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		90.2 °C
	D.F. = 72.2 %		
D4 1SS184TE85L TOSHIBA	T <sub>jmax</sub> = 125 °C	Θ <sub>j-l</sub> = 100 °C/W	P <sub>dmax</sub> = 150 mW
	P <sub>d</sub> = 10.8 mW	Δ T <sub>I</sub> = 37.2 °C	T(lead) = 87.2 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		88.3 °C
	D.F. = 70.6 %		
D5 D1FL20U SHINDENGEN	T <sub>jmax</sub> = 150 °C	Θ <sub>j-l</sub> = 23 °C/W	P <sub>dmax</sub> = 5.43 W
	P <sub>d</sub> = 28.6 mW	Δ T <sub>I</sub> = 39.3 °C	T(lead) = 89.3 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		90.0 °C
	D.F. = 60.0 %		
D6 D1FL20U SHINDENGEN	T <sub>jmax</sub> = 150 °C	Θ <sub>j-l</sub> = 23 °C/W	P <sub>dmax</sub> = 5.43 W
	P <sub>d</sub> = 0.159 W	Δ T <sub>I</sub> = 52.7 °C	T(lead) = 102.7 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		106.4 °C
	D.F. = 70.9 %		
D7 D1FL20U SHINDENGEN	T <sub>jmax</sub> = 150 °C	Θ <sub>j-l</sub> = 23 °C/W	P <sub>dmax</sub> = 5.43 W
	P <sub>d</sub> = 0.179 W	Δ T <sub>I</sub> = 52.7 °C	T(lead) = 102.7 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		106.8 °C
	D.F. = 71.2 %		
D8 D1FL20U SHINDENGEN	T <sub>jmax</sub> = 150 °C	Θ <sub>j-l</sub> = 23 °C/W	P <sub>dmax</sub> = 5.43 W
	P <sub>d</sub> = 0.179 W	Δ T <sub>I</sub> = 52.7 °C	T(lead) = 102.7 °C
	T <sub>j</sub> = T <sub>I</sub> + (Θ <sub>j-l</sub> ) * P <sub>d</sub> =		106.8 °C
	D.F. = 71.2 %		

SEMICONDUCTOR DERATING

DWG. NO. : PA775-56-05

DATE : 6-DEC-92

MODEL : KWD15-1212

VIN = AC 100V

LOAD = 100%

Ta = 50°C

D9 D1FL20U SHINDENGEN	Tjmax = 150 °C	Θj-I = 23 °C/W	Pdmax = 5.43 W
	Pd = 0.179 W	Δ Tl = 52.7 °C	T(lead) = 102.7 °C
	Tj = Tl + (Θj-I)*Pd = 106.8 °C		
	D.F. = 71.2 %		
D10 D1FL20U SHINDENGEN	Tjmax = 150 °C	Θj-I = 23 °C/W	Pdmax = 5.43 W
	Pd = 0.179 W	Δ Tl = 52.3 °C	T(lead) = 102.3 °C
	Tj = Tl + (Θj-I)*Pd = 106.4 °C		
	D.F. = 70.9 %		
D11 D1FL20U SHINDENGEN	Tjmax = 150 °C	Θj-I = 23 °C/W	Pdmax = 5.43 W
	Pd = 0.179 W	Δ Tl = 52.3 °C	T(lead) = 102.3 °C
	Tj = Tl + (Θj-I)*Pd = 106.4 °C		
	D.F. = 70.9 %		
D12 D1FL20U SHINDENGEN	Tjmax = 150 °C	Θj-I = 23 °C/W	Pdmax = 5.43 W
	Pd = 0.179 W	Δ Tl = 52.3 °C	T(lead) = 102.3 °C
	Tj = Tl + (Θj-I)*Pd = 106.4 °C		
	D.F. = 70.9 %		
D13 D1FL20U SHINDENGEN	Tjmax = 150 °C	Θj-I = 23 °C/W	Pdmax = 5.43 W
	Pd = 0.179 W	Δ Tl = 52.3 °C	T(lead) = 102.3 °C
	Tj = Tl + (Θj-I)*Pd = 106.4 °C		
	D.F. = 70.9 %		
ZD1 1N4744A MOTOROLA	Tjmax = 200 °C	Θj-I = 175 °C/W	Pdmax = 1 W
	Pd = 0 W	Δ Tl = 37.2 °C	T(lead) = 87.2 °C
	Tj = Tl + (Θj-I)*Pd = 87.2 °C		
	D.F. = 43.6 %		
ZD2 02CZ12-Z TOSHIBA	Tjmax = 150 °C	Θj-I = 100 °C/W	Pdmax = 150 mW
	Pd = 21.6 mW	Δ Tl = 32.3 °C	T(lead) = 82.3 °C
	Tj = Tl + (Θj-I)*Pd = 84.5 °C		
	D.F. = 56.3 %		

Δ T TEMPERATURE RISE

DWG. NO.	PA775-66-02
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MODEL : KWD15-1212

DATE : 6-DEC-92

INPUT VOLTAGE = 100VAC

Ta = 50°C		Δ T TEMPERATURE RISE (°C)				
OUTPUT DERATING (%)		100%	100%	100%	100%	100%
SYMBOL	PARTS NAME	MOUNTING A	MOUNTING B	MOUNTING C	MOUNTING D	MOUNTING E
Q1	MOSFET	41.2	42.8	41.9	42.5	41.6
A1	PWM IC	36.3	39.2	36.9	38.7	37.0
D6	UFRD	52.7	49.7	50.5	49.3	50.3
D11	UFRD	52.3	54.3	54.6	53.5	54.7
C6	E. CAP	34.2	34.6	32.6	33.6	33.0
C18	OS CAP	34.6	34.1	33.6	34.4	36.8
C21	OS CAP	33.3	30.3	32.2	31.7	34.0

INPUT VOLTAGE = 200VAC

Ta = 50°C		Δ T TEMPERATURE RISE (°C)				
OUTPUT DERATING (%)		100%	100%	100%	100%	100%
SYMBOL	PARTS NAME	MOUNTING A	MOUNTING B	MOUNTING C	MOUNTING D	MOUNTING E
Q1	MOSFET	48.4	49.7	47.8	47.8	47.5
A1	PWM IC	42.2	41.8	38.6	40.1	38.6
D6	UFRD	53.4	53.6	53.6	54.0	54.1
D11	UFRD	54.5	54.5	54.2	54.7	58.6
C6	E. CAP	37.1	38.2	35.1	35.9	35.7
C18	OS CAP	36.0	39.6	39.0	38.9	31.0
C21	OS CAP	30.8	31.1	34.0	35.6	36.8

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

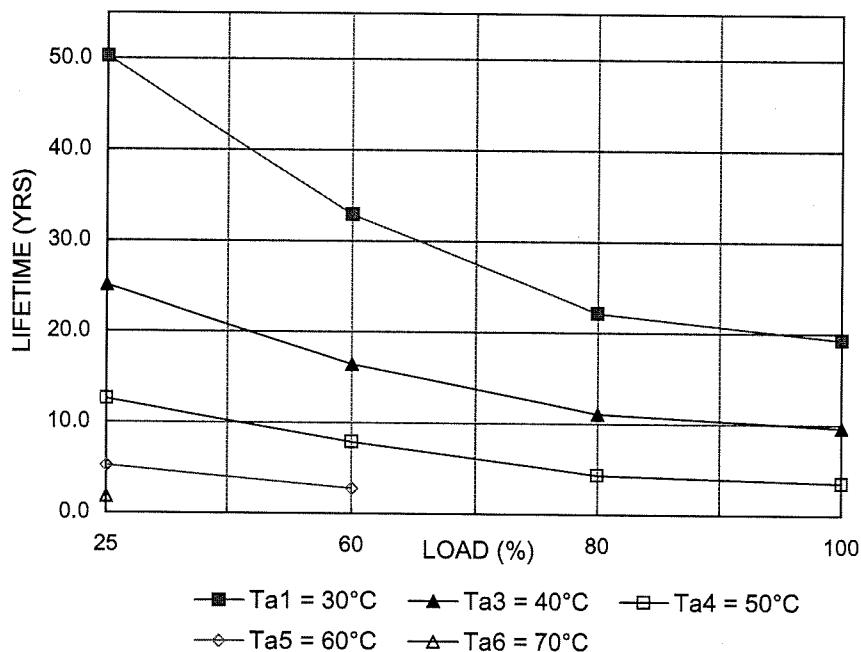
MOUNTING : A

VIN : 100VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	50.2	25.1	12.6	5.2	1.8
60	32.9	16.5	7.9	2.8	
80	22.2	11.1	4.3		
100	19.3	9.7	3.6		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING A KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
Al. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo :

電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

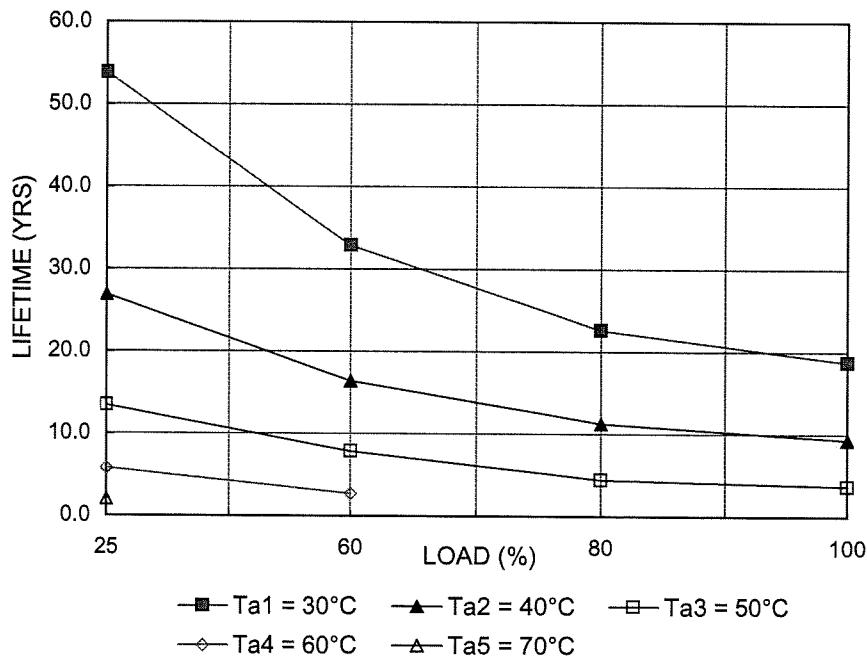
MOUNTING : B

VIN : 100VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	53.9	26.9	13.5	5.8	2.0
60	32.9	16.5	7.9	2.8	
80	22.6	11.3	4.5		
100	18.8	9.4	3.8		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING B KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24 時間連続稼動、365 日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

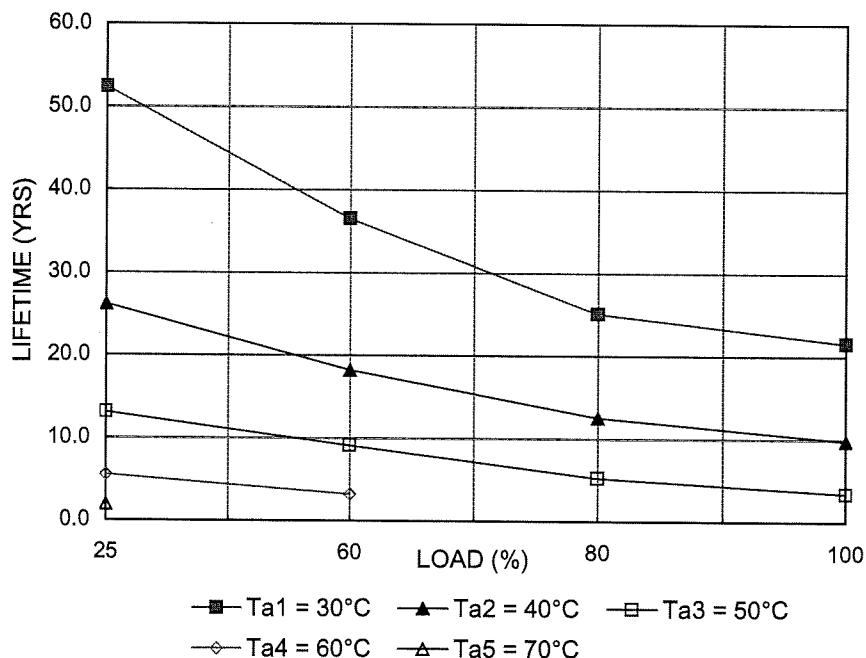
MOUNTING : C

VIN : 100VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	52.4	26.2	13.1	5.6	2.0
60	36.5	18.3	9.1	3.2	
80	25.1	12.6	5.2		
100	21.6	9.8	3.5		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING C KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
Al. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値  
Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ  
O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値  
Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度  
Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

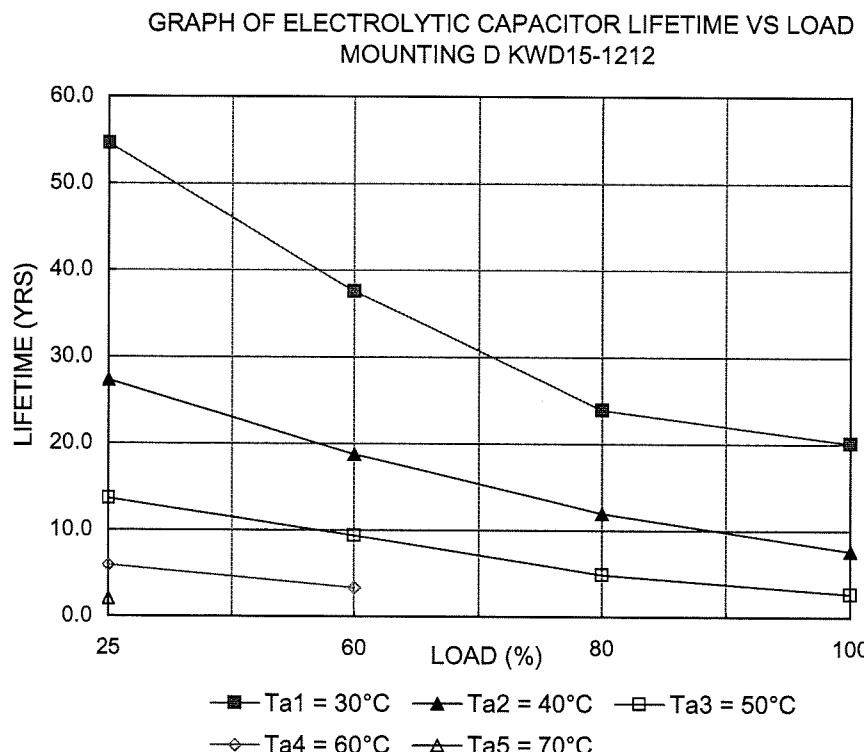
MODEL : KWD15-1212

MOUNTING : D

VIN : 100VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	54.6	27.3	13.7	5.9	2.1
60	37.6	18.8	9.4	3.4	
80	23.9	12.0	4.9		
100	20.1	7.6	2.7		



### 計算式 FORMULA

1. アルミ電解コンデンサ

AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

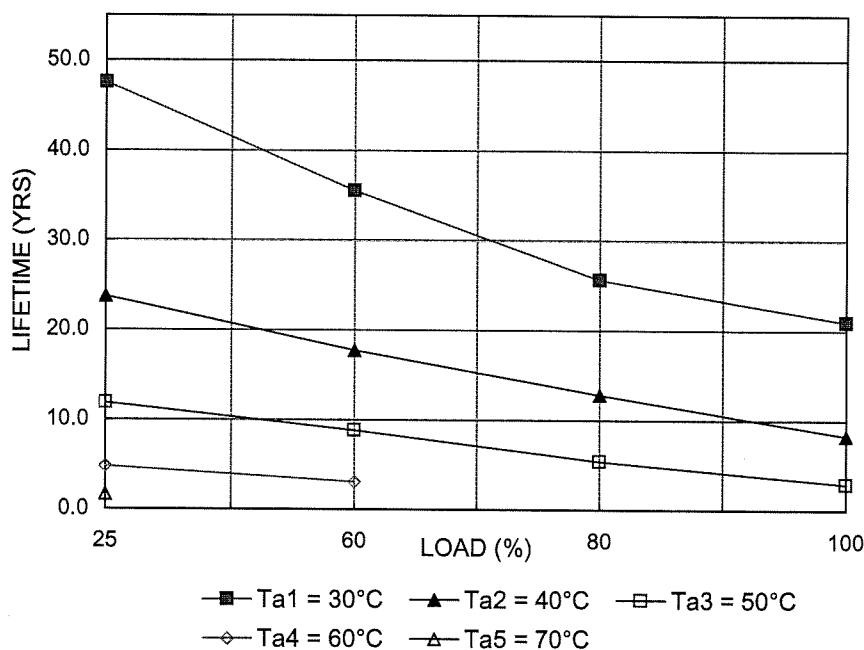
MOUNTING : E

VIN : 100VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	47.5	23.8	11.9	4.8	1.7
60	35.5	17.8	8.9	3.1	
80	25.7	12.8	5.4		
100	21.0	8.3	2.9		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING E KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
Al. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24 時間連続稼動、365 日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo :

電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

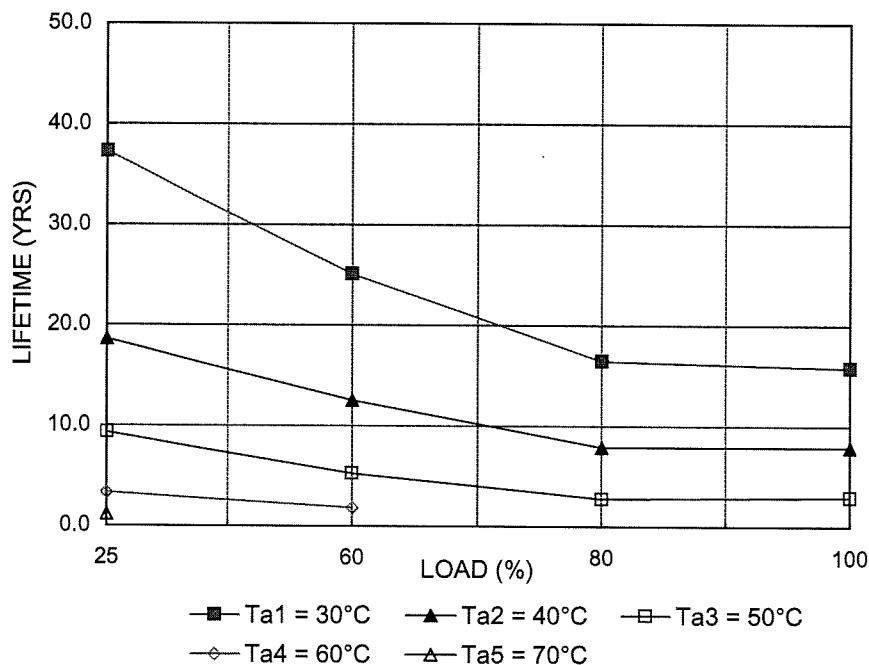
MOUNTING : A

VIN : 200VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	37.3	18.6	9.3	3.3	1.2
60	25.1	12.6	5.2	1.8	
80	16.5	7.9	2.8		
100	15.8	7.9	2.9		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING A KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
Al. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo :

電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

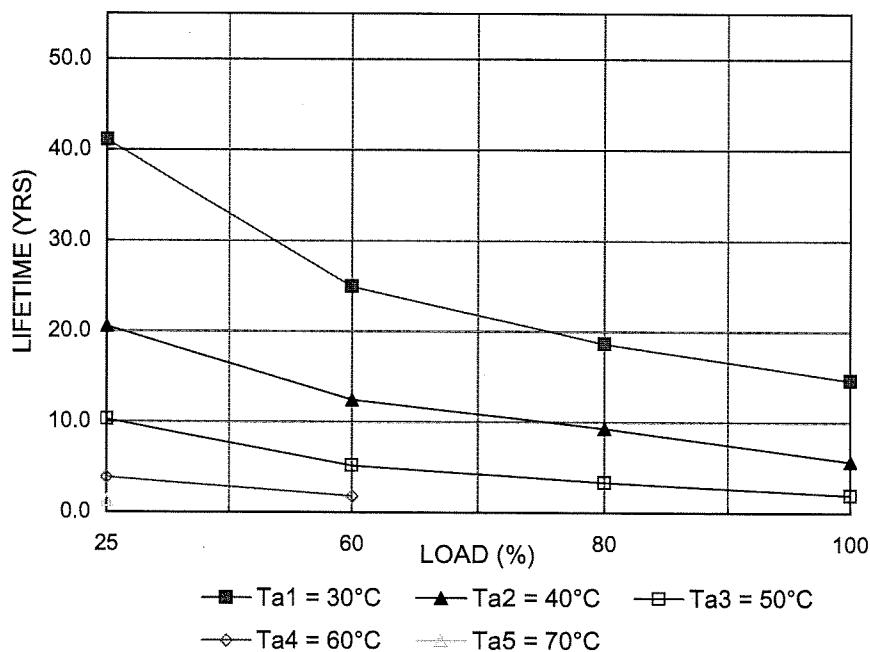
MOUNTING : B

VIN : 200VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	41.1	20.5	10.3	3.9	1.4
60	25.0	12.5	5.2	1.8	
80	18.6	9.3	3.3		
100	14.6	5.7	2.0		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING B KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo :

電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

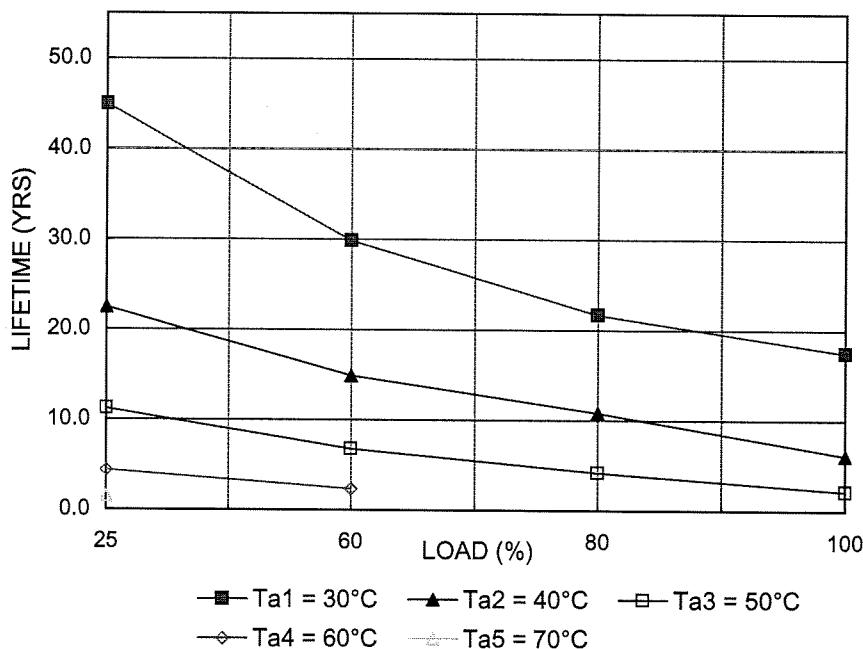
MOUNTING : C

VIN : 200VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	45.0	22.5	11.2	4.4	1.6
60	29.9	14.9	6.8	2.4	
80	21.7	10.9	4.2		
100	17.5	6.1	2.2		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING C KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Ta)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値

Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ

O.S capacitor

$$L = Lo \times 10^{(105-Ta)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値

Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

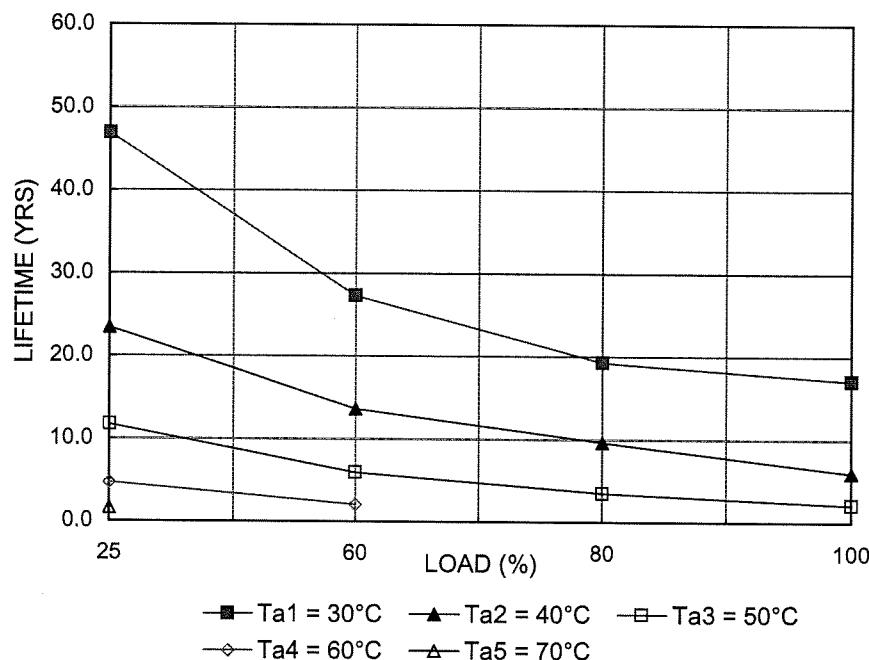
MOUNTING : D

VIN : 200VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	46.9	23.4	11.7	4.7	1.7
60	27.3	13.7	5.9	2.1	
80	19.3	9.7	3.5		
100	17.1	6.0	2.1		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING D KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値  
Elec. Capacitor computed life.

(24 時間連続稼動、365 日)  
(24 hrs per day, 365 days per year)

2. OSコンデンサ  
O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値  
Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度  
Case temperature of Elec. cap.

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : KWD15-1212

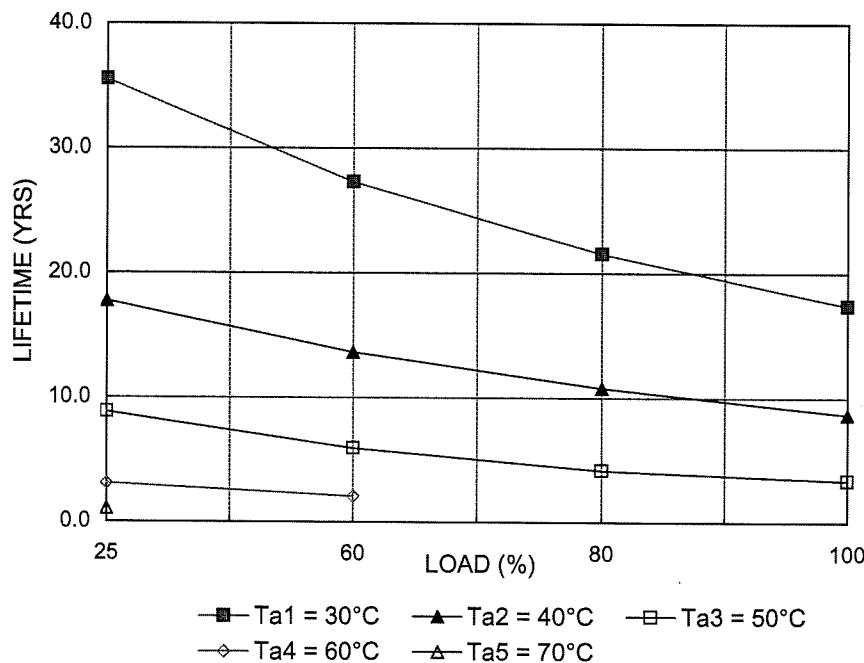
MOUNTING : E

VIN : 200VAC

DATE: SEPT 12, 2008

LOAD (%)	LIFETIME (YRS)				
	Ta = 30°C	Ta = 40°C	Ta = 50°C	Ta = 60°C	Ta = 70°C
25	35.5	17.8	8.9	3.1	1.1
60	27.3	13.7	5.9	2.1	
80	21.6	10.8	4.2		
100	17.4	8.7	3.4		

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VS LOAD  
MOUNTING E KWD15-1212



### 計算式 FORMULA

1. アルミ電解コンデンサ  
AL. Electrolytic capacitor

$$L = Lo \times 2^{(105-Tc)/10} \quad (\text{year})$$

L : 電解コンデンサ推定寿命計算値  
Elec. Capacitor computed life.

(24時間連続稼動、365日)

(24 hrs per day, 365 days per year)

2. OSコンデンサ  
O.S capacitor

$$L = Lo \times 10^{(105-Tc)/22} \quad (\text{year})$$

Lo : 電解コンデンサ保証寿命値  
Guarantee life for Elec. cap.

Tc : 電解コンデンサのケース温度

Case temperature of Elec. cap.

MODEL : KWTD15-1212		ABNORMAL TESTING												TEST CONDITIONS		APPROVED BY TESTED BY	
														LOAD = 100%		Vin = 200VAC	
														Ta = 25°C		Ta = 25°C	
PARTS NAME	PART NO.	TEST MODE	S	O	F	S	B	S	R	D	F	O	O	N	O	R	N
		H	P	I	M	S	B	A	D	A	U	O	O	C	E	O	
		O	E	R	O	M	U	E	M	E	D	O	O	H	T	K	
		R	N	E	K	K	R	S	S	L	A	E	O	T	E	S	
		T	T	E	E	E	T	L	L	H	G	O	P	N	T	O	
1	MOSFET	Q1	D-G	Y				C	V	P	P	T	P	R			
2	2SK1510-01L		D-S	Y				·	·	·	·	·	U	G			
3		G-S	Y					Y	Y	Y	Y	Y	Y	Y	Y	Y	
4		D	Y										Y	Y	Y	Y	
5		S	Y										Y	Y	Y	Y	
6		G	Y										Y	Y	Y	Y	
7																	
8	TRANSISTOR	Q2	C-E	Y									Y	Y	Y	Y	
9	2SC2873-Y-TE12L		C-B	Y									Y	Y	Y	Y	
10		B-E	Y										Y	Y	Y	Y	
11		C	Y										Y	Y	Y	Y	
12		E	Y										Y	Y	Y	Y	
13		B	Y										Y	Y	Y	Y	
14																	
15	I.C.	A1	1-2	Y									Y	Y	Y	Y	
16	UC2842ADW		2-3	Y									Y	Y	Y	Y	
17			3-4	Y													
18			4-5	Y													
19			5-6	Y													
20			6-7	Y													
21			7-8	Y													
22			9-10	Y													
23			10-11	Y													
24			11-12	Y													
25																	

\*\*\* A : SUGHT B : PROLONGED

TDK-Lambda

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MODEL : KWD15-1212		ABNORMAL TESTING												TEST CONDITIONS		APPROVED   TESTED		DWG NO: PA775-57-03	
														LOAD = 100%		Vin = 200VAC Ta = 25°C		CCNEO 15/10 3/23	
PART NAME	PART NO.	TEST MODE												NOTE					
		S	O	F	S	S	B	R	D	F	O	O	N	O					
1	A1	H	P	I	M	M	U	M	E	A	U	.	O	O	C	T	R	N	
2		O	E	R	O	R	E	D	W	S	B	C	V	U	H	E	E	O	
3		R	N	E	K	K	S	L	A	E	L	.	O	T	A	S	K	G	
4		T			E	E	T	L	H	G	O	P	P	P	N	R	S	O	
5								O	E	W	.	W	.	U	G	T	T	T	
6								T	N	T	N	.	T	E				D	
7																			
8																			
9																			
10																			
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25																			

\*\*\* A : SLIGHT B : PROLONGED

TDK-Lambda

MODEL : KWD15-1212		ABNORMAL TESTING										TEST CONDITIONS		TESTED		
												LOAD = 100%		Vin = 200VAC Ta = 25°C		
	PARTS NAME	PART NO.	TEST MODE	S	O	F	S	B	S	R	D	F	O	O	N	O
				H	P	I	M	U	M	E	A	U	O	O	C	T
1	SHUNT REGULATOR	A2	K-A	Y												
2	HA17431UA-TL		K-R	Y												
3			R-A	Y												
4			K	Y												
5			A	Y												
6			R	Y												
7																
8	PHOTO COUPLER	PC1	1-3	Y												
9	TLP121GR-TPL		4-6	Y												
10				1	Y											
11					3	Y										
12					4	Y										
13					6	Y										
14																
15	CHIP BRIDGE	D1	AC	Y												
16	S1WB(A)60B	D1	AC	Y												
17		D1	DC	Y												
18		D1	DC	Y												
19																
20																
21	CHIP DIODE	D2	A-K	Y												
22	EC8FS6-TE12L		A-K	Y												
23																
24	ISS184-TE85L	D3	A-K	Y												
25			A-K	Y												

\*\*\* A : SUGHT B : PROLONGED

TDK-Lambda

MODEL: KWD15-1212		ABNORMAL TESTING												TEST CONDITIONS		DWG NO: PA775-57-05	
														APPROVED TESTED			
														V <sub>IN</sub> = 200VAC		LCN20 1703/93	
PARTS NAME	PART NO.	TEST MODE	S	O	F	S	B	S	R	D	F	O	O	N	O	R	
		H	P	I	M	M	U	M	E	A	U	O	O	C	E		
		O	E	R	O	O	R	E	D	M	S	H	H	T	O		
		R	N	E	K	K	S	L	A	E	B	U	U	K	E		
		T	T	E	E	E	T	L	H	G	O	T	T	S	G		
1	1SS184-TE85L	D4	A-K	Y					G	O	W	.	.	N	R		
2			A-K	Y					E	N	T	.	.	E	T		
3									T	O	E	.	.	A	E		
4	CHIP DIODE	D5	A-K	Y					T	H	G	P	P	N	G		
5	D1FL20U		A-K	Y					E	O	W	.	.	R	S		
6									T	T	O	.	.	T	S		
7	CHIP DIODE	D6	A-K	Y					N	E	T	.	.	E	T		
8	D1FL20U		A-K	Y					T	T	E	.	.	T	D		
9																	
10	CHIP DIODE	D7	A-K	Y													
11	D1FL20U		A-K	Y													
12																	
13	CHIP DIODE	D8	A-K	Y													
14	D1FL20U		A-K	Y													
15																	
16	CHIP DIODE	D9	A-K	Y													
17	D1FL20U		A-K	Y													
18																	
19	CHIP DIODE	D10	A-K	Y													
20	D1FL20U		A-K	Y													
21																	
22	CHIP DIODE	D11	A-K	Y													
23	D1FL20U		A-K	Y													
24																	
25																	

\*\*\* A: SIGHT B: PROLONGED

MODEL : KWD15 - 1212		ABNORMAL TESTING												TEST CONDITIONS		DWG NO: PA775-57-06	
														LOAD = 100%		APPROVED TESTED	
														Vin = 200VAC		LCCN E0 17n:10.3	
	PARTS NAME	PART NO.	TEST MODE	S	O	F	S	B	S	R	D	F	O	O	N	O	
			H	P	I	M	U	M	E	A	U	C	O	O	C	T	
			O	E	R	O	R	R	D	M	S	V	O	O	H	H	
			R	N	E	K	S	L	A	A	E	P	T	H	A	E	
			T	E	E	E	T	L	H	G	O	P	P	N	R	S	
1	CHIP DIODE	D12	A-K	Y				A	B	T	O	W	-	T	U	G	
2	CHIP ZENER DIODE	D1FL20U	A-K	Y												Hiccup	
3																	
4	CHIP DIODE	D13	A-K	Y													
5	CHIP ZENER DIODE	D1FL20U	A-K	Y													
6																	
7	ZENER DIODE	ZD1	A-K	Y													
8	1N4744A		A-K	Y													
9																	
10	CHIP ZENER DIODE	ZD2	A-K	Y													
11	02CZ12-Z		A-K	Y													
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	

\*\*\* A: SUGHT B: PROLONGED

TDK-Lambda

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MODEL : KWD15-1212		ABNORMAL TESTING										TEST CONDITIONS		DWG NO: PA775 - 57-07	
												LOAD = 100%		APPROVED TESTED	
												Vin = 200VAC		CCNEO 17/193	
PARTS NAME	PART NO.	TEST MODE										NOTE		R	N
		S	O	F	S	B	R	D	F	O	O	N	O	E	O
		H	P	I	M	U	M	E	A	U	O	O	C	T	G
		O	E	R	O	R	D	M	S	B	C	V	H	H	S
		R	N	E	K	K	E	A	E	L	P	P	A	E	O
		T	T	E	E	T	L	H	G	O	U	U	N	R	T
1	CAP. FILM	C1		Y		A	B	T	E	W	.	T	G	S	D
2	MKC-S683M			Y						Y		Y		Y	
3															
4	CAP. CERAMIC	C2		Y										Y	
5	ECKDNS101MB			Y										Y	
6															
7	CAP. CERAMIC	C3		Y										Y	
8	ECKDNS332ME			Y										Y	
9															
10	NOT ASSIGNED	C4													
11															
12															
13	CAP. CERAMIC	C5		Y										Y	
14	ECKDNS332ME			Y										Y	
15															
16	CAP. ELECT	C6		Y										Y	
17	LXA400VBSN-75(M)			Y										Y	
18															
19	CHIP CAP. CERAMIC	C7		Y										Y	
20	GR43-2W5R103K500RT			Y										Y	
21															
22	CHIP CAP. CERAMIC	C8		Y										Y	
23	C3216X7R1E334KT			Y										Y	
24															
25	NOT ASSIGNED	C9													

\*\*\* A : SLIGHT B : PROLONGED

TDK-Lambda

MODEL: KWD15-1212		TEST CONDITIONS												DWG NO: PA775-57-08	
		LOAD = 100% Vin = 200VAC Ta = 25°C												APPROVED TESTED	
		ABNORMAL TESTING												CCNEO 170323	
PARTS NAME	PART NO.	TEST MODE	S	O	F	S	S	B	R	D	F	O	O	N	O
1 CHIP CAP. CERAMIC	C10	Y	H	P	I	M	M	U	M	E	A	O	O	O	O
2 C55Y5U1E186Z-TE12		Y	O	E	R	O	O	R	E	D	M	S	B	C	H
3			R	N	E	K	K	S	L	A	E	L	E	T	A
4 CHIP CAP. CERAMIC	C11	Y													
5 C2012X7R1E104KT		Y													
6															
7 CHIP CAP. CERAMIC	C12	Y													
8 C3225COG1H472JT		Y													
9															
10 CHIP CAP. CERAMIC	C13	Y													
11 C2012COG1H221KT		Y													
12															
13 CHIP CAP. CERAMIC	C14	Y													
14 C2012X7R1H23KT		Y													
15															
16 CHIP CAP. CERAMIC	C15	Y													
17 C2012COG1H101KT		Y													
18															
19															
20 CHIP CAP. CERAMIC	C16	Y													
21 CM21W5R331K200BT		Y													
22															
23 CHIP CAP. CERAMIC	C17	Y													
24 C25Y5U1E106Z		Y													
25															

\*\*\* A : SLIGHT B : PROLONGED

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\*\*\* A : SUGHT B : PROLONGED

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MODEL: KWD15-1212		ABNORMAL TESTING		TEST CONDITIONS		DWG NO: PA775-57-10	APPROVED	TESTED
				LOAD = 100%	Vin = 200VAC	Ta = 25°C	CC NCO	170/23
		TEST MODE		NOTE				
PART NO.	PART NAME	S O F S R D F O O N O C T R N	H P I M B D M E A U M E D M S B C V O O C T H H A E R	O R N E R O N E K S L A E L . . P P N G S	O R N E R O N E K S L A E L . . P P N G S	O R N E R O N E K S L A E L . . P P N G S	O R N E R O N E K S L A E L . . P P N G S	O R N E R O N E K S L A E L . . P P N G S
1	CHIP RESISTOR R1	Y						
2	ERJ8GEYJ304V	Y						
3								
4	CHIP RESISTOR R2	Y						
5	ERJ8GEYJ304V	Y						
6								
7	CHIP RESISTOR R3	Y						
8	ERJ8GEYJ304V	Y						
9								
10	METAL O. RESISTOR R4	Y						
11	ERG1SJ-623	Y						
12								
13	METAL O. RESISTOR R5	Y						
14	ERG1SJ-623	Y						
15								
16	CHIP RESISTOR R6	Y						
17	ERJ8GEYJ823V	Y						
18								
19	CHIP RESISTOR R7	Y						
20	ERJ8GEYJ823V	Y						
21								
22	CHIP RESISTOR R8	Y						
23	ERJ8GEYJ823V	Y						
24								
25								

\*\*\* A : SUGHT B : PROLONGED

TDK-Lambda

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MODEL : KWD15 - 1212		ABNORMAL TESTING		TEST CONDITIONS		APPROVED	TESTED
				LOAD = 100%	Vin = 200VAC Ta = 25°C	CCNEO 17/3/93	Not done
		TEST MODE		NOTE			
PART NO.	PART NAME	S O F S R D F O O N O O C T H A E R N E R O K E E T L H G O P U N R G S E	H P I M U M E D M A S B C V U T P U G S E	O E R O K E E T L H G O P U N R G S E	O E R O K E E T L H G O P U N R G S E	O E R O K E E T L H G O P U N R G S E	O E R O K E E T L H G O P U N R G S E
1	CHIP RESISTOR R9	Y				Y	Y
2	ERJ8GEYJ823V		Y			Y	Y
3						Y	Y
4	CHIP RESISTOR R10	Y	Y			Y	Y
5	ERJ8GEYJ100V		Y			Y	Y
6						Y	Y
7	CHIP RESISTOR R11	Y	Y			Y	Y
8	ERJ8GEYJ563V		Y			Y	Y
9						Y	Y
10	CHIP RESISTOR R12	Y	Y			Y	Y
11	ERJ8GEYJ332V		Y			Y	Y
12						Y	Y
13	CHIP RESISTOR R13	Y	Y			Y	Y
14	ERJ8GEYJ101V		Y			Y	Y
15						Y	Y
16	CHIP RESISTOR R14	Y	Y			Y	Y
17	CR1/10W2211DV		Y			Y	Y
18						Y	Y
19	CHIP RESISTOR R15	Y	Y			Y	Y
20	CR1/10W152JV		Y			Y	Y
21						Y	Y
22	CHIP RESISTOR R16	Y	Y			Y	Y
23	CR1/10W331JV		Y			Y	Y
24						Y	Y
25						Y	Y

\*\*\* A : SLIGHT B : PROLONGED

TDK-Lambda

MODEL : KWD15-1212		ABNORMAL TESTING		TEST CONDITIONS		DWG NO: PA775-57-12	
				LOAD = 100%		APPROVED TESTED	
				Vin = 200VAC Ta = 25°C		CCVFO 176:193	
PARTS NAME	PART NO.	TEST MODE		S O F S M B R D F O O N O C T H A E N R D		R N E O T G S O T O D	
		H P I M U E D M S B A E L C V U T A E N R D		O E R O R E D M A E L C V U T A E N R D		O T K E S O T O D	
		O R N E K E E T L H G O W . . T		O R N E K E E T L H G O W . . T		O T K E S O T O D	
		A B T		A B T		A B T	
1	CHIP RESISTOR ERJ8GEYJ100V	R17	Y	Y	Y	Y	Y
2			Y		Y		Y
3							
4	CHIP RESISTOR ERJ8GEYJ390V	R18	Y		Y		Y
5			Y		Y		Y
6							
7	CHIP RESISTOR CR1110W102JV	R19	Y		Y		Y
8			Y		Y		Y
9							
10	CHIP RESISTOR ERJ8GEYJ100V	R20	Y		Y		Y
11			Y		Y		Y
12							
13	CHIP RESISTOR ERJ8GEYJ100V	R21	Y		Y		Y
14			Y		Y		Y
15							
16	CHIP RESISTOR ERJ8GEYJ100V	R22	Y		Y		Y
17			Y		Y		Y
18							
19	CHIP RESISTOR CR1110W183JV	R23	Y		Y		Y
20			Y		Y		Y
21							
22	CHIP RESISTOR ERJ8GEYJ3R9V	R24	Y			Y	Y
23			Y			Y	Y
24							
25							

\*\*\* A: SUGHT B: PROLONGED

TDK-Lambda

MODEL: KWD15-1212		ABNORMAL TESTING										TEST CONDITIONS		DWG NO: PA775 - 57-13	
												LOAD = 100%		APPROVED TESTED	
												Vin = 200VAC		CCNEO 17.3/9.3	
PARTS NAME	PART NO.	TEST MODE	S	O	F	S	B	S	R	D	F	O	O	N	O
1 CHIP RESISTOR ERJ8GEYJ3R9V	R25	Y	H	P	I	M	U	M	D	A	U	O	O	C	T
2 CHIP RESISTOR ERJ8GEYJ3R9V	R26	Y	O	E	R	O	R	D	M	S	B	·	U	H	E
3 CHIP RESISTOR ERJ8GEYJ3R9V	R27	Y	R	N	E	K	K	E	A	E	L	·	T	A	O
4 CHIP RESISTOR ERJ8GEYJ3R9V	R28	Y	E	E	T	E	T	L	G	O	W	·	P	N	G
5 CHIP RESISTOR ERJ8GEYJ100V	R29	Y	A	B				T	O	E	N	·	U	R	S
6 CHIP RESISTOR ERJ8GEYJ100V	R30	Y										·	T	A	D
7 CHIP RESISTOR ERJ8GEYJ100V	R31	Y										·	U	N	
8 CHIP RESISTOR ERJ8GEYJ100V	R32	Y										·	U	G	
9 CHIP RESISTOR ERJ8GEYJ100V	R33	Y										·	U	R	
10 CHIP RESISTOR ERJ8GEYJ100V	R34	Y										·	U	G	
11 CHIP RESISTOR ERJ8GEYJ100V	R35	Y										·	U	R	
12 CHIP RESISTOR ERJ8GEYJ100V	R36	Y										·	U	G	
13 CHIP RESISTOR ERJ8GEYJ100V	R37	Y										·	U	R	
14 CHIP RESISTOR ERJ8GEYJ100V	R38	Y										·	U	G	
15 CHIP RESISTOR ERJ8GEYJ100V	R39	Y										·	U	R	
16 CHIP RESISTOR ERJ8GEYJ100V	R40	Y										·	U	G	
17 CHIP RESISTOR ERJ8GEYJ100V	R41	Y										·	U	R	
18 CHIP RESISTOR ERJ8GEYJ100V	R42	Y										·	U	G	
19 CHIP RESISTOR ERJ8GEYJ100V	R43	Y										·	U	R	
20 CHIP RESISTOR ERJ8GEYJ100V	R44	Y										·	U	G	
21 CHIP RESISTOR ERJ8GEYJ100V	R45	Y										·	U	R	
22 CHIP RESISTOR ERJ8GEYJ100V	R46	Y										·	U	G	
23 CHIP RESISTOR ERJ8GEYJ100V	R47	Y										·	U	R	
24 CHIP RESISTOR ERJ8GEYJ100V	R48	Y										·	U	G	
25 CHIP RESISTOR ERJ8GEYJ100V	R49	Y										·	U	R	

\*\*\* A: SIGHT B: PROLONGED

TDK-Lambda

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MODEL : KWD15-1212		ABNORMAL TESTING												TEST CONDITIONS		DWG NO: PA775-57-14	
														APPROVED TESTED			
														V <sub>in</sub> = 200VAC Ta = 25°C		RCNEO 1574/93	
PARTS NAME	PART NO.	TEST MODE												LOAD = 100%	V <sub>in</sub> = 200VAC Ta = 25°C	APPROVED TESTED	DWG NO: PA775-57-14
1 CHIP RESISTOR ERJ8GEYJ100V	R33	Y												Y	Y	Y	
2 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
3 CHIP RESISTOR ERJ8GEYJ100V	R34		Y											Y	Y	Y	
4 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
5 CHIP RESISTOR ERJ8GEYJ100V														Y	Y	Y	
6 CHIP RESISTOR ERJ8GEYJ100V	R35		Y											Y	Y	Y	
7 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
8 CHIP RESISTOR ERJ8GEYJ100V	R36		Y											Y	Y	Y	
9 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
10 CHIP RESISTOR ERJ8GEYJ100V	R37		Y											Y	Y	Hiccup	
11 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
12 CHIP RESISTOR ERJ8GEYJ100V	R38		Y											Y	Y	Hiccup	
13 CHIP RESISTOR ERJ8GEYJ100V			Y											Y	Y	Y	
14 CHIP RESISTOR ERJ8GEYJ132V	R39		Y											Y	Y	Y	
15 CHIP RESISTOR ERJ8GEYJ132V			Y											Y	Y	Y	
16 CHIP RESISTOR ERJ8GEYJ132V	R40		Y											Y	Y	Y	
17 CHIP RESISTOR ERJ8GEYJ132V			Y											Y	Y	Y	
18 CHIP RESISTOR ERJ8GEYJ132V	R41		Y											Y	Y	Y	
19 CHIP RESISTOR ERJ8GEYJ132V	R42		Y											Y	Y	Y	
20 CHIP RESISTOR ERJ8GEYJ132V	R43		Y											Y	Y	Y	
21 CHIP RESISTOR ERJ8GEYJ132V			Y											Y	Y	Y	
22 CHIP RESISTOR ERJ8GEYJ132V	R44		Y											Y	Y	Y	
23 CHIP RESISTOR ERJ8GEYJ132V	R45		Y											Y	Y	Y	
24 CHIP RESISTOR ERJ8GEYJ132V	R46		Y											Y	Y	Y	
25 CHIP RESISTOR ERJ8GEYJ132V	R47		Y											Y	Y	Y	

\*\*\* A : SLIGHT B : PROLONGED

TDK-Lambda

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\*\*\* A: SIGHT B: PROLONGED

TDK-Lambda

\*\*\* A : SUGHT B : PROLONGED

MODEL: KWD15-1212		ABNORMAL TESTING		TEST CONDITIONS		DWG NO: PA775-57-16	
				LOAD = 100%		APPROVED TESTED	
				Vin = 200VAC		CC/VEO 157.3/9.3	
PARTS NAME	PART NO.	TEST MODE					
		S	O	F	S	R	D
		H	P	I	M	B	N
		O	E	R	M	S	O
		R	N	E	R	B	E
		T	T	E	K	A	G
1	BALUN COIL	L1	1-2	Y			
2	UF1717H-702Y0R3-01		3-4	Y			
3			1	Y			
4			2	Y			
5			3	Y			
6			4	Y			
7	CHIP COIL	L4					
8			Y	Y			
9							
10	CHIP COIL	L5		Y			
11							
12							
13							
14	CHIP COIL	L6		Y			
15							
16							
17	CHIP COIL	L7		Y			
18							
19							
20							
21							
22							
23							
24							
25							

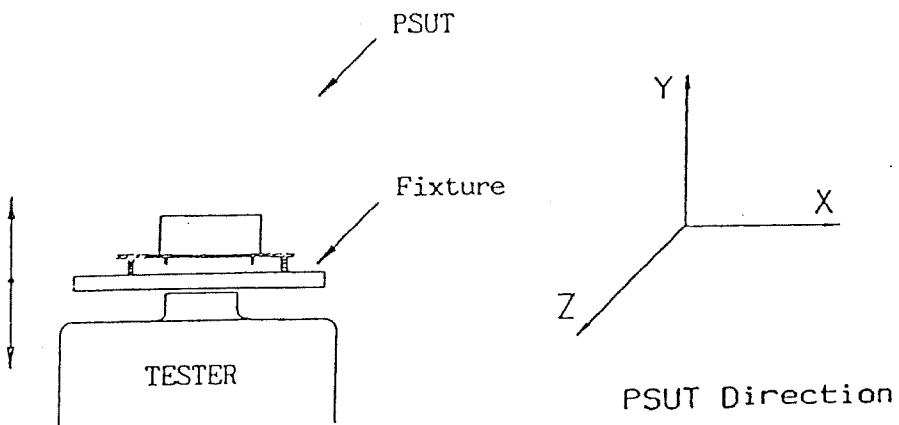
VIBRATION TEST

## TYPES OF VIBRATION TEST :

- A) OSCILLATOR FREQUENCY SWEEP
- B) RESONANCE FREQUENCY

EQUIPMENT : EMIC CORPORATION VIBRATION TEST SYSTEM F-400-BM-E47  
VIBRATION GENERATOR 905-FN

## PROCEDURE :



## VIBRATION TEST WITH FREQUENCY SWEEP

FREQUENCY	10 ~ 55 Hz.
SWEEP TIME	1 min.
ACCELERATION	MAX 10G.
AMPLITUDE	1.65mmPP CONSTANT.
DIRECTION	X, Y, Z.
DURATION	1 hr. for each direction.

## TEST POINT :

1. Output voltage (Apply some shock when checking the o/p voltage, and observe any abnormalities.)
2. Ripple voltage (At AC100V input and output)
3. Mechanical Condition (No breakage)

認 APPD	<i>S. P. Hsu</i> SINAK/93	設 計 ENGR	<i>SLK/kw</i> 27 - Nov - 92	図面番号 D WG-No.
検 図 C H K	CCNEO 30. DEC. 92	製 図 D W G	WILLIAM PHIA 25 - Nov - 92	PA775-64-01

TEST RESULTS :  
(after vibration)

TEST POINT	OUTPUT VOLTAGE (V)			RIPPLE VOLTAGE (mV)			MECHANICAL CONDITION	NOTE
	CH1	CH2	CH3	CH1	CH2	CH3		
BEFORE TEST	11.853	11.855	—	50	50	—	O.K.	
DIRECT <sup>n</sup> TEST								
X	11.865	11.860	—	50	50	—	O.K.	
Y	11.854	11.861	—	50	50	—	O.K.	
Z	11.862	11.855	—	50	50	—	O.K.	

EVALUATION RESULT :

PASS

/ FAIL

VISUAL INSPECTION RESULT :

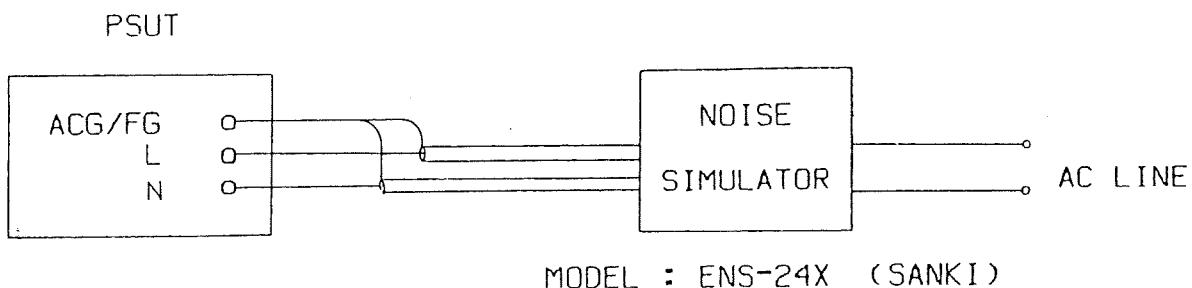
PASS

/ FAIL

認 APPD	- -	設 計 ENGR	- -	図面番号 DWG-No.	
検 図 CHK	- -	製 図 DWG	- -		

NOISE SIMULATION TEST

Circuit for measurement and equipment used :



## Testing Conditions :

Input Voltage : AC100V

Output Voltage : Rated

Output Current : 0% , 100%

Ambient Temp. : 25 °C

## Settings :

MODE ..... Normal , Common

TRIG SELECT .... Line or Ext (Line)

PULSE WIDTH .... 50, 200, 800, 1000ns

PHASE SHIFT .... 0 ~ 360 Degree

POLARITY ..... + , -

NOISE LEVEL .... 0 ~ 2KV

## Acceptance Criteria :

1) No damage of PSUT

2) No output failure  
(eg. Over/Undershoot ≤ 3% of Vo)

3) Check any abnormalities (eg. noise)

## Evaluation Result :

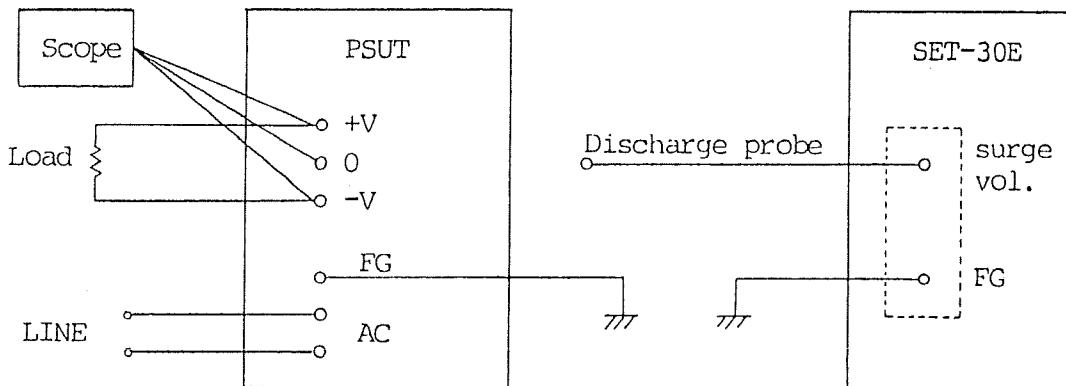
PASS

/ FAIL

認 APPD	<i>[Signature]</i> 15 MAR '93	設 計 ENGR	<i>[Signature]</i> 12. 1. 93	図面番号 DWG-No.	PA775-61-01 - <input type="checkbox"/>
検 図 CHK	CCNEO 12. 01. 93	製 図 DWG	Ramach. m 12. 1. 93		

ELECTROSTATIC DISCHARGE TEST

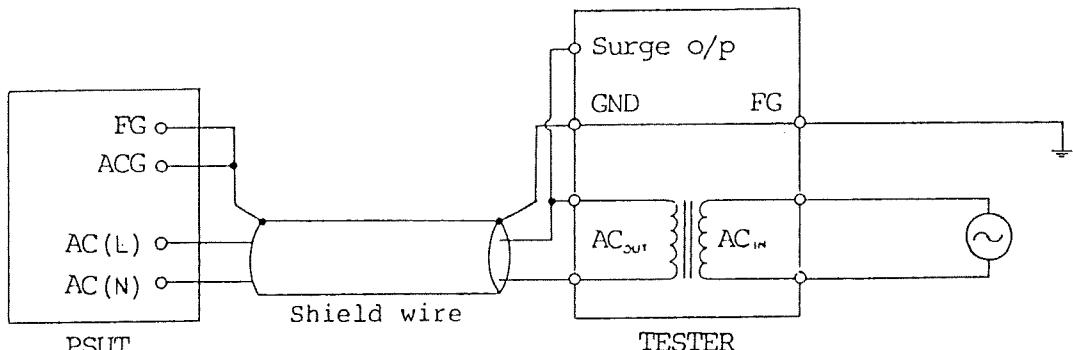
EQUIPMENT : SET-30E (SANKI ELECTRONIC)

Discharge Resistor : 250 ohm  
Capacitor unit : 200 pFCONDITIONS : Ambient Temperature : 25°C  
Input Voltage : AC100V  
Output Voltage : Rated  
Output Current : Rated  
Applied Voltage : ±3kV, ±5kV, ±10kV, ±15kVPROCEDURE :  
The PSUT should be in a good working condition.  
Discharge the applied voltage to the touchable parts of the PSUT (Chassis, Input Terminal, Output Terminal, FG Terminal, ACG Terminal) and check any abnormalities.  
  
Each point to be tested 3 times with different polarity. Voltage should be applied from 3kV to 15kV.ACCEPTANCE CRITERIA :  
 1. No damage of PSUT  
 2. No output failure ( $\Delta V_o < 3\% \text{ of } V_o$ )  
 3. No abnormalitiesEVALUATION RESULT : PASS / ~~FAIL~~

認 APPD	<i>S. Johnson</i> 15 MAR '93	設計 ENGR	<i>P. Goh</i> 12. 1. 93	図面番号 DWG-No.
検 圖 CHK	<i>KCNEO</i> 12. 01. 93	製 圖 DWG	<i>Ramach. M</i> 12. 1. 93	PA775-62-01 - <span style="border: 1px solid black; border-radius: 5px; padding: 2px;"> </span>

LIGHTNING SURGE TEST

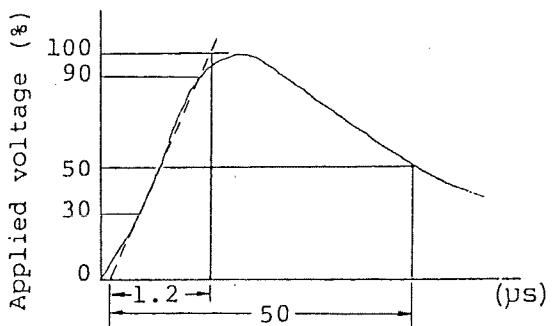
## TEST CIRCUIT, TEST EQUIPMENT



MODEL : LSG - 12K - E ( SANKI )

CONDITIONS :	Input Voltage	: AC100V
	Output Voltage	: Rated
	Output Current	: Rated
	Applied Voltage	: From 3kV in steps of 0.5kV Check the max. withstand voltage
	Applied Point	: Between FG - AC
	Number of Test	: Each voltage 3 times
	Polarity	: + , -
	Ambient Temp.	: 25°C

## APPLIED VOLTAGE WAVEFORM :



- ACCEPTANCE CRITERIA :
1. No damage to the PSUT
  2. No output failure
  3. No abnormalities

EVALUATION RESULT :

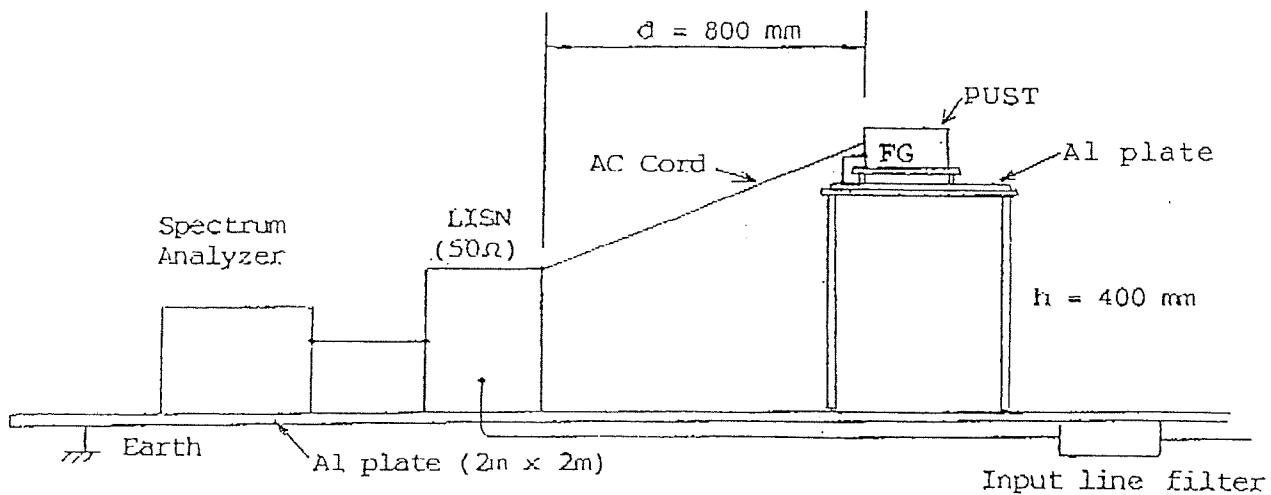
PASS  
5KV

/ FAIL

認 APPD	<i>S. Mar. 93</i>	設 計 ENGR	<i>J. S.</i> 10. 1. 93	図面番号 Dwg-No.
検 図 C H K	<i>OKNEO</i> 10. 01. 93	製 図 DWG	<i>R. Ramach. M.</i> 10. 1. 93	PA775-74-01 - <input type="checkbox"/>

EMI TEST

## TEST CIRCUIT :



## TEST EQUIPMENTS :

SPECTRUM ANALYZER	8568B	HEWLETT PACKARD
QUASI-PEAK ADAPTER	85650A	HEWLETT PACKARD
RF PRESELECTOR	85685A	HEWLETT PACKARD
LISN	3825/2	EMCO

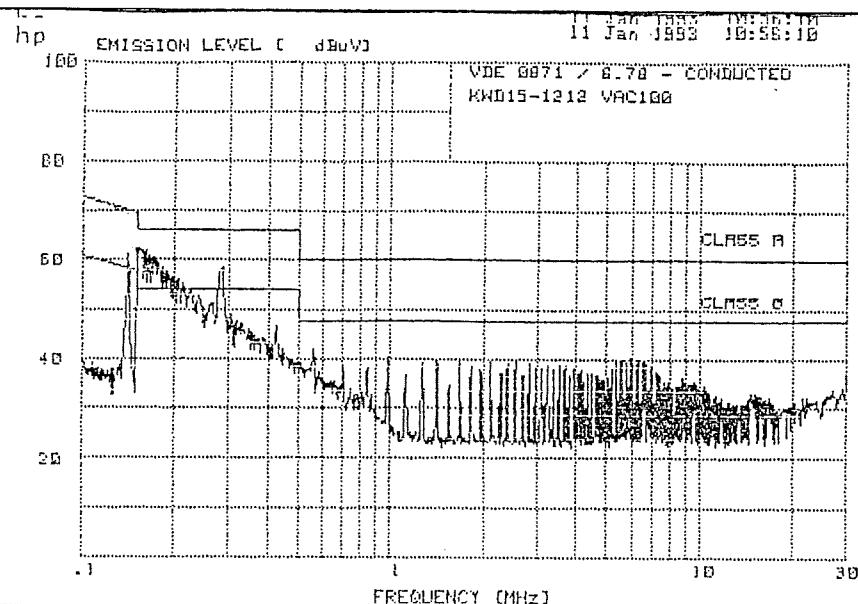
## CONDITIONS :

INPUT VOLTAGE	: AC100V, AC200V
OUTPUT VOLTAGE	: RATED
OUTPUT CURRENT	: RATED
AMBIENT TEMP	: 25°C

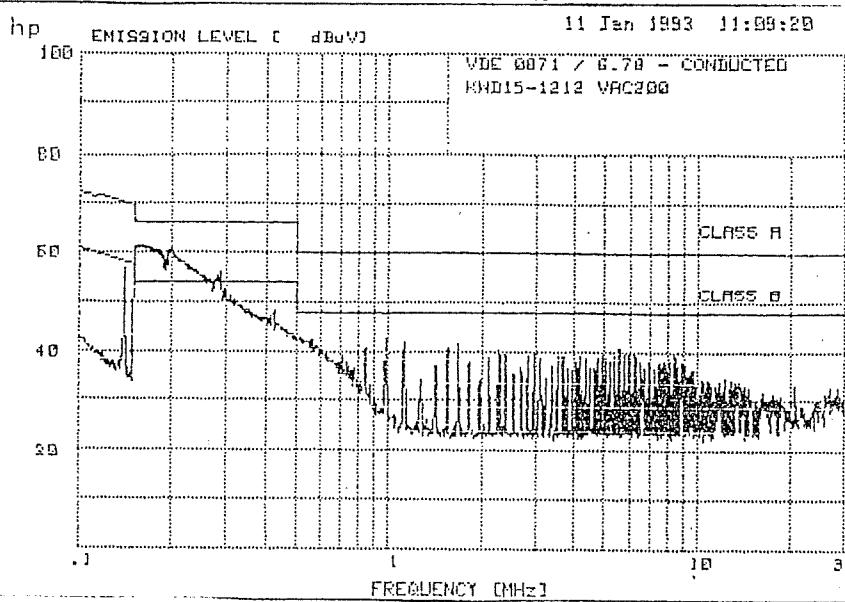
記 号 APPD		設 計 ENGR		図面番号 DWG-No.
検 査 C H K		製 図 DWG		

KWD15 -1212

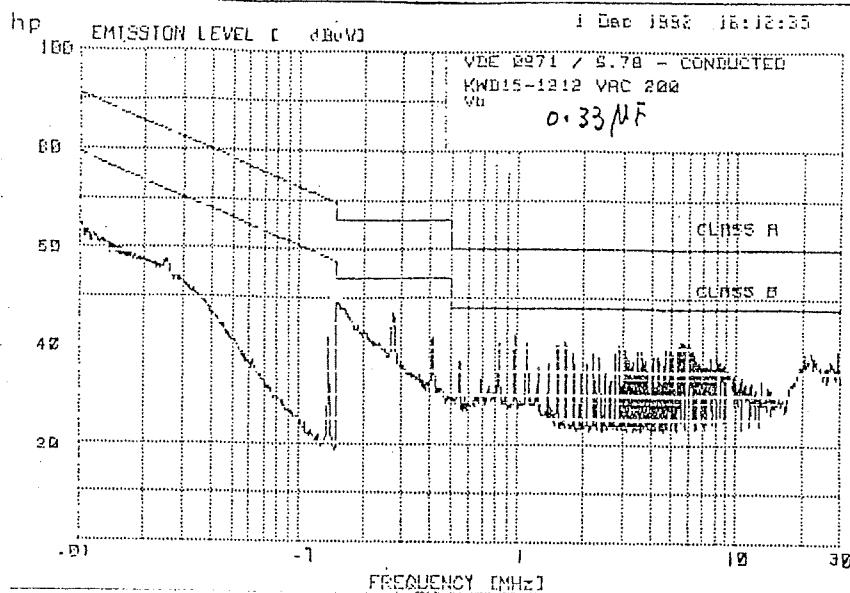
DATE 15 · MAR · 1993 ·



Vin = 100Vac



Vin = 200Vac



Vin = 200Vac

V D E

with external cap.  
0.33μF between  
AC(L) and AC(N)

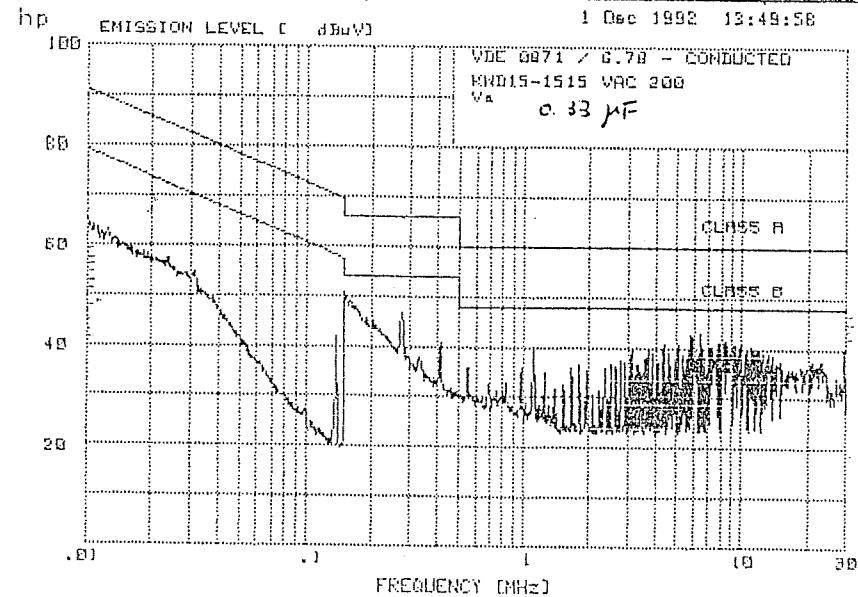
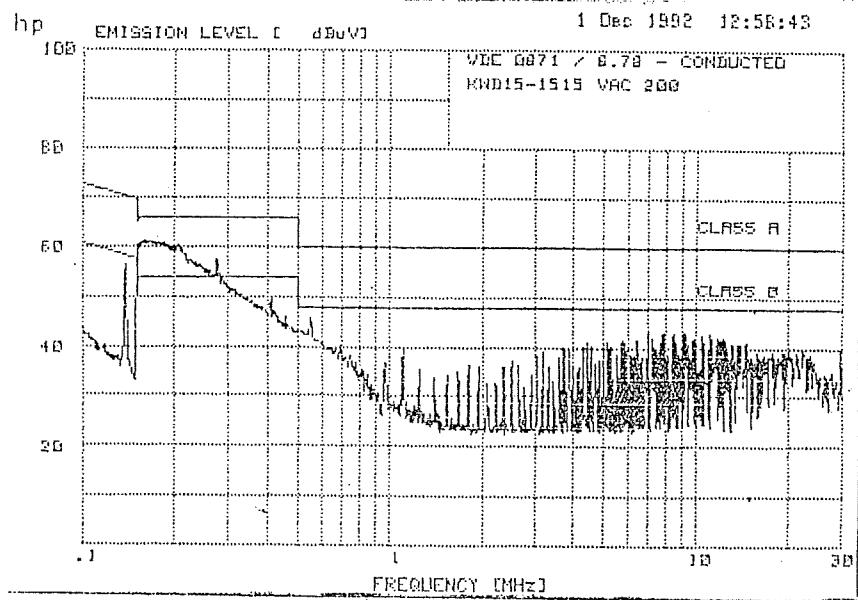
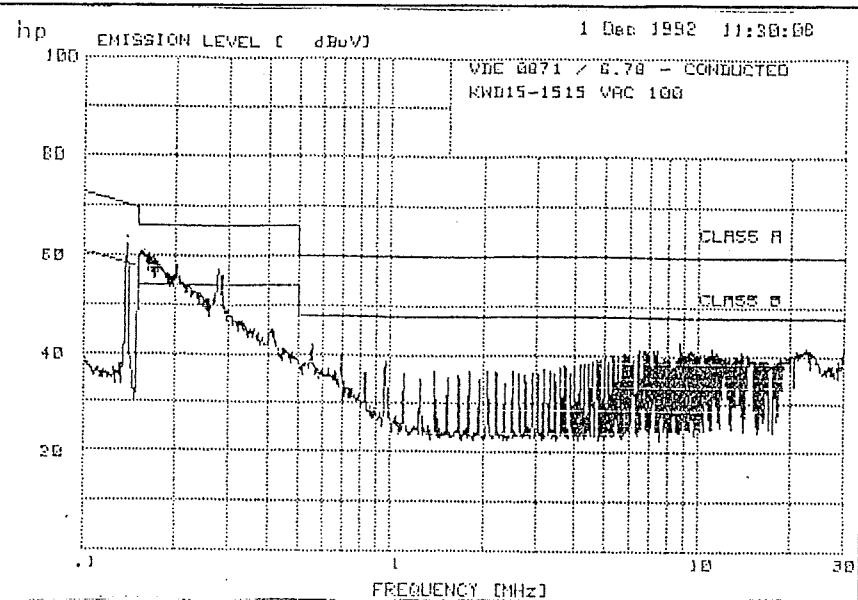
製 APPD		設計 ENGR		図面番号 DWG - No.
.	.	.	.	PA775-70-02-
検 CHK	.	製 DWG	.	

KWD 15-1515

LOAD 100%

DATE

15 · MAR · 1993 ·

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認 APPD	.	設計 ENGR	.	図面番号 DWG-No.
.	.	.	.	PA775-70-03-
検 査 C H K	.	製 図 DWG	.	
.	.	.	.	