

PSD1R5- *-1212

EVALUATION DATA

型式データ

DWG.No. C199-53-01		
承認	査閲	担当
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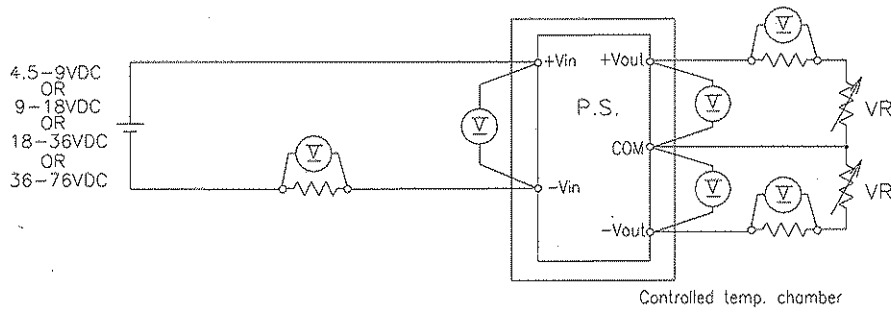
使用記号 Terminology used

	Definition	
Vin	入力電圧 Input Voltage
Vout	出力電圧 Output Voltage
Iin	入力電流 Input Current
Iout	出力電流 Output Current
Ta	周囲温度 Ambient Temperature

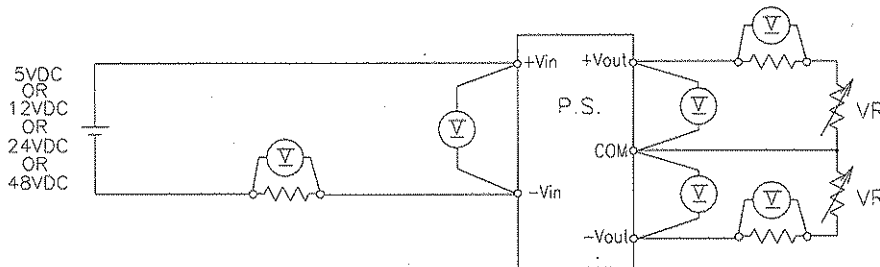
1. 測定方法 Evaluation Method

1.1 測定回路 Circuits used for determination

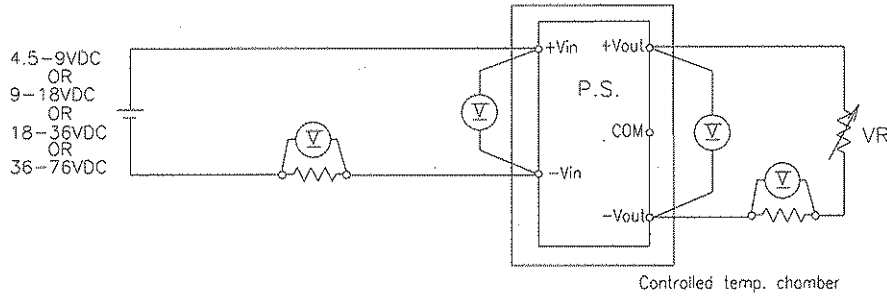
(1) 静特性 Steady state data



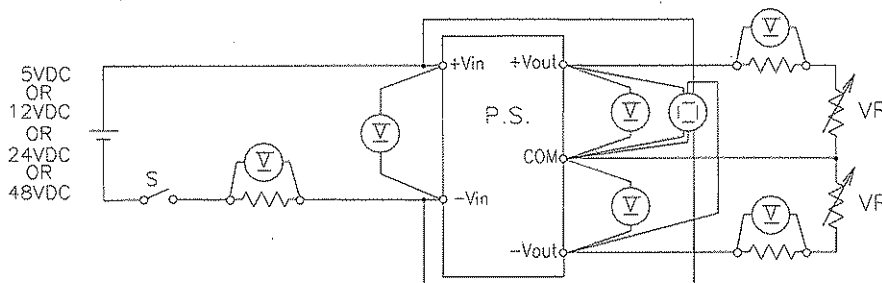
(2) 通電ドリフト特性 Warm up voltage drift characteristics



(3) 過電流保護特性 Over current protection (OCP) characteristics



(4) 出力立ち上がり特性 Output rise characteristics

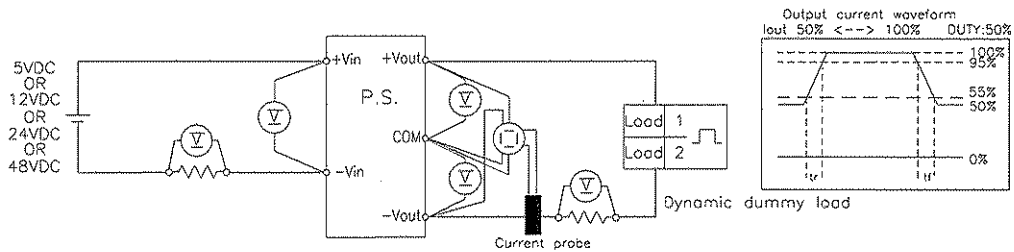


(5) 出力立ち下がり特性 Output fall characteristics

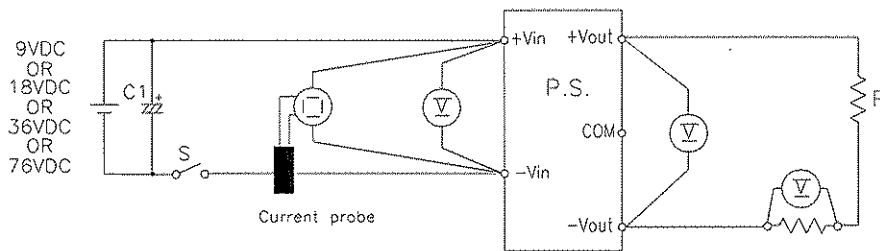
出力立ち上がり特性と同じ

Same as output rise characteristics

(6) 過渡応答(負荷急変)特性 Dynamic load response characteristics

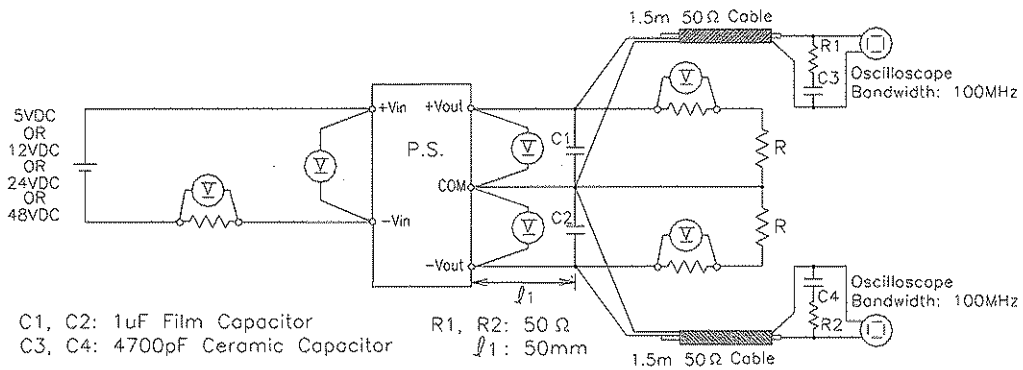


(7) 入力サージ電流(突入電流)特性 Inrush current characteristics

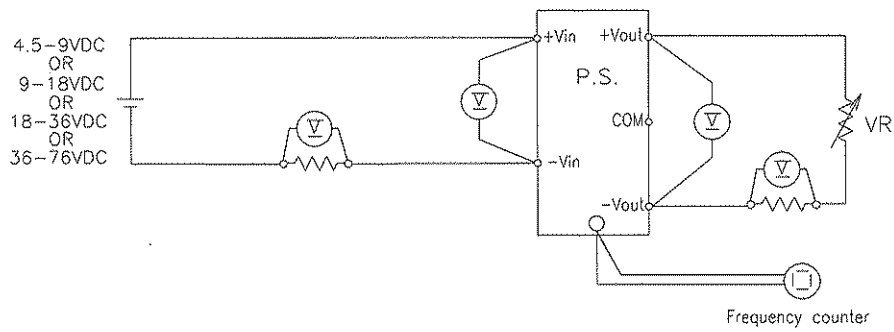


C1: 4000uF Electrolytic Capacitor

(8) 出力リップル、ノイズ波形 Output ripple and noise waveform

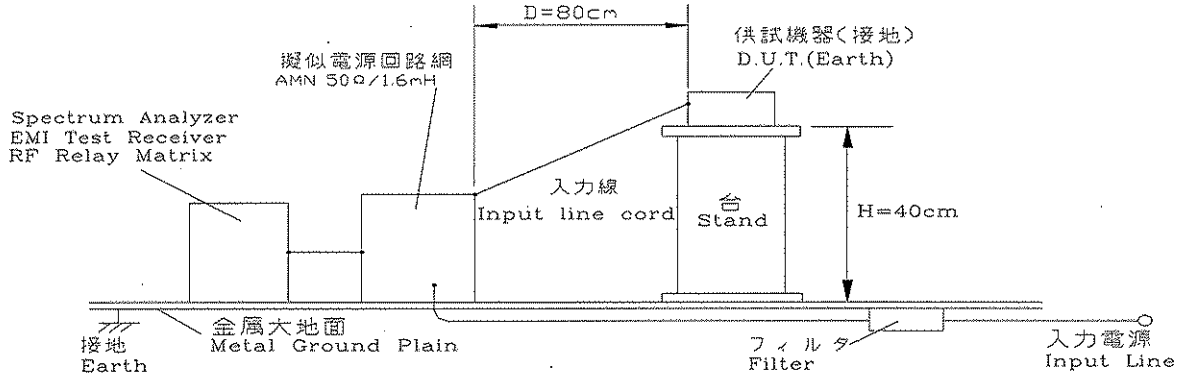


(9) スイッチング周波数対出力電力 Switching frequency v.s. output power

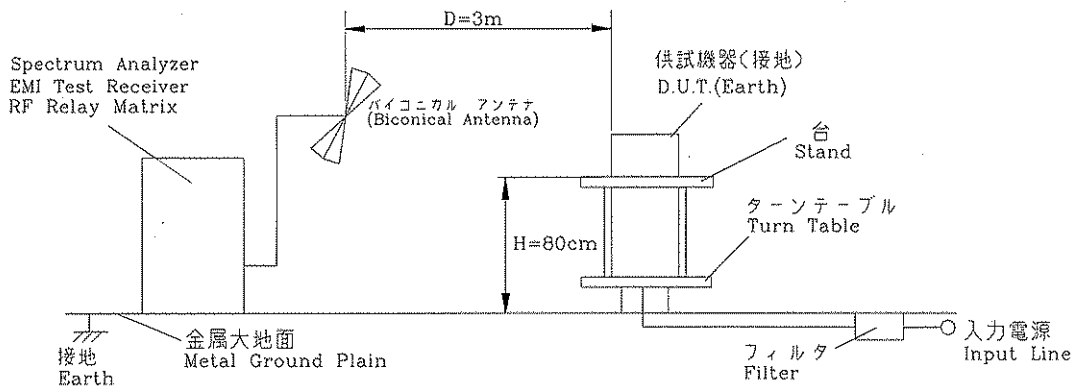


(10) EMI 特性 Electro-Magnetic Interference characteristics

(a) 雑音端子電圧 (帰還ノイズ) Conducted Emission Noise

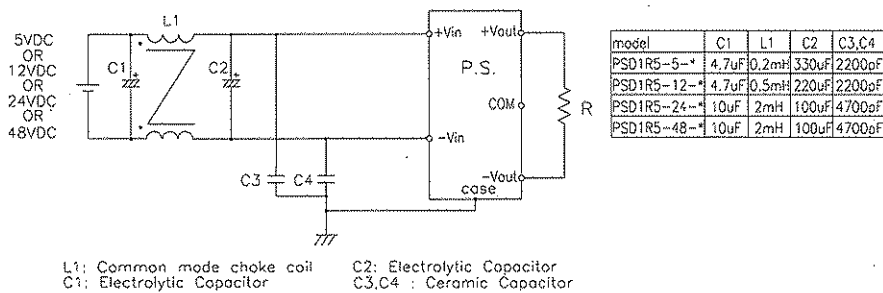


(b) 雑音電界強度 (輻射ノイズ) Radiated Emission Noise



(1) VCCI class A 対応アプリケーションシステム

VCCI class A application system



1.2 使用測定機器 List of equipment used

	EQUIPMENT USED	MANUFACTURER	MODEL NO.
1	OSCILLO SCOPE	HITACHI DENSHI	V-1100A
2	DIGITAL STORAGE OSCILLOSCOPE	YOKOGAWA ELECT. IWATSU-LeCroy	DL1740 LT364L
3	DIGITAL MULTIMETER	AGILENT	34970A
4	CURRENT PROBE/AMPLIFIER	TEKTRONIX	A6303/TM503B
5	SHUNT RESISTER	YOKOGAWA ELECT.	2215
6	DYNAMIC DUMMY LOAD	TAKASAGO	FK-200L
7	INPUT POWER SUPPLY	DENSEI-LAMBDA	GEN100-7.5
8	CONTROLLED TEMP. CHAMBER	TABAI ESPEC	SU-261
9	SPECTRUM ANALYZER	ROHDE & SCHWARZ	FSA
10	EMI TEST RECEIVER	ROHDE & SCHWARZ	ESHS10
11	EMI TEST RECEIVER	ROHDE & SCHWARZ	ESVS10
12	RF RELAY MATRIX	ROHDE & SCHWARZ	PSU
13	AMN	KYORITU DENSHI	KNW-242
14	ANTENNA(BICONICAL ANTENNA)	SCHWARZBECK	BBA9106

2. 特性データ Characteristics

2.1 静特性 Steady state data

(1) 入力・負荷・温度変動 Regulation - line and load, temperature drift

PSD1R5-5-1212

12V (CH1)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	4.5VDC	5VDC	9VDC	line regulation	
0%	12.127V	12.130V	12.130V	3.0mV	0.025%
50%	12.133V	12.133V	12.133V	0.0mV	0.000%
100%	12.136V	12.136V	12.135V	1.0mV	0.008%
load	9.0mV	6.0mV	5.0mV		
regulation	0.07%	0.05%	0.04%		

2. Temperature drift Conditions Vin : 5VDC

Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	12.084V	12.136V	12.145V	61.0mV	0.50%

-12V (CH2)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	4.5VDC	5VDC	9VDC	line regulation	
0%	-12.145V	-12.146V	-12.146V	1.0mV	0.008%
50%	-12.142V	-12.143V	-12.144V	2.0mV	0.016%
100%	-12.140V	-12.141V	-12.142V	2.0mV	0.016%
load	5.0mV	5.0mV	4.0mV		
regulation	0.04%	0.04%	0.03%		

2. Temperature drift Conditions Vin : 5VDC

Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	-12.087V	-12.141V	-12.150V	63.0mV	0.52%

2.1 静特性 Steady state data

(1) 入力・負荷・温度変動 Regulation - line and load, temperature drift

PSD1R5-12-1212

12V (CH1)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	9VDC	12VDC	18VDC	line regulation	
0%	12.227V	12.224V	12.225V	2.0mV	0.016%
50%	12.223V	12.223V	12.224V	1.0mV	0.008%
100%	12.221V	12.221V	12.222V	1.0mV	0.008%
load	6.0mV	3.0mV	3.0mV		
regulation	0.05%	0.02%	0.02%		

2. Temperature drift Conditions Vin : 12VDC

Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	12.134V	12.221V	12.262V	128.0mV	1.05%

-12V (CH2)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	9VDC	12VDC	18VDC	line regulation	
0%	-12.203V	-12.202V	-12.202V	1.0mV	0.008%
50%	-12.207V	-12.206V	-12.205V	2.0mV	0.016%
100%	-12.210V	-12.209V	-12.208V	2.0mV	0.016%
load	7.0mV	7.0mV	6.0mV		
regulation	0.06%	0.06%	0.05%		

2. Temperature drift Conditions Vin : 12VDC

Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	-12.124V	-12.209V	-12.248V	124.0mV	1.02%

2.1 静特性 Steady state data

(1) 入力・負荷・温度変動 Regulation - line and load, temperature drift

PSD1R5-24-1212

12V (CH1)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	18VDC	24VDC	36VDC	line regulation	
0%	12.102V	12.103V	12.104V	2.0mV	0.017%
50%	12.100V	12.100V	12.101V	1.0mV	0.008%
100%	12.098V	12.099V	12.098V	1.0mV	0.008%
load	4.0mV	4.0mV	6.0mV		
regulation	0.03%	0.03%	0.05%		

2. Temperature drift Conditions Vin : 24VDC
Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	12.054V	12.099V	12.089V	45.0mV	0.37%

-12V (CH2)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	18VDC	24VDC	36VDC	line regulation	
0%	-12.093V	-12.093V	-12.095V	2.0mV	0.017%
50%	-12.095V	-12.094V	-12.094V	1.0mV	0.008%
100%	-12.095V	-12.095V	-12.093V	2.0mV	0.017%
load	2.0mV	2.0mV	2.0mV		
regulation	0.02%	0.02%	0.02%		

2. Temperature drift Conditions Vin : 24VDC
Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	-12.050V	-12.095V	-12.086V	45.0mV	0.37%

2.1 静特性 Steady state data

(1) 入力・負荷・温度変動 Regulation - line and load, temperature drift

PSD1R5-48-1212

12V (CH1)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	36VDC	48VDC	76VDC	line regulation	
0%	12.091V	12.091V	12.092V	1.0mV	0.008%
50%	12.090V	12.090V	12.090V	0.0mV	0.000%
100%	12.088V	12.089V	12.090V	2.0mV	0.017%
load	3.0mV	2.0mV	2.0mV		
regulation	0.02%	0.02%	0.02%		

2. Temperature drift Conditions Vin : 48VDC
Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	12.027V	12.089V	12.103V	76.0mV	0.63%

-12V (CH2)

1. Regulation - line and load Condition Ta : 25°C

Iout \ Vin	36VDC	48VDC	76VDC	line regulation	
0%	-12.086V	-12.086V	-12.087V	1.0mV	0.008%
50%	-12.086V	-12.085V	-12.086V	1.0mV	0.008%
100%	-12.086V	-12.085V	-12.085V	1.0mV	0.008%
load	0.0mV	1.0mV	2.0mV		
regulation	0.00%	0.01%	0.02%		

2. Temperature drift Conditions Vin : 48VDC
Iout : 100%

Ta	-40°C	25°C	85°C	temperature stability	
Vout	-12.024V	-12.085V	-12.100V	76.0mV	0.63%

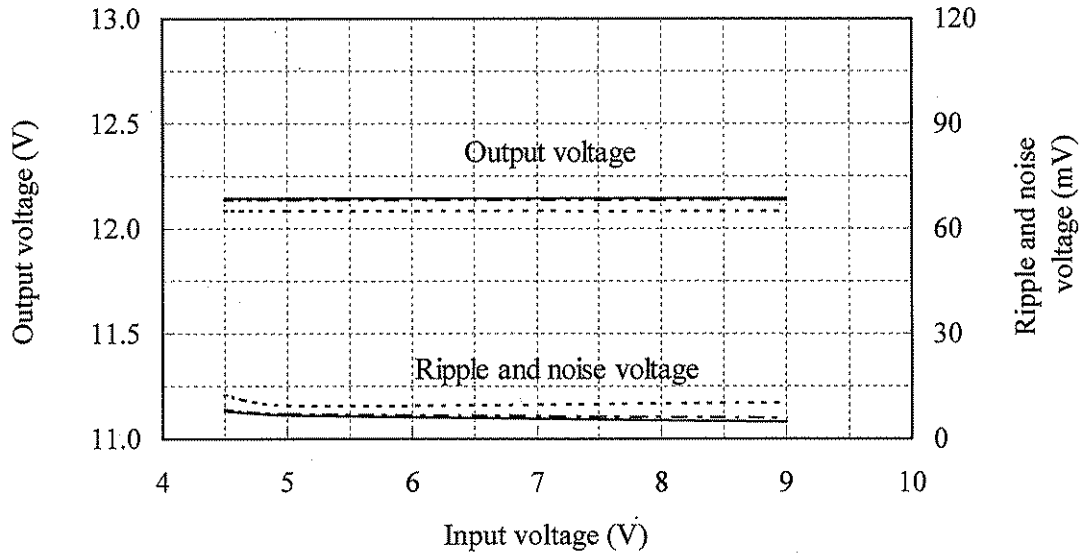
2.1 (2) 出力電圧・リップル電圧対入力電圧
Output voltage and ripple voltage v.s. input voltage

PSD1R5-5-1212

Conditions Iout : 100 %

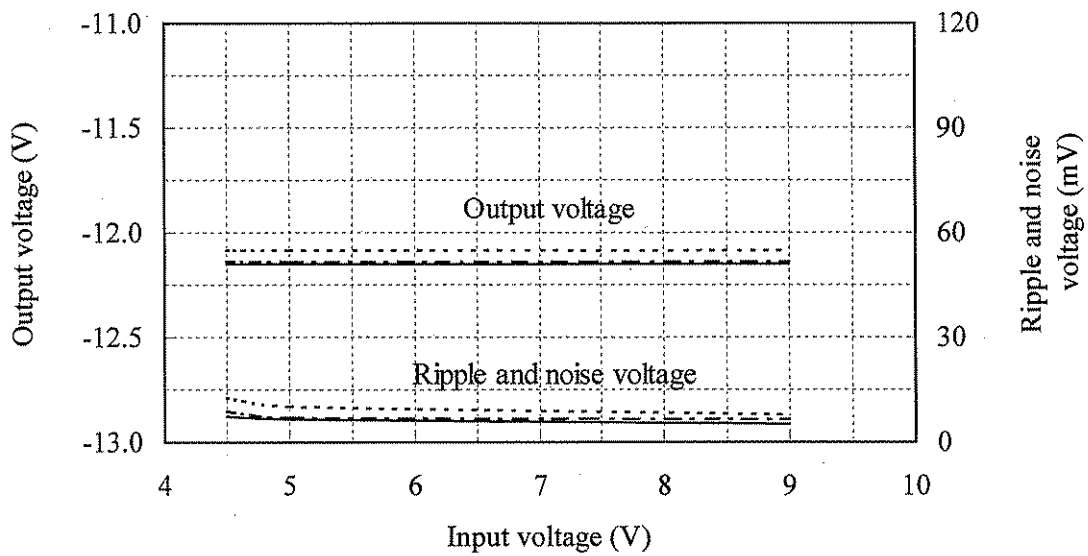
Ta : -40 °C -----
25 °C - - - - -
85 °C _____

12V (CH1)



-12V (CH2)

Ta : -40 °C -----
25 °C - - - - -
85 °C _____



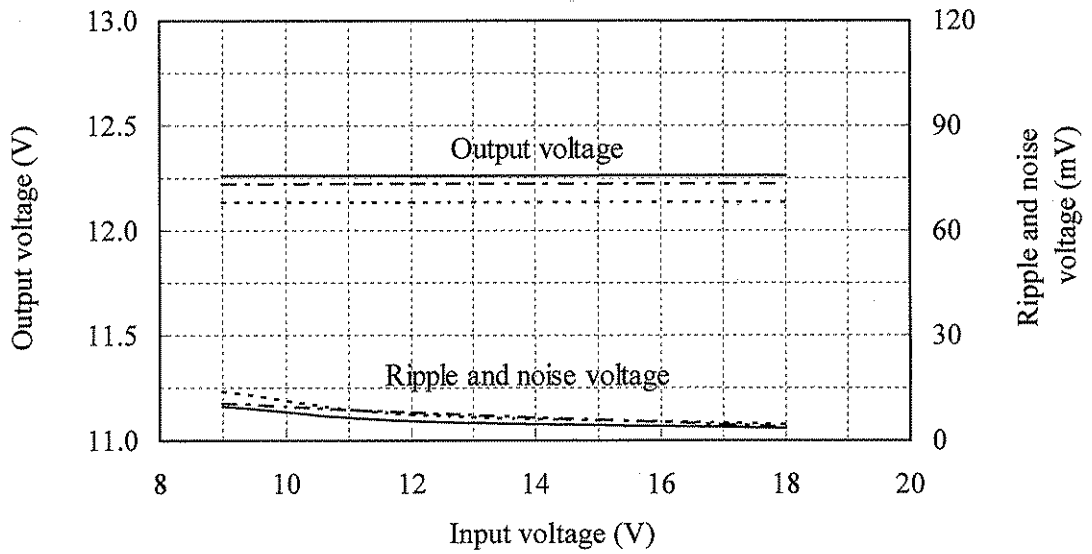
2.1 (2) 出力電圧・リップル電圧対入力電圧
Output voltage and ripple voltage v.s. input voltage

PSD1R5-12-1212

Conditions Iout : 100 %

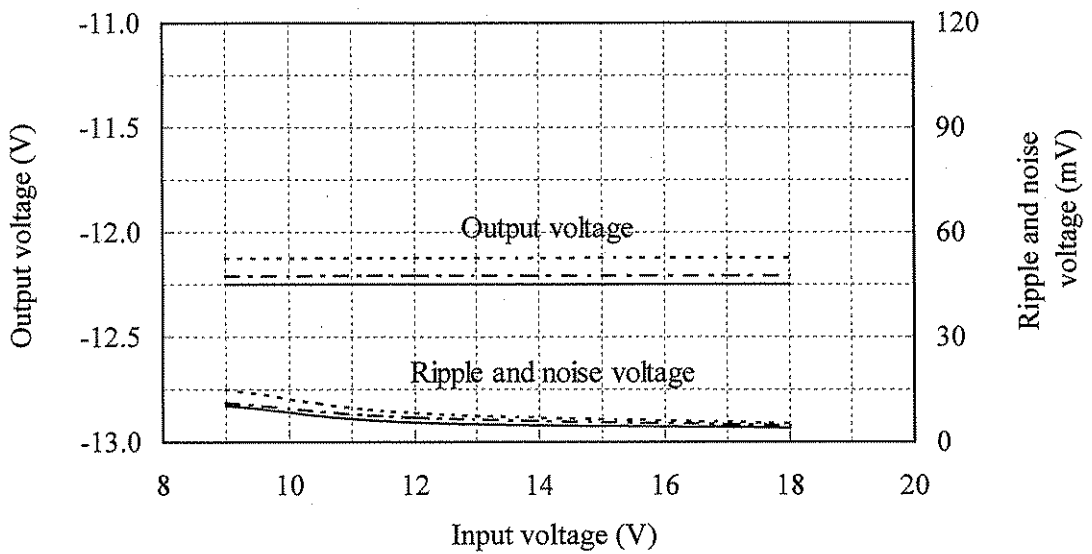
Ta : -40 °C -----
25 °C - - - - -
85 °C _____

12V (CH1)



-12V (CH2)

Ta : -40 °C -----
25 °C - - - - -
85 °C _____



2.1 (2) 出力電圧・リップル電圧対入力電圧
Output voltage and ripple voltage v.s. input voltage

PSD1R5-24-1212

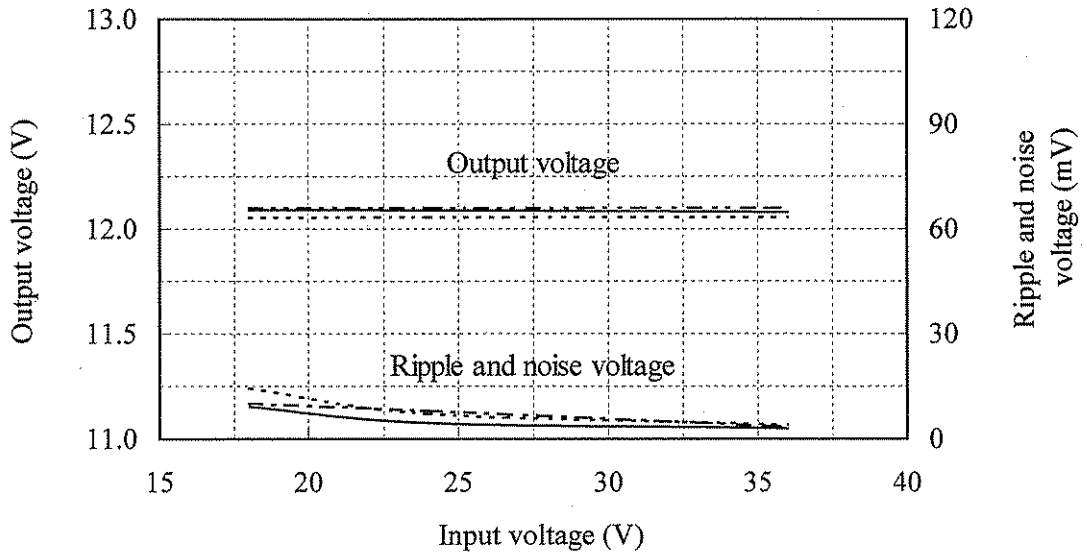
Conditions Iout : 100 %

Ta : -40 °C -----

25 °C - - - - -

85 °C _____

12V (CH1)

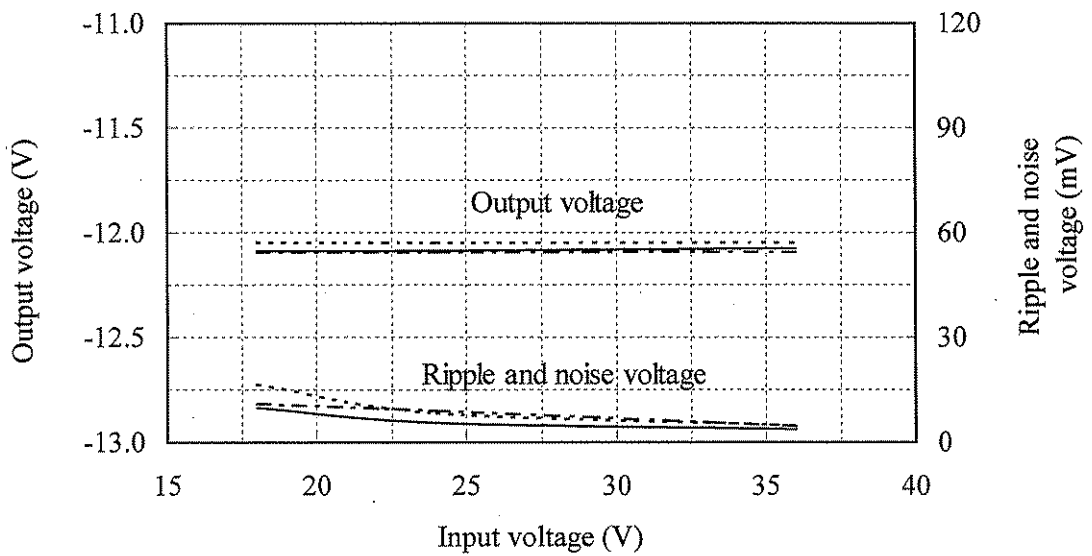


-12V (CH2)

Ta : -40 °C -----

25 °C - - - - -

85 °C _____



2.1 (2) 出力電圧・リップル電圧対入力電圧
Output voltage and ripple voltage v.s. input voltage

PSD1R5-48-1212

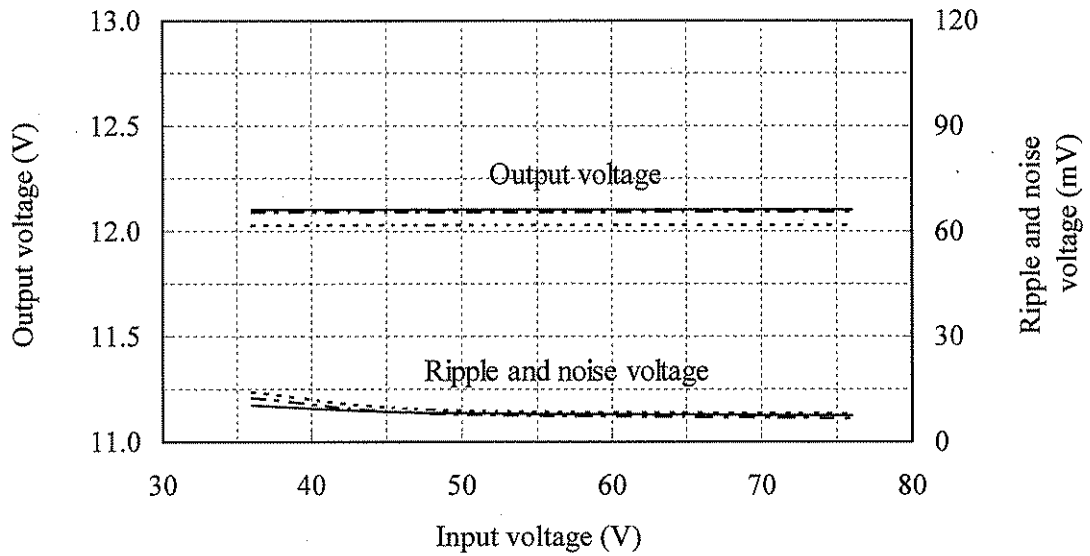
Conditions Iout : 100 %

Ta : -40 °C -----

25 °C - - - - -

85 °C _____

12V (CH1)

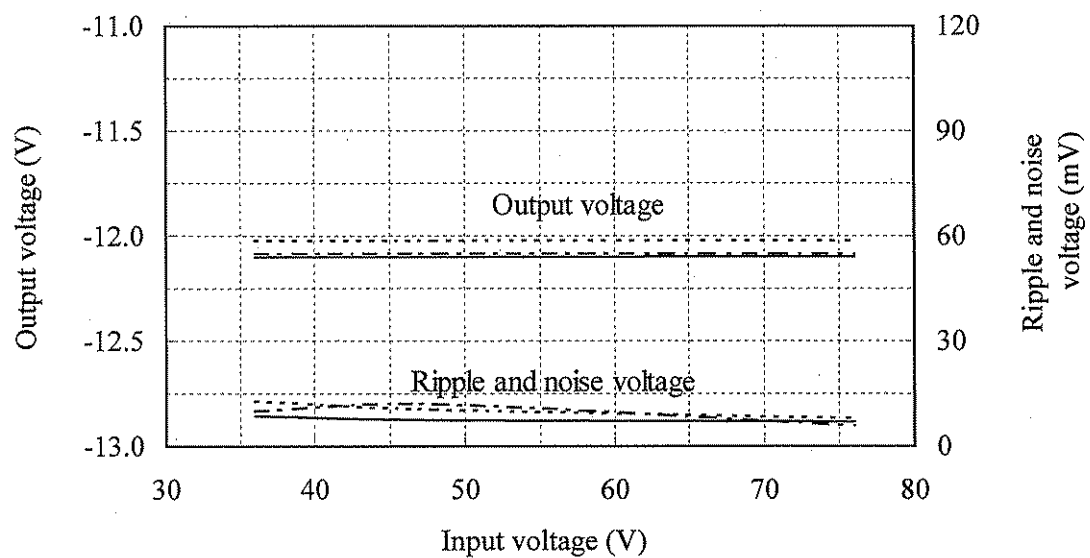


-12V (CH2)

Ta : -40 °C -----

25 °C - - - - -

85 °C _____

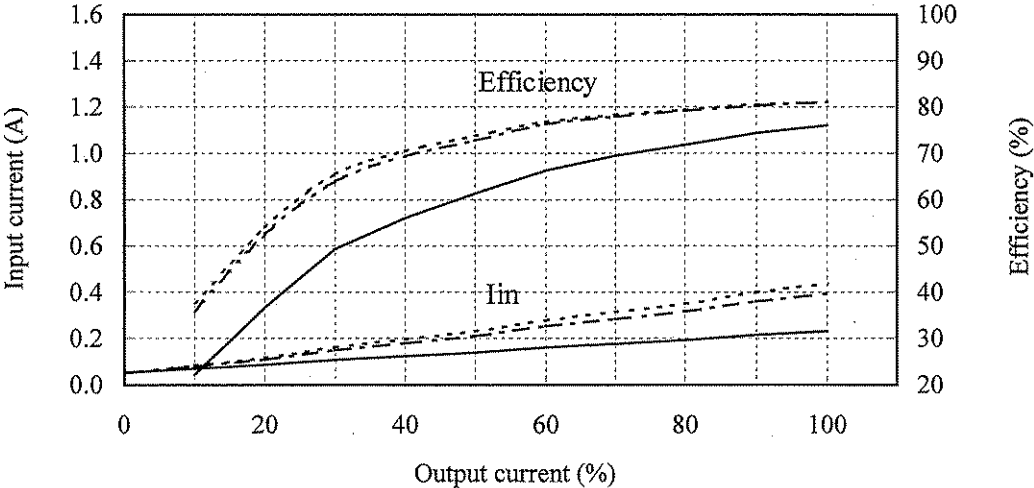


2.1 (3) 効率・入力電流対出力電流

Efficiency and input current v.s. output current

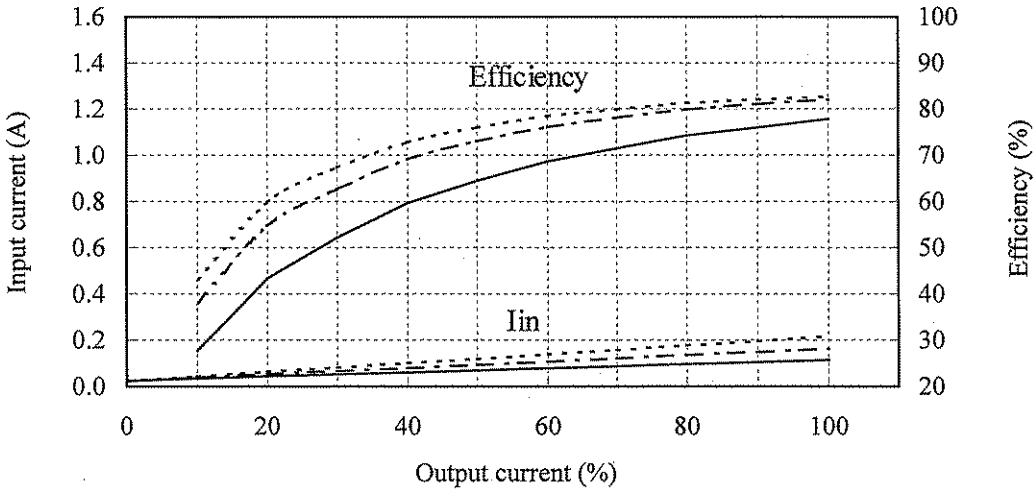
Conditions V_{in} : 4.5 VDC -----
 : 5 VDC - - - - -
 : 9 VDC ————
 T_a : 25 °C

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Conditions V_{in} : 9 VDC -----
 : 12 VDC - - - - -
 : 18 VDC ————
 T_a : 25 °C

PSD1R5-12-1212

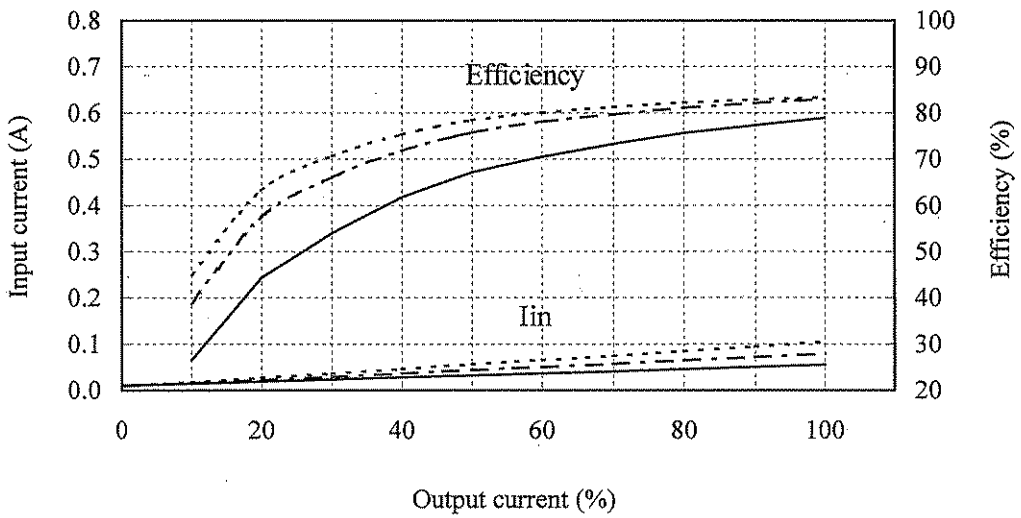


2.1 (3) 効率・入力電流対出力電流

Efficiency and input current v.s. output current

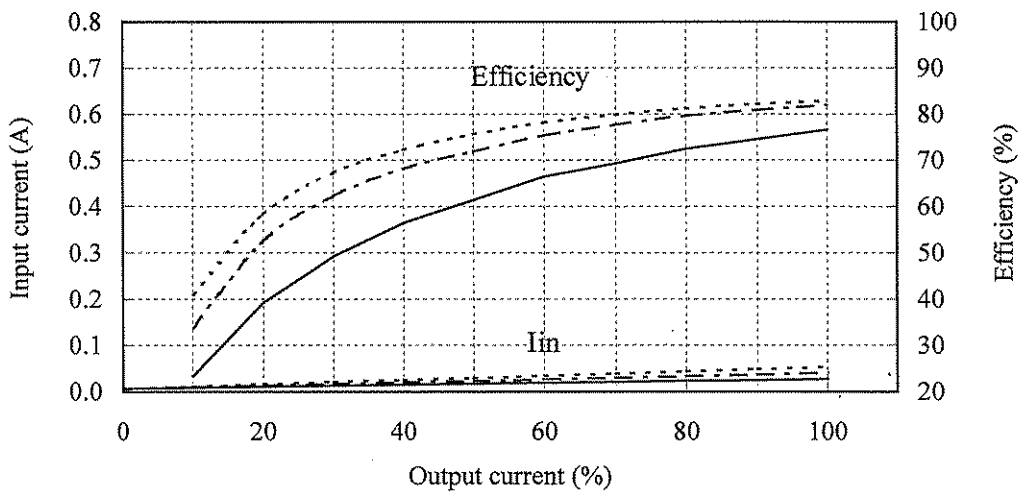
Conditions V_{in} : 18 VDC -----
 : 24 VDC - - - - -
 : 36 VDC ————
 T_a : 25 °C

PSD1R5-24-1212



Conditions V_{in} : 36 VDC -----
 : 48 VDC - - - - -
 : 76 VDC ————
 T_a : 25 °C

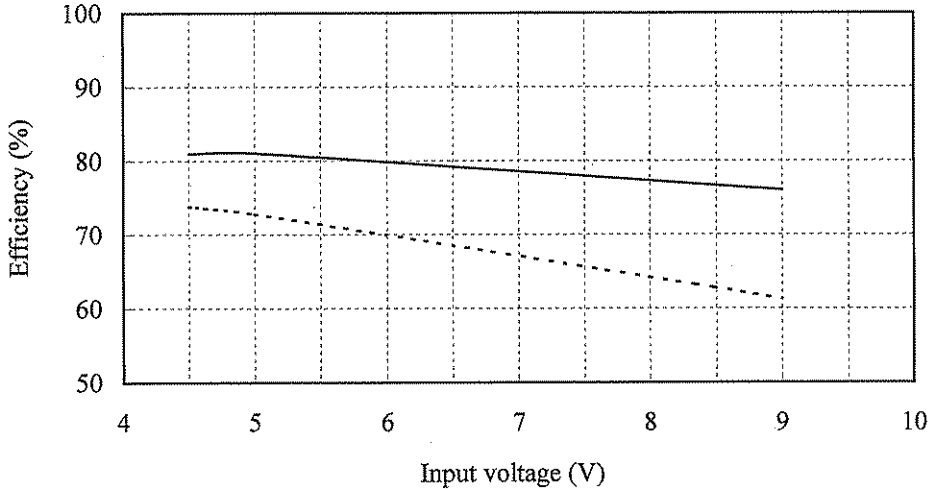
PSD1R5-48-1212



2.1 (4) 効率対入力電圧
Efficiency v.s. input voltage

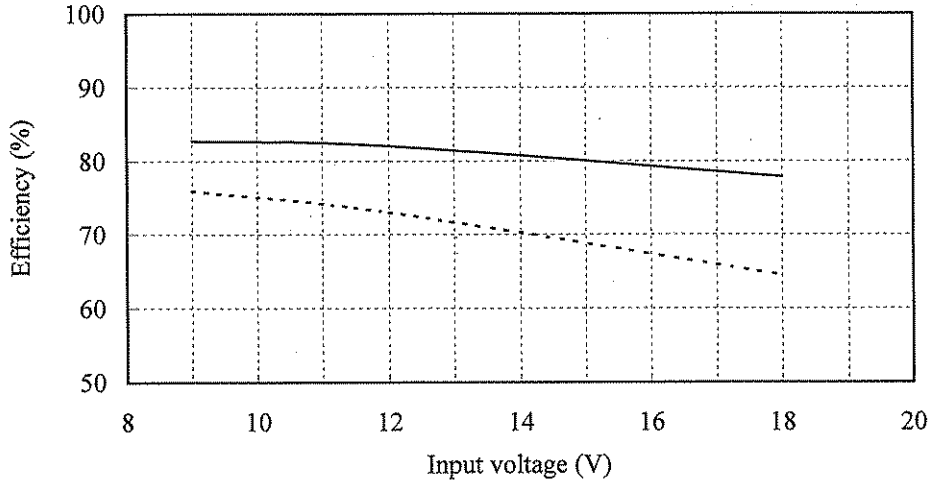
Conditions Ta : 25 °C
Iout : 50 % -----
100 % ————

PSD1R5-5-1212



Conditions Ta : 25 °C
Iout : 50 % -----
100 % ————

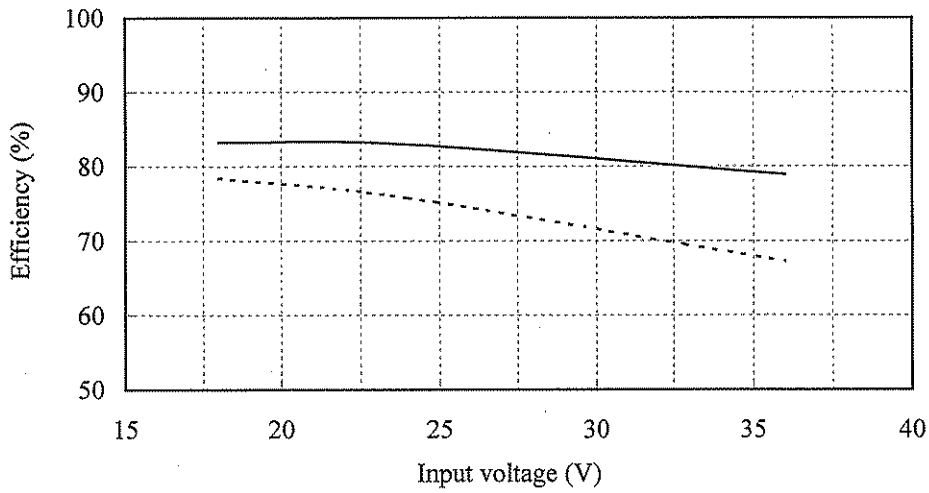
PSD1R5-12-1212



2.1 (4) 効率対入力電圧
Efficiency v.s. input voltage

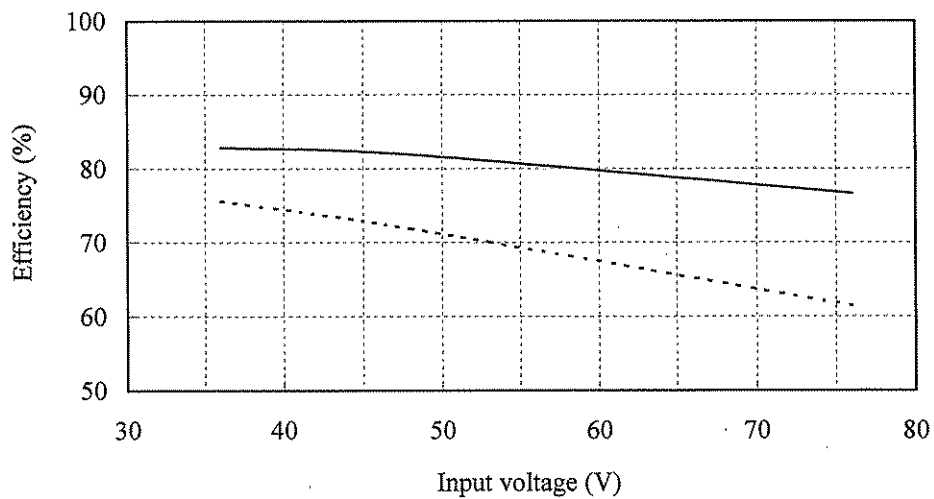
Conditions Ta : 25 °C
Iout : 50 % -----
100 % —————

PSD1R5-24-1212



Conditions Ta : 25 °C
Iout : 50 % -----
100 % —————

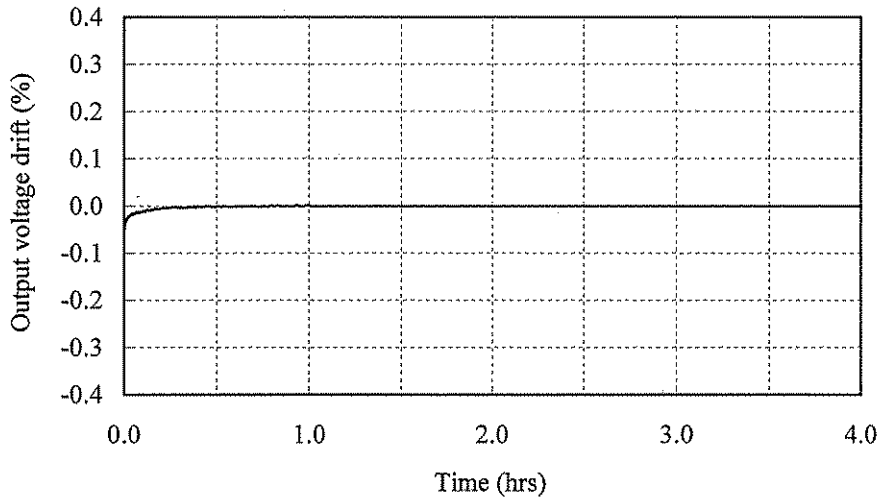
PSD1R5-48-1212



2.2 通電ドリフト特性
Warm up voltage drift characteristics

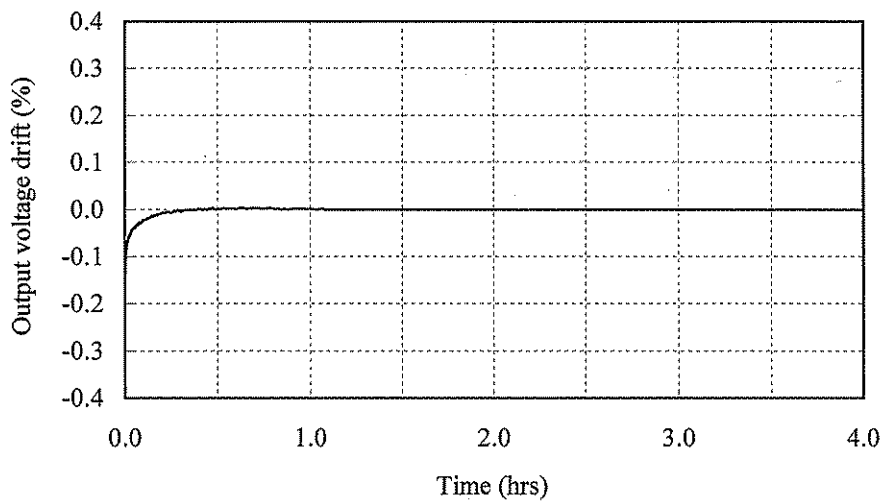
Conditions Vin : 5 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 12 VDC
Iout : 100 %
Ta : 25 °C

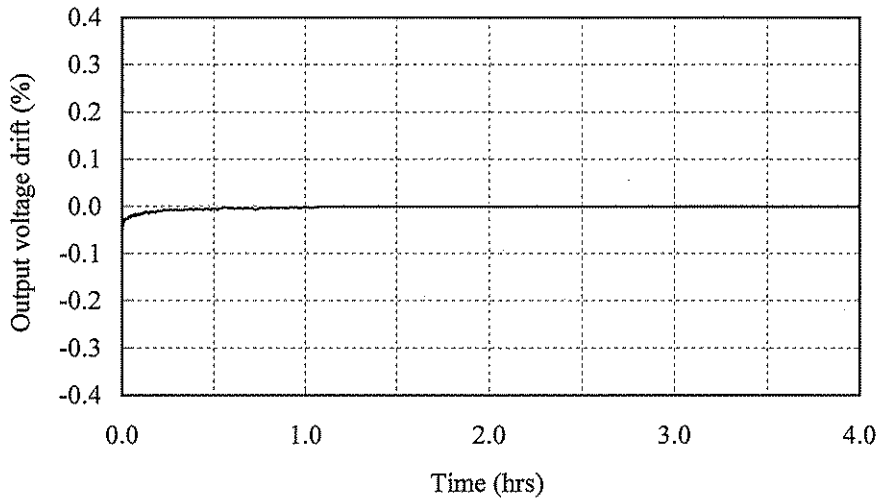
PSD1R5-12-1212



2.2 通電ドリフト特性
Warm up voltage drift characteristics

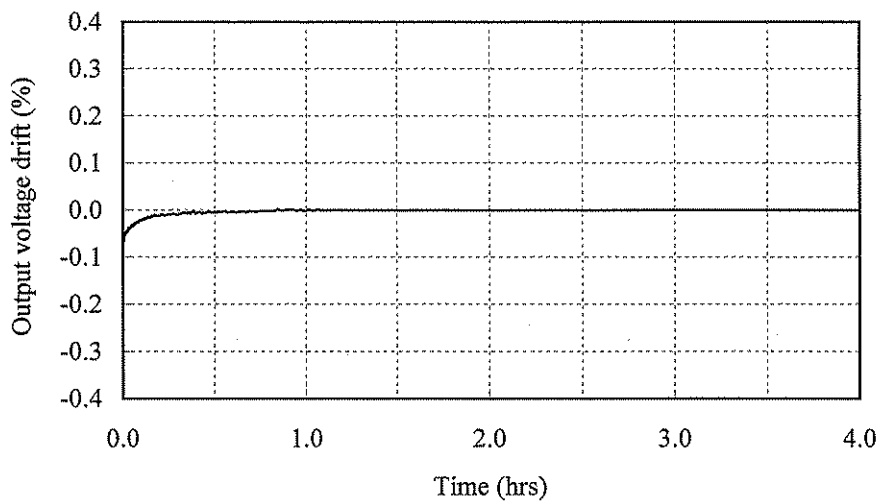
Conditions V_{in} : 24 VDC
 I_{out} : 100 %
 T_a : 25 °C

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Conditions V_{in} : 48 VDC
 I_{out} : 100 %
 T_a : 25 °C

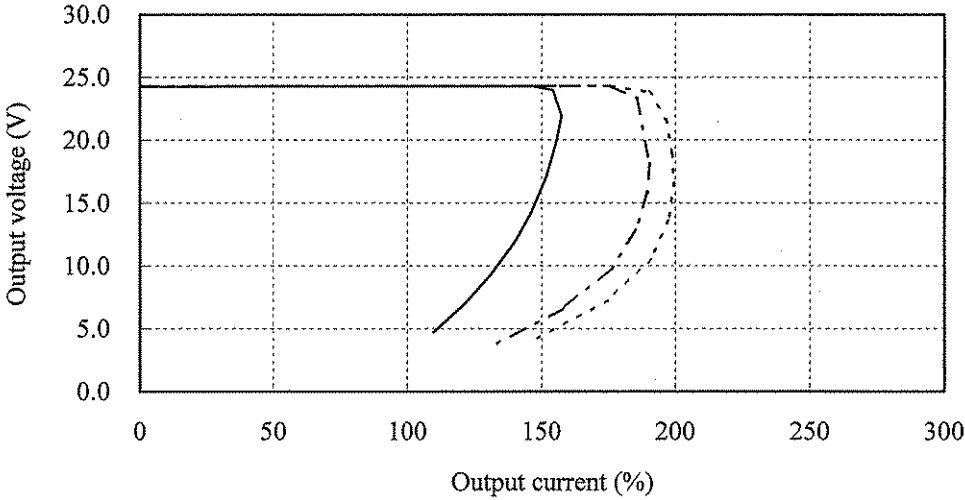
PSD1R5-48-1212



2.3 過電流保護特性
Over current protection (OCP) characteristics

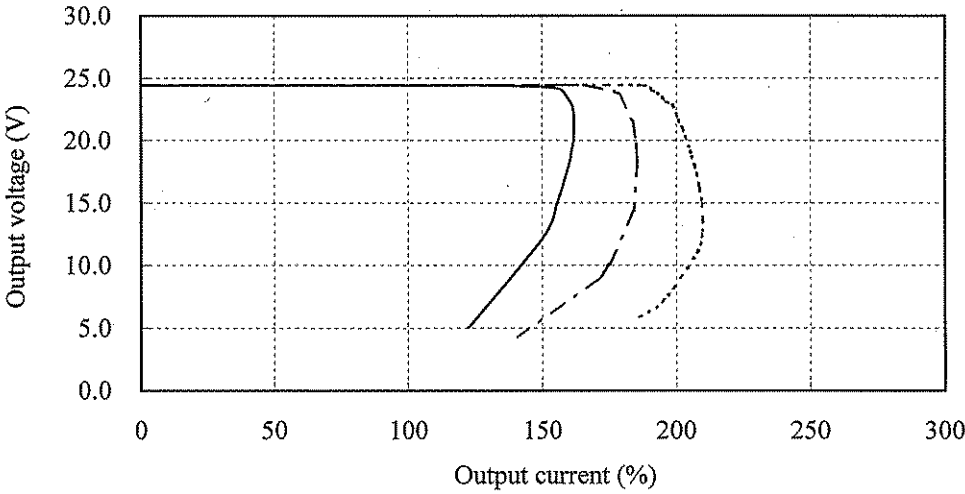
Conditions Vin : 4.5 VDC -----
5 VDC - - - - -
9 VDC ————
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 9 VDC -----
12 VDC - - - - -
18 VDC ————
Ta : 25 °C

PSD1R5-12-1212

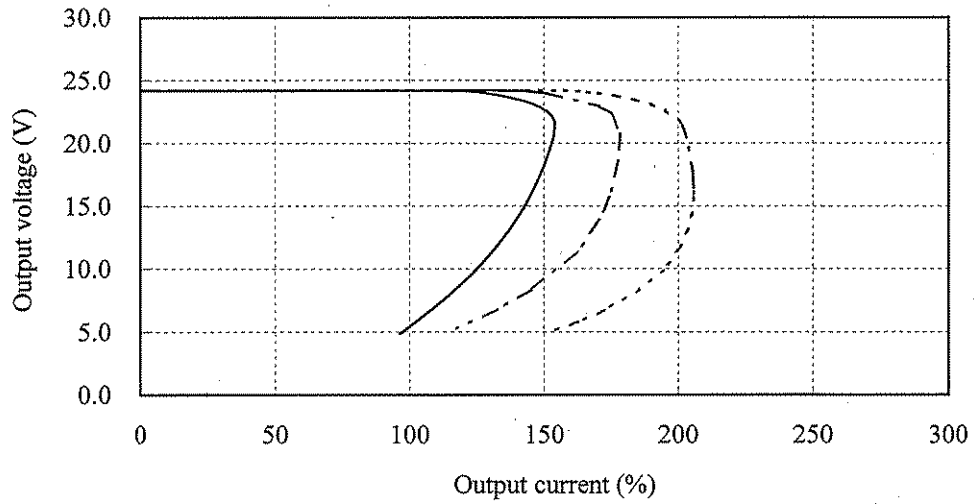


2.3 過電流保護特性

Over current protection (OCP) characteristics

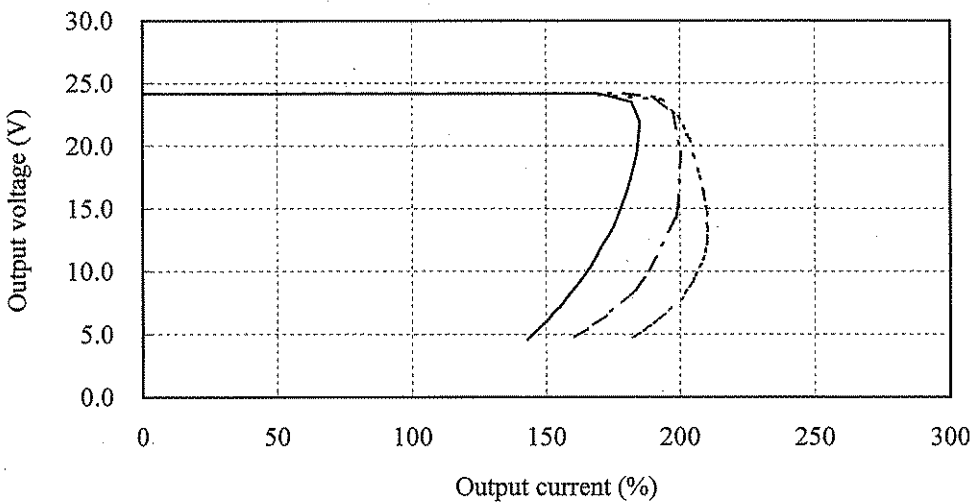
Conditions Vin : 18 VDC -----
 24 VDC -----
 36 VDC -----
 Ta : 25 °C

PSD1R5-24-1212



Conditions Vin : 36 VDC -----
 48 VDC -----
 76 VDC -----
 Ta : 25 °C

PSD1R5-48-1212



2.3 過電流保護特性

Over current protection (OCP) characteristics

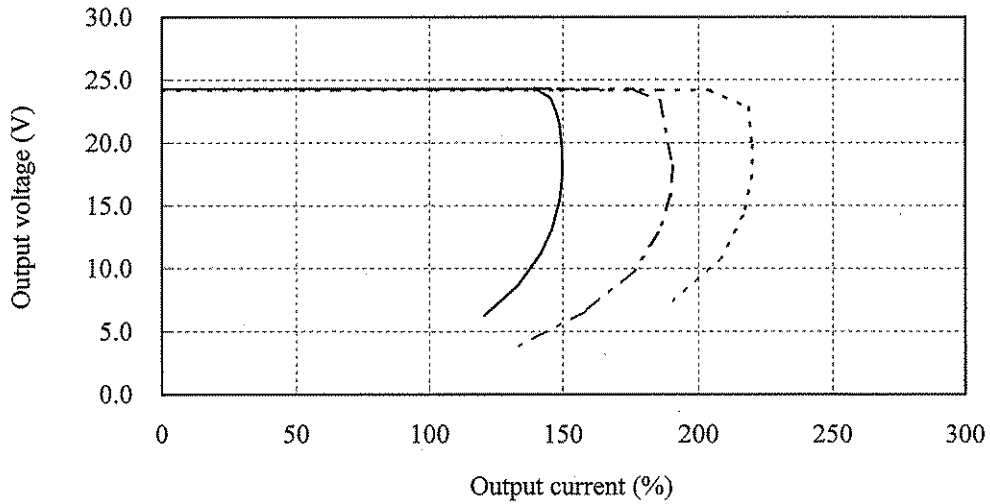
Conditions Vin : 5 VDC

Ta : -40 °C -----

25 °C - - - - -

85 °C _____

PSD1R5-5-1212



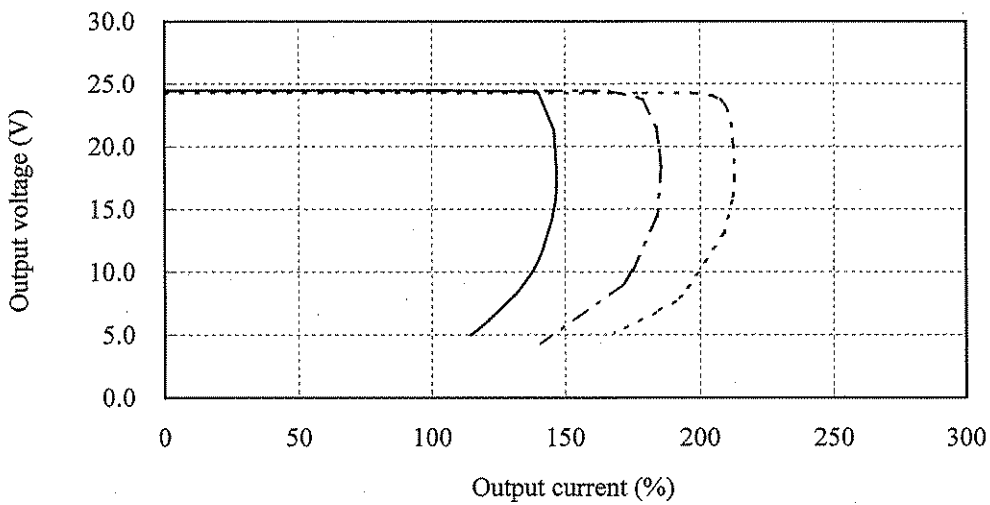
Conditions Vin : 12 VDC

Ta : -40 °C -----

25 °C - - - - -

85 °C _____

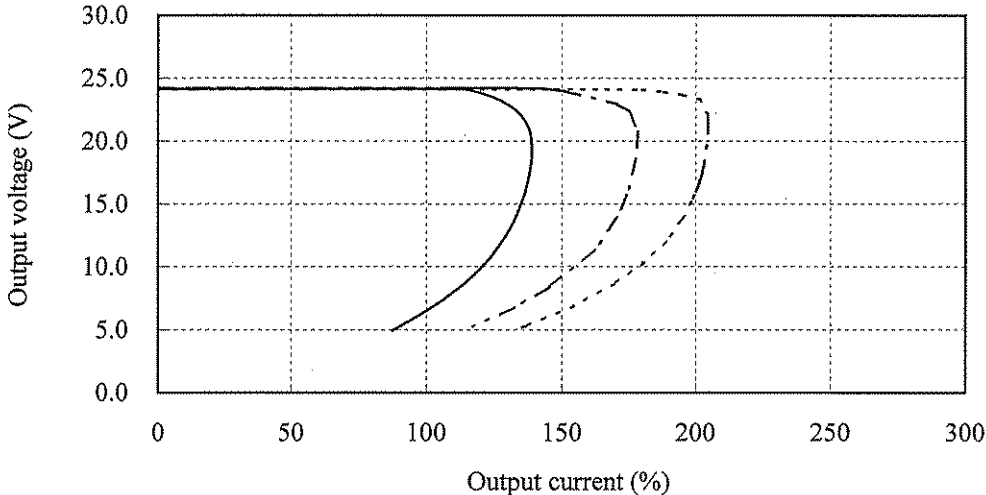
PSD1R5-12-1212



2.3 過電流保護特性
Over current protection (OCP) characteristics

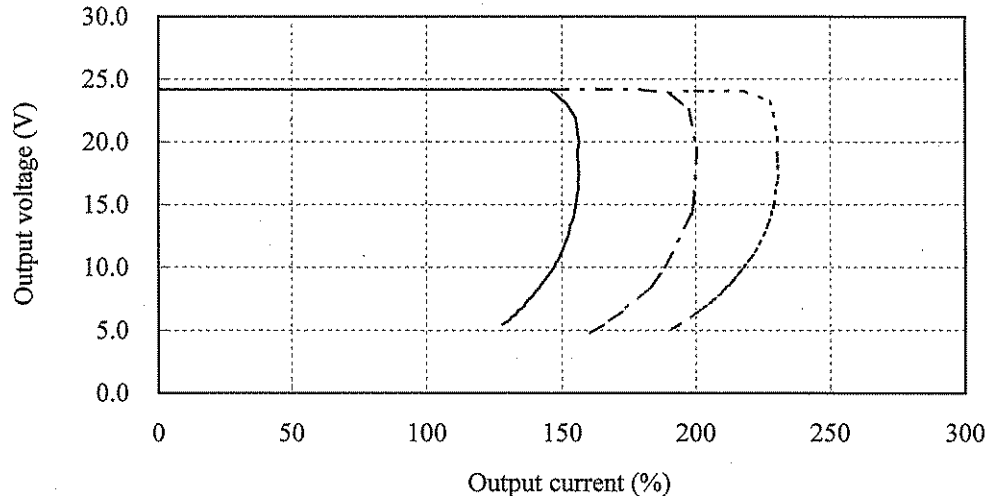
Conditions V_{in} : 24 VDC
 T_a : -40 °C -----
25 °C - - - - -
85 °C ———

PSD1R5-24-1212



Conditions V_{in} : 48 VDC
 T_a : -40 °C -----
25 °C - - - - -
85 °C ———

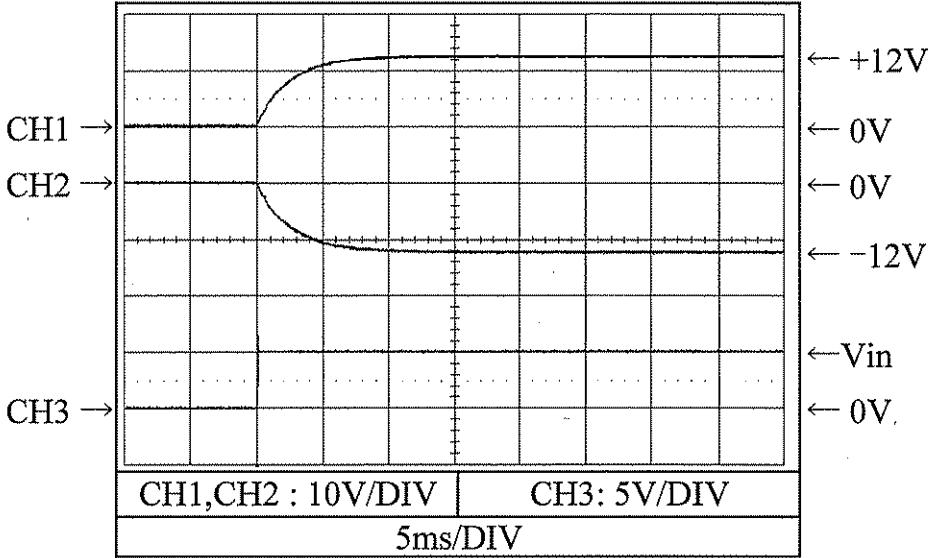
PSD1R5-48-1212



2.4 出力立ち上がり特性
Output rise characteristics

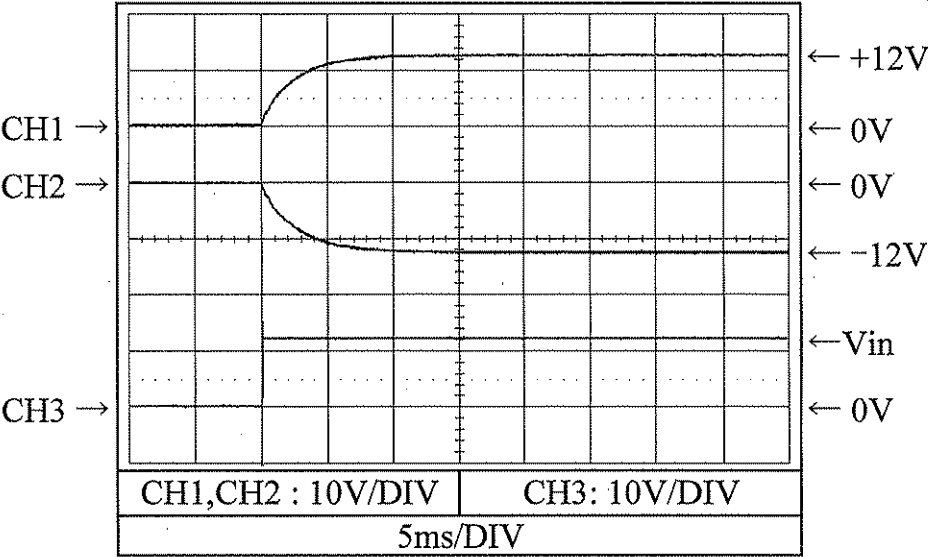
Conditions Vin : 5 VDC
Iout : 0 %
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 12 VDC
Iout : 0 %
Ta : 25 °C

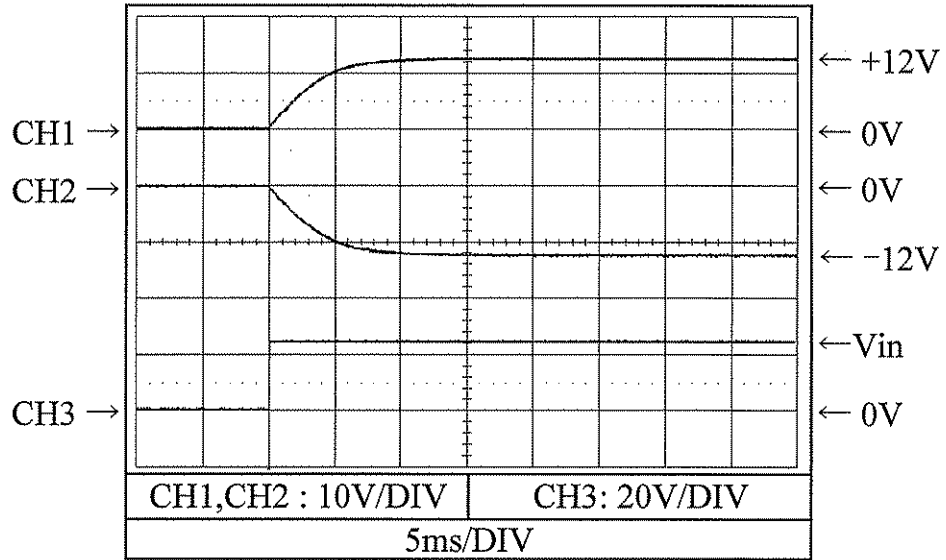
PSD1R5-12-1212



2.4 出力立ち上がり特性
Output rise characteristics

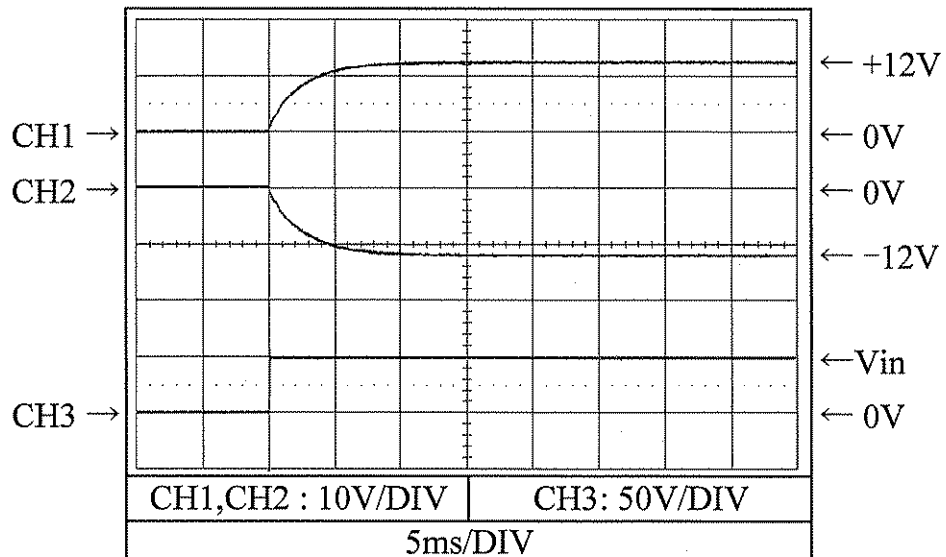
Conditions V_{in} : 24 VDC
 I_{out} : 0 %
 T_a : 25 °C

PSD1R5-24-1212



Conditions V_{in} : 48 VDC
 I_{out} : 0 %
 T_a : 25 °C

PSD1R5-48-1212

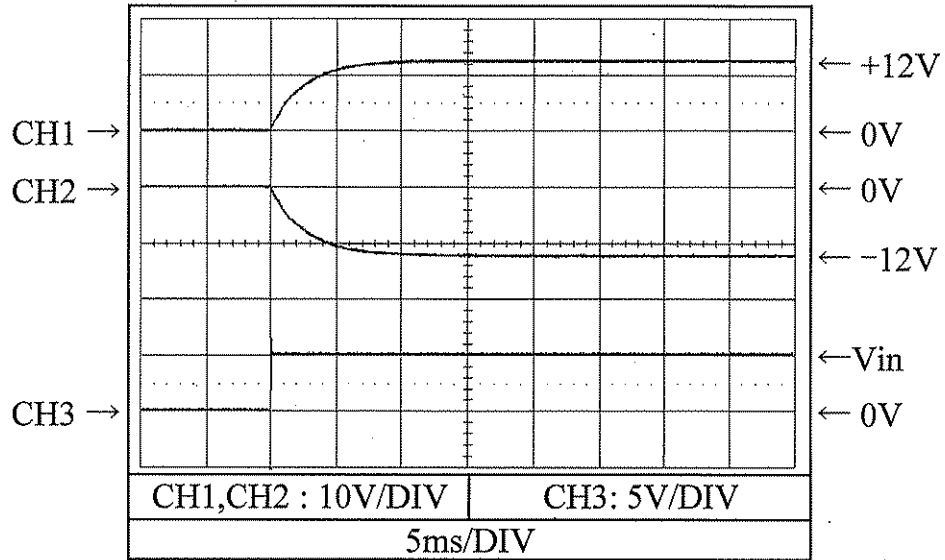


PSD1R5-* -1212

2.4 出力立ち上がり特性 Output rise characteristics

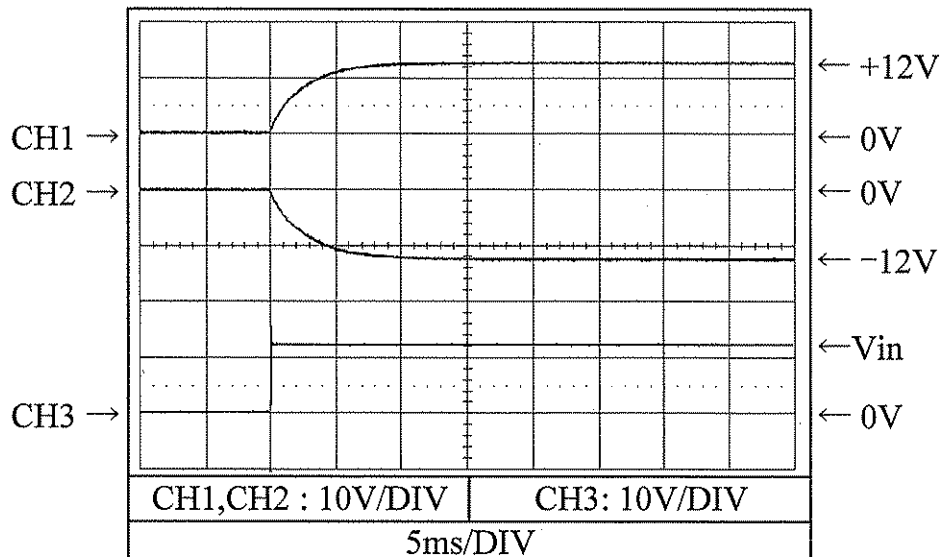
Conditions V_{in} : 5 VDC
 I_{out} : 100 %
 T_a : 25 °C

PSD1R5-5-1212



Conditions V_{in} : 12 VDC
 I_{out} : 100 %
 T_a : 25 °C

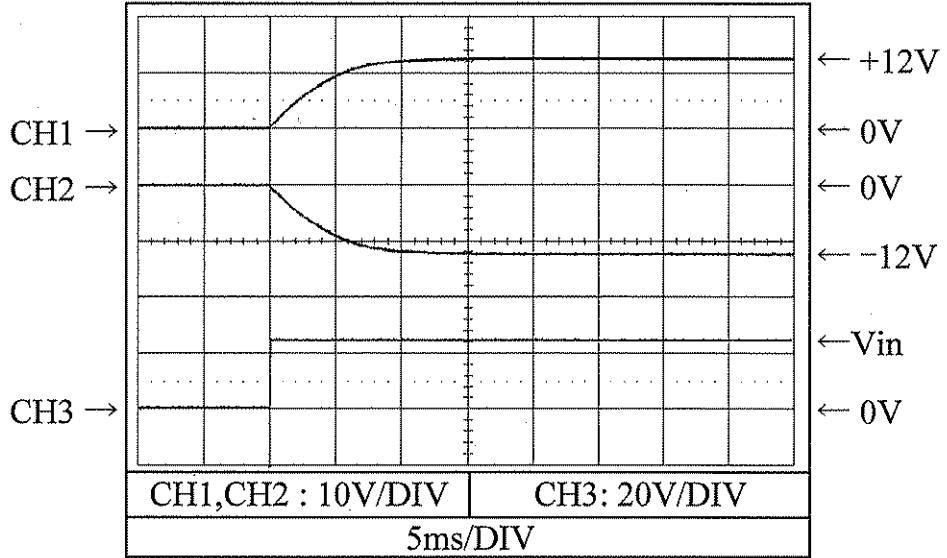
PSD1R5-12-1212



2.4 出力立ち上がり特性
Output rise characteristics

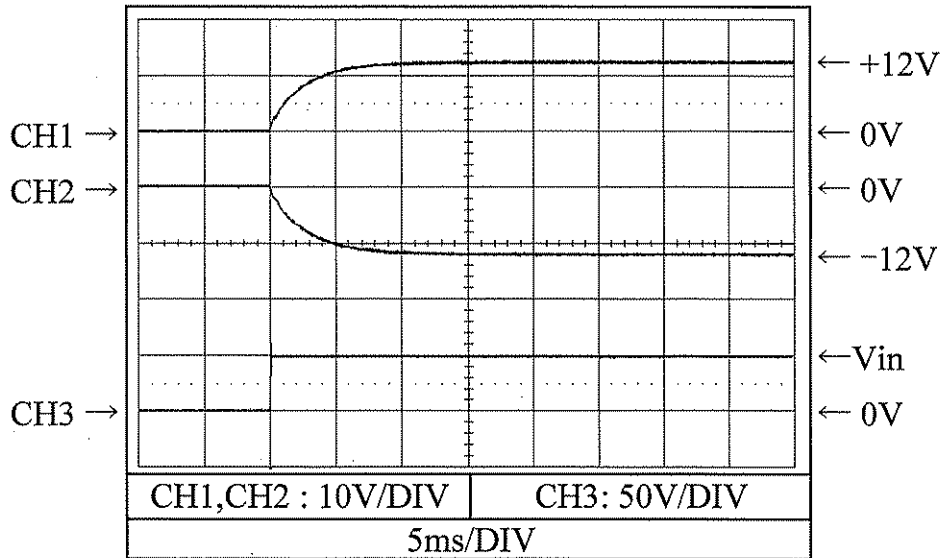
Conditions V_{in} : 24 VDC
 I_{out} : 100 %
 T_a : 25 °C

PSD1R5-24-1212



Conditions V_{in} : 48 VDC
 I_{out} : 100 %
 T_a : 25 °C

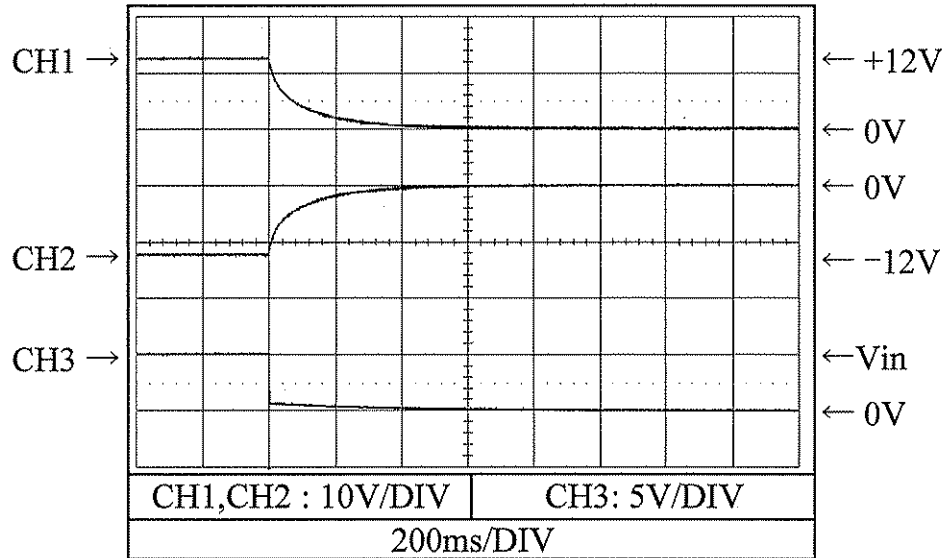
PSD1R5-48-1212



2.5 出力立ち下がり特性
Output fall characteristics

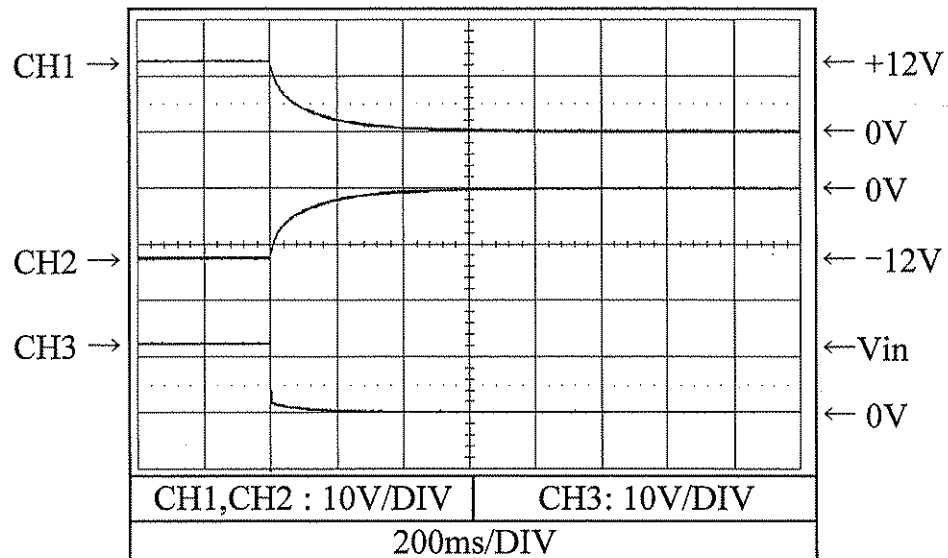
Conditions Vin : 5 VDC
Iout : 0 %
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 12 VDC
Iout : 0 %
Ta : 25 °C

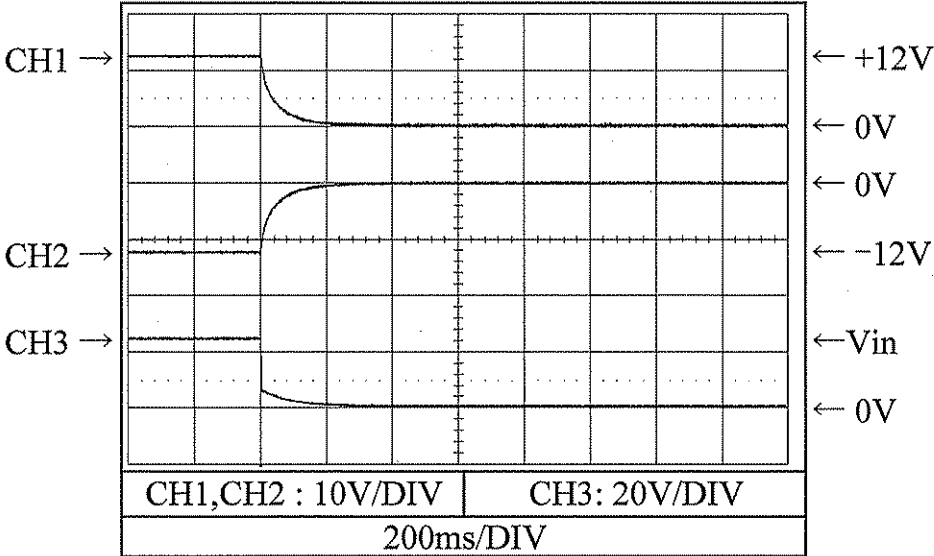
PSD1R5-12-1212



2.5 出力立ち下がり特性
Output rise characteristics

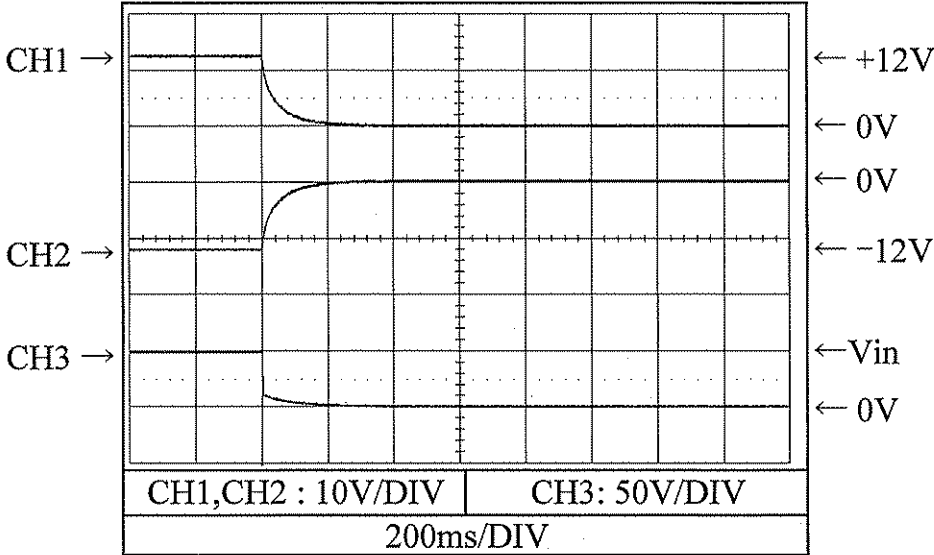
Conditions V_{in} : 24 VDC
 I_{out} : 0 %
 T_a : 25 °C

PSD1R5-24-1212



Conditions V_{in} : 48 VDC
 I_{out} : 0 %
 T_a : 25 °C

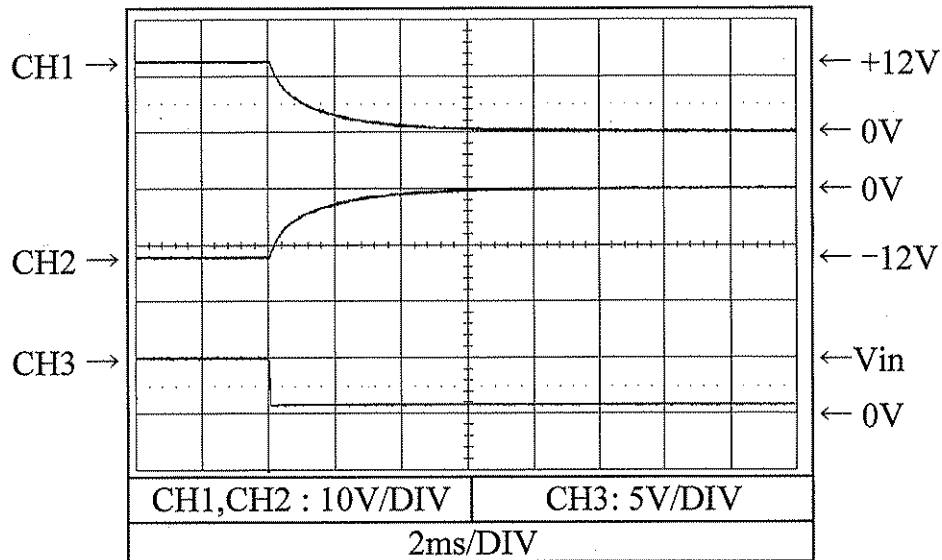
PSD1R5-48-1212



2.5 出力立ち下がり特性
Output fall characteristics

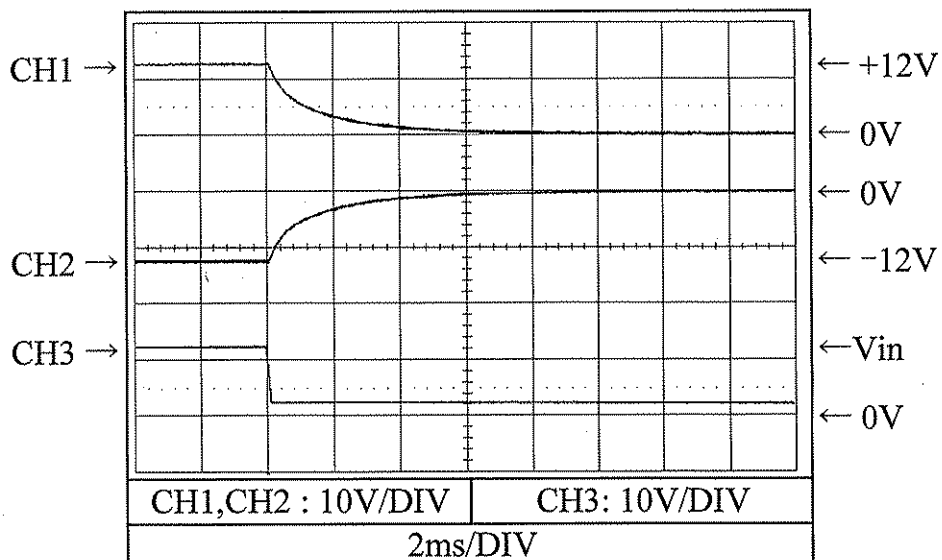
Conditions Vin : 5 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 12 VDC
Iout : 100 %
Ta : 25 °C

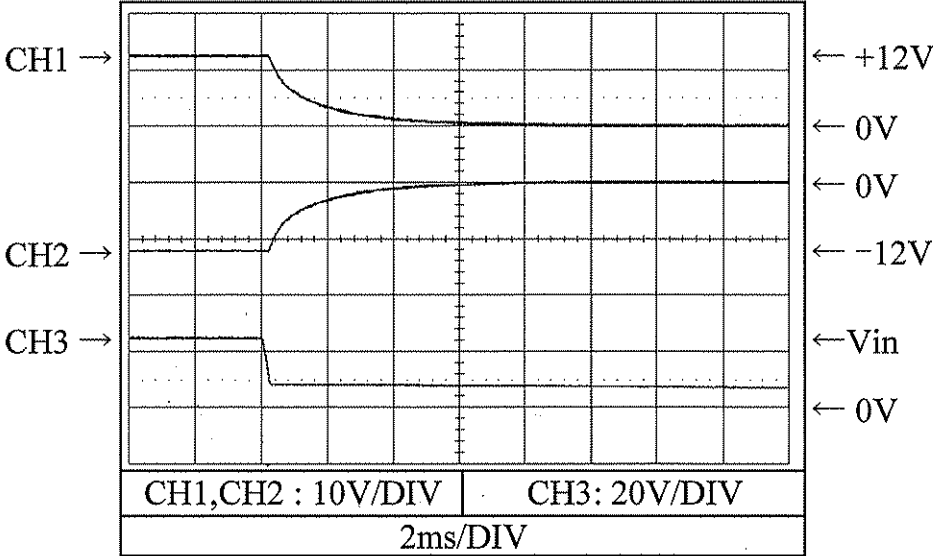
PSD1R5-12-1212



2.5 出力立ち下がり特性
Output rise characteristics

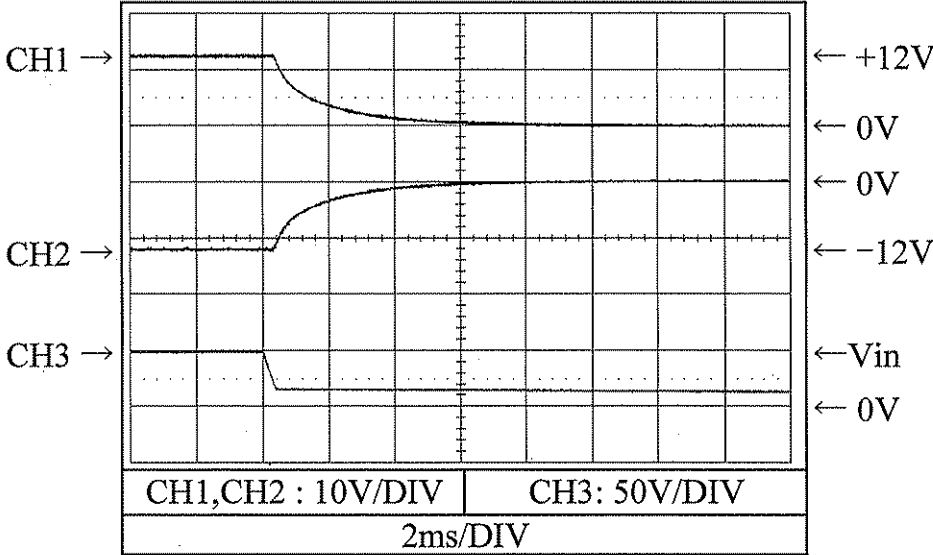
Conditions Vin : 24 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-24-1212



Conditions Vin : 48 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-48-1212

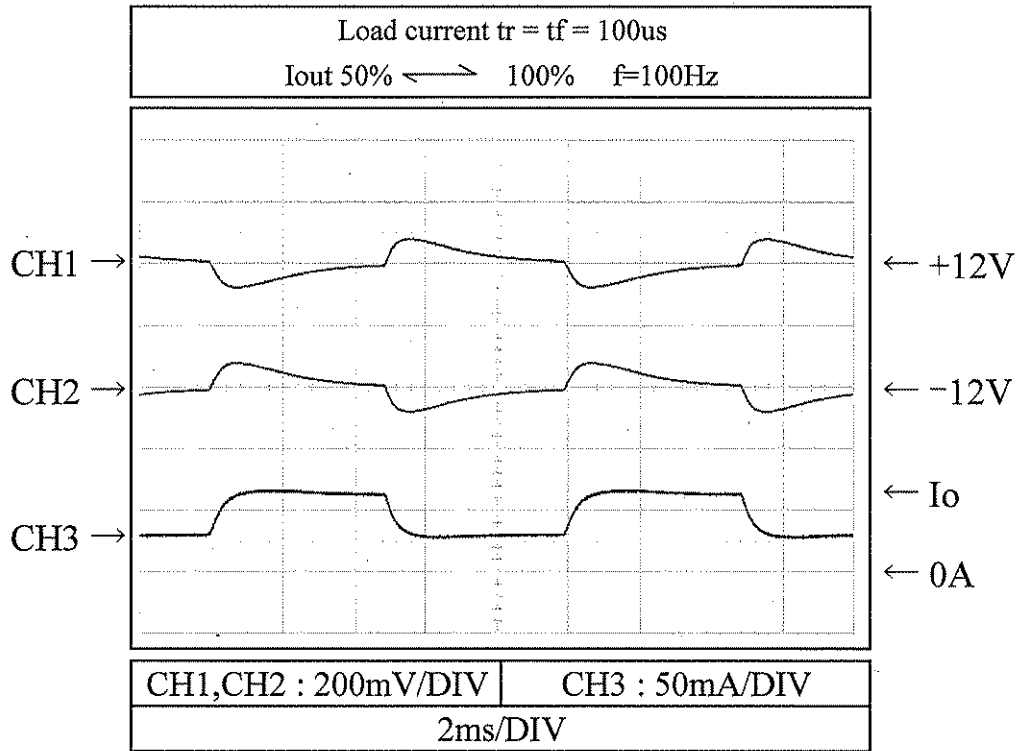


2.6 過渡応答（負荷急変）特性
Dynamic load response characteristics

PSD1R5-* -1212

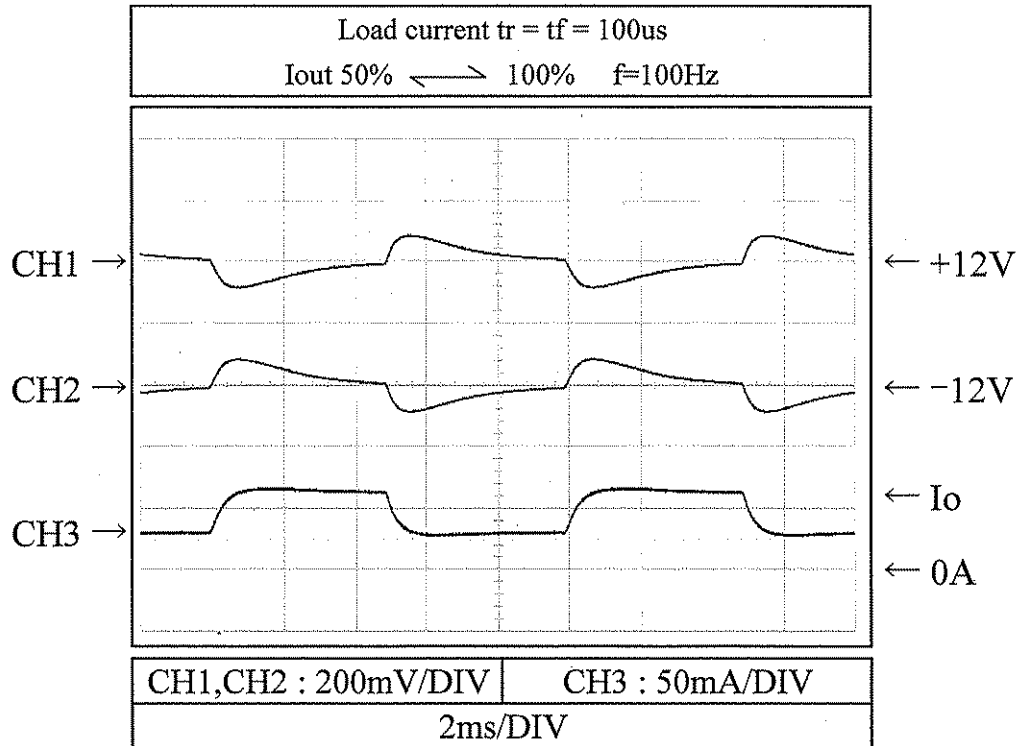
Conditions Vin : 5 VDC
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 12 VDC
Ta : 25 °C

PSD1R5-12-1212

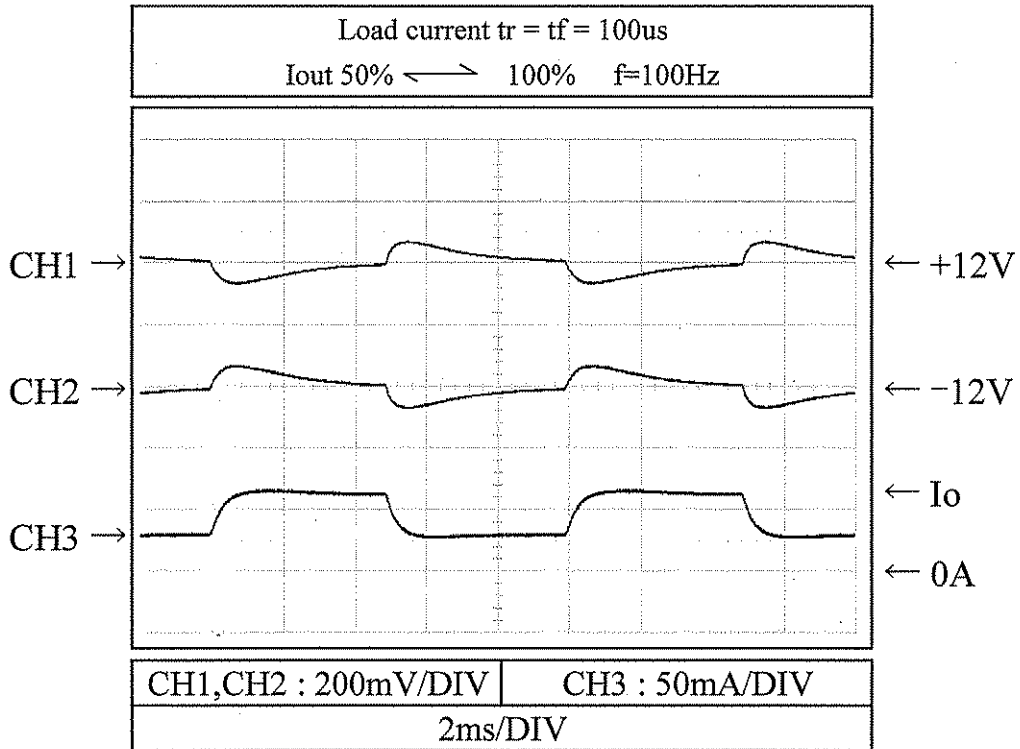


2.6 過渡応答 (負荷急変) 特性
Dynamic load response characteristics

PSD1R5-* -1212

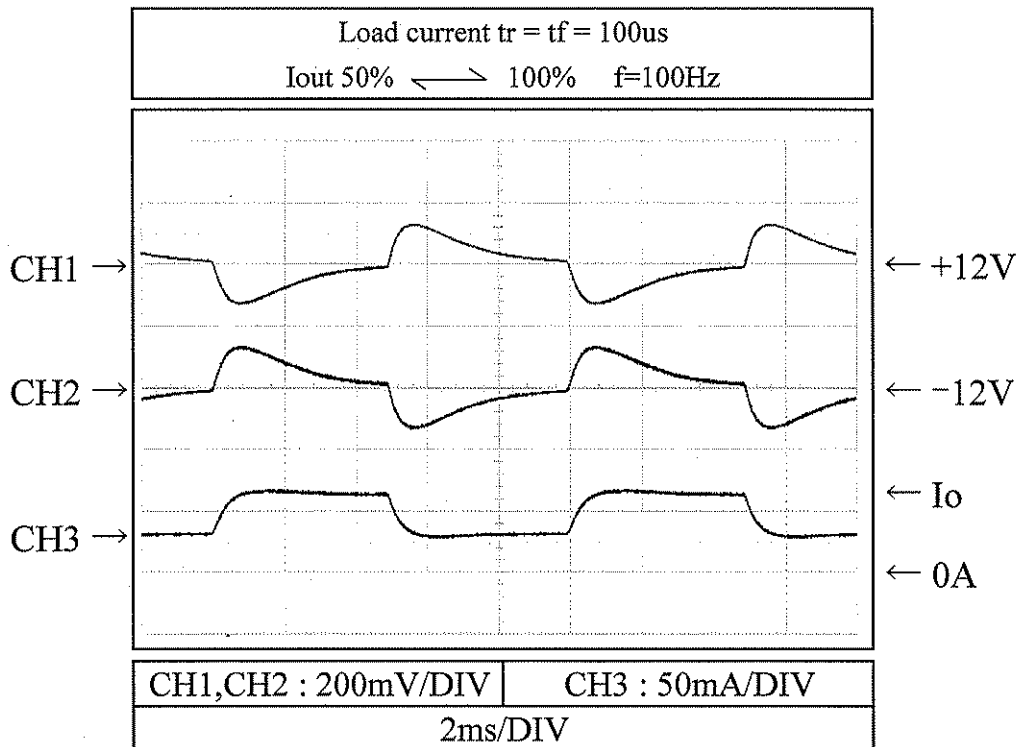
Conditions V_{in} : 24 VDC
 T_a : 25 °C

PSD1R5-24-1212



Conditions V_{in} : 48 VDC
 T_a : 25 °C

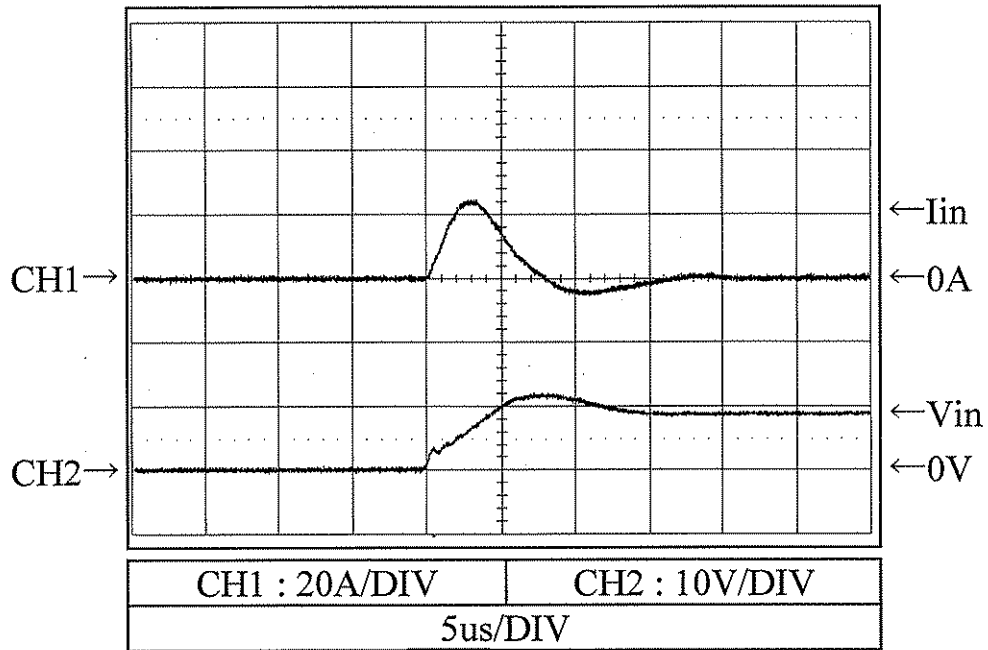
PSD1R5-48-1212



2.7 入力サージ電流 (突入電流) 特性
Inrush current waveform

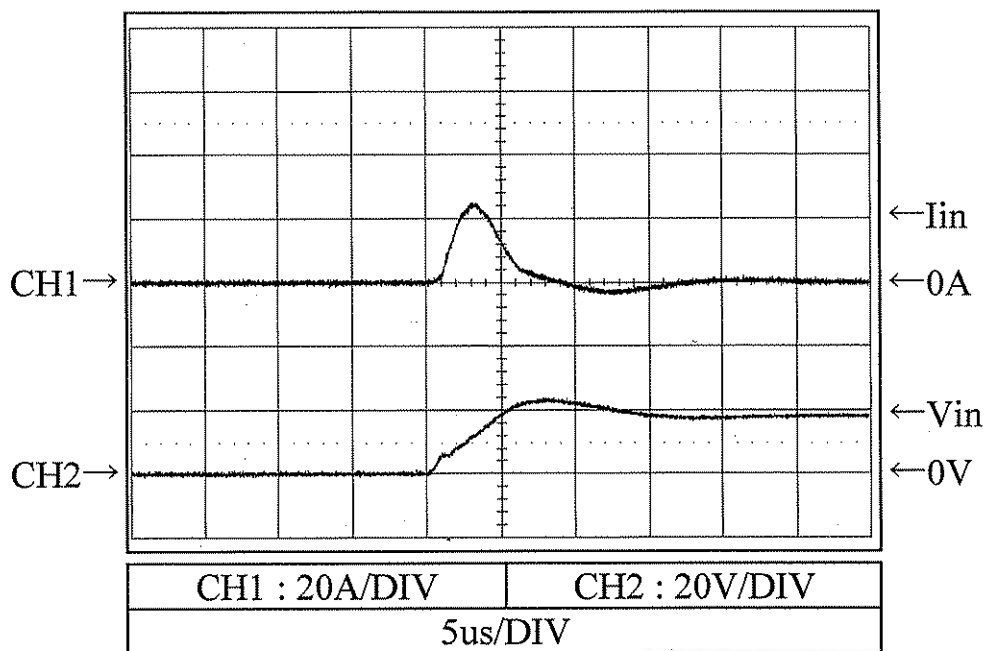
Conditions Vin : 9 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 18 VDC
Iout : 100 %
Ta : 25 °C

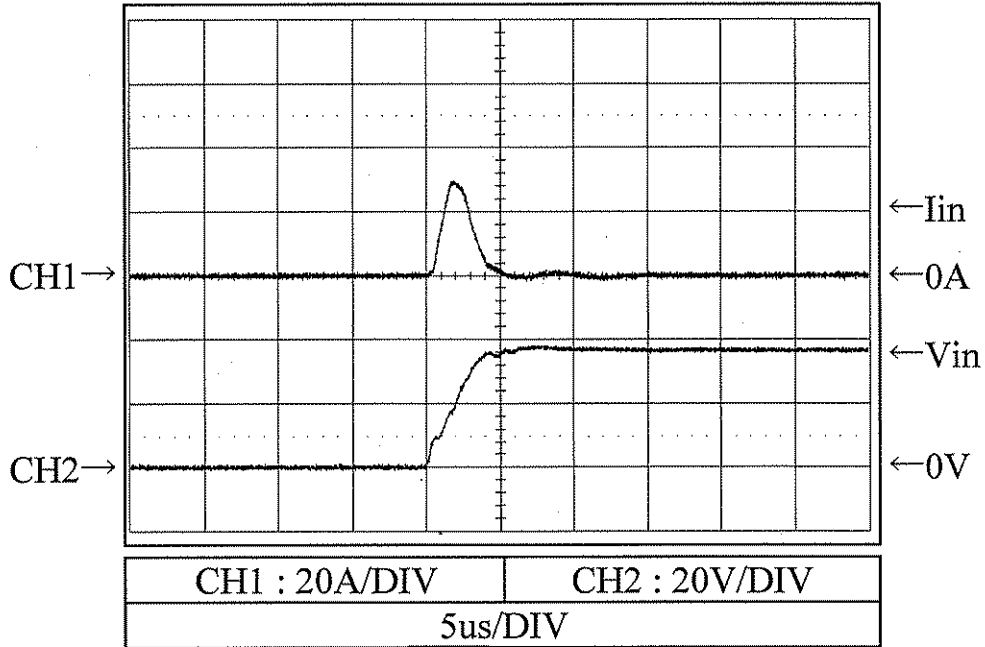
PSD1R5-12-1212



2.7 入力サージ電流 (突入電流) 特性
Inrush current waveform

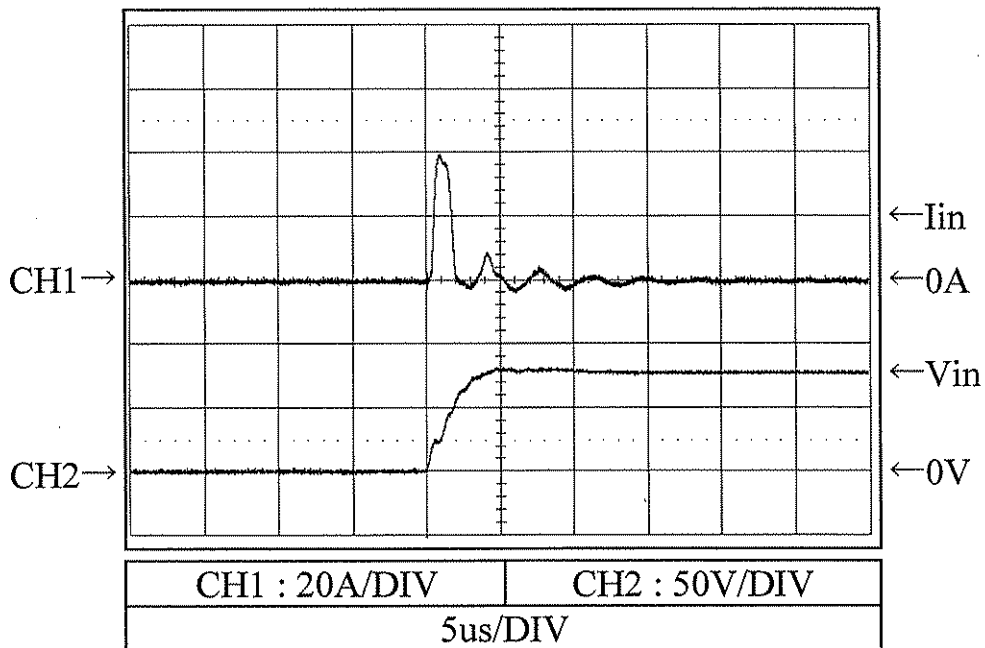
Conditions Vin : 36 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-24-1212



Conditions Vin : 76 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-48-1212

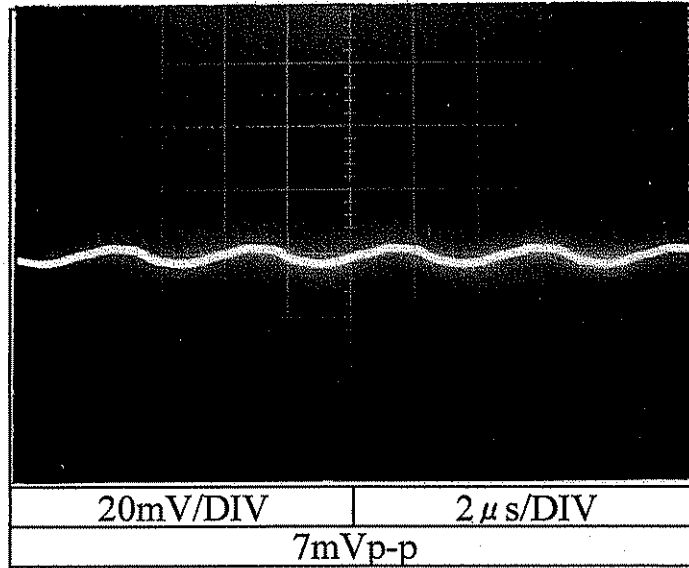


2.8 出力リップル、ノイズ波形
Output ripple and noise waveform

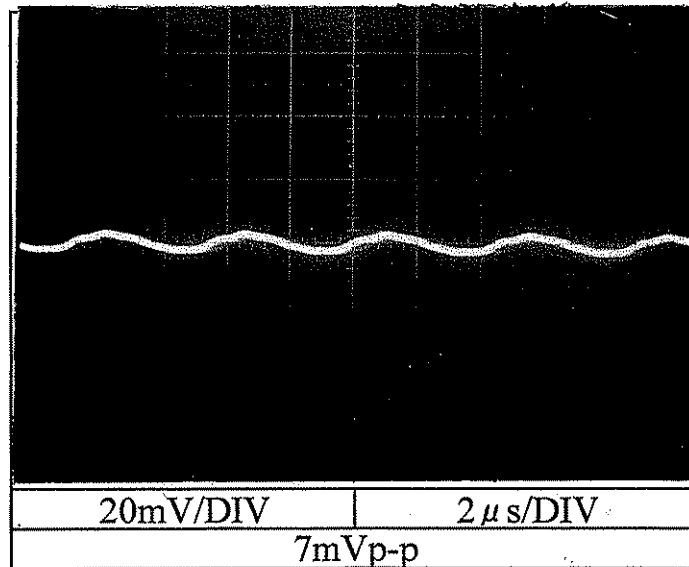
PSD1R5-5-1212

Conditions Vin : 5 VDC
Iout : 100 %
Ta : 25 °C

12V (CH1)



-12V (CH2)

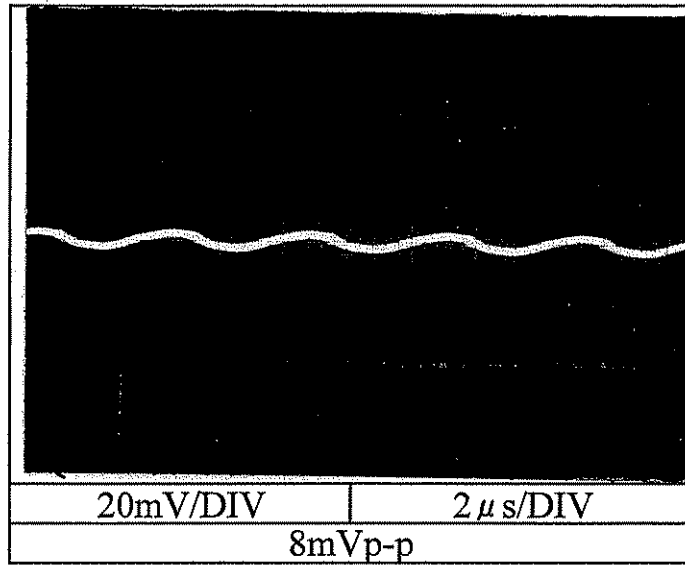


2.8 出力リップル、ノイズ波形
Output ripple and noise waveform

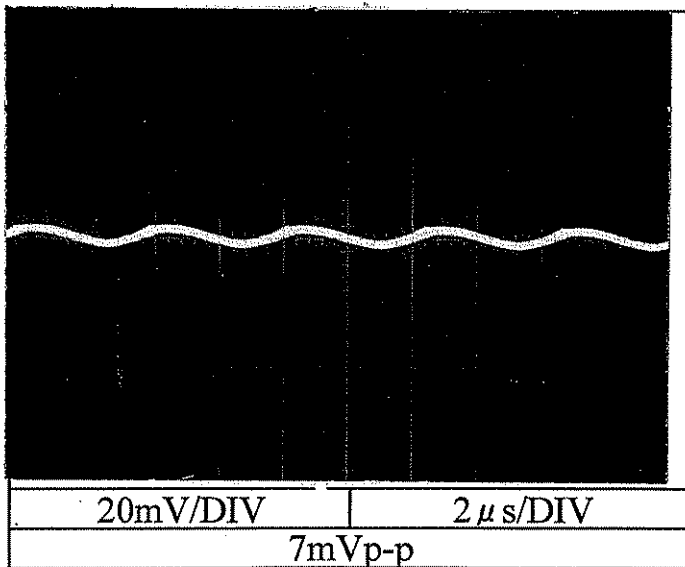
PSD1R5-12-1212

Conditions Vin : 12 VDC
Iout : 100 %
Ta : 25 °C

12V (CH1)



-12V (CH2)

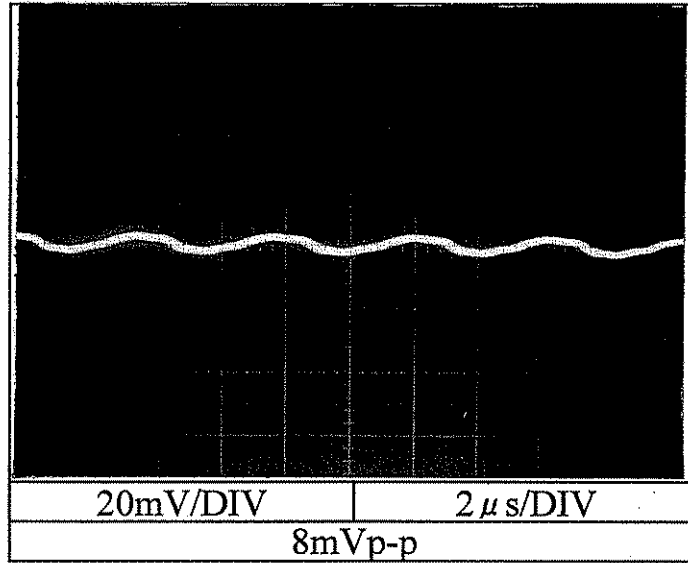


2.8 出力リップル、ノイズ波形
Output ripple and noise waveform

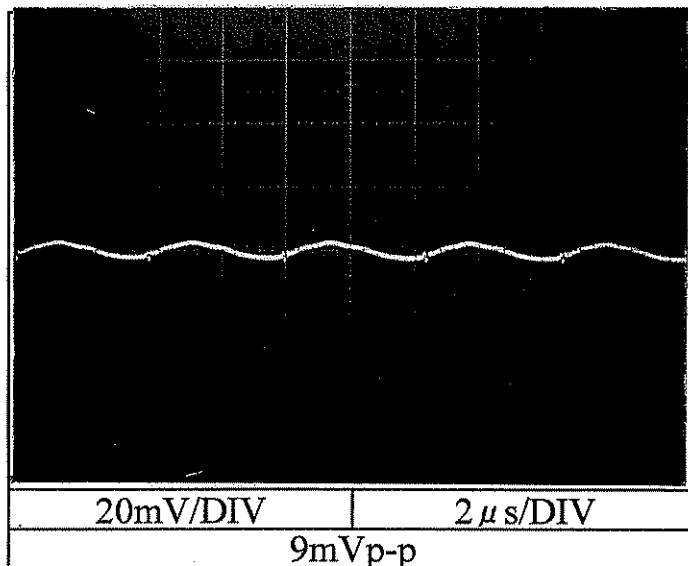
PSD1R5-24-1212

Conditions V_{in} : 24 VDC
 I_{out} : 100 %
 T_a : 25 °C

12V (CH1)



-12V (CH2)

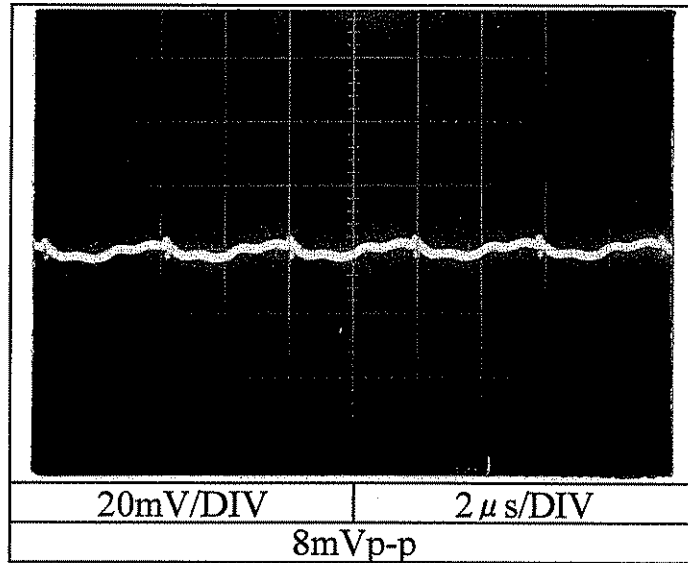


2.8 出力リップル、ノイズ波形
Output ripple and noise waveform

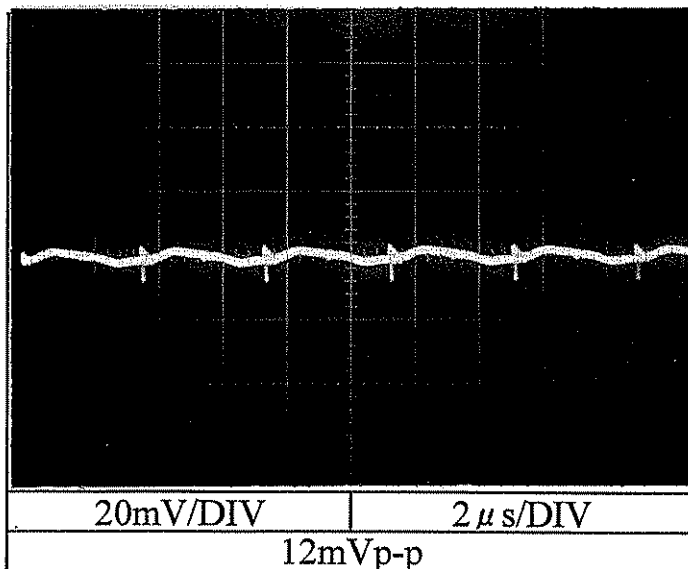
PSD1R5-48-1212

Conditions V_{in} : 48 VDC
 I_{out} : 100 %
 T_a : 25 °C

12V (CH1)



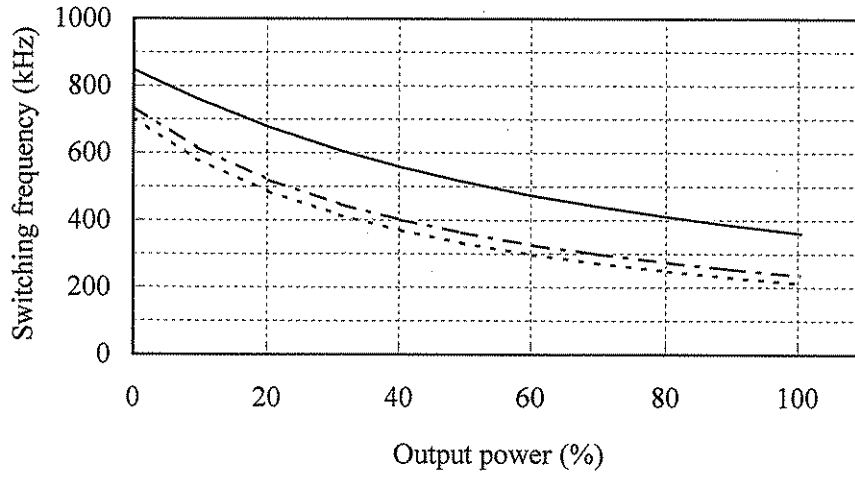
-12V (CH2)



2.9 スイッチング周波数対出力電力
Switching frequency v.s. output power

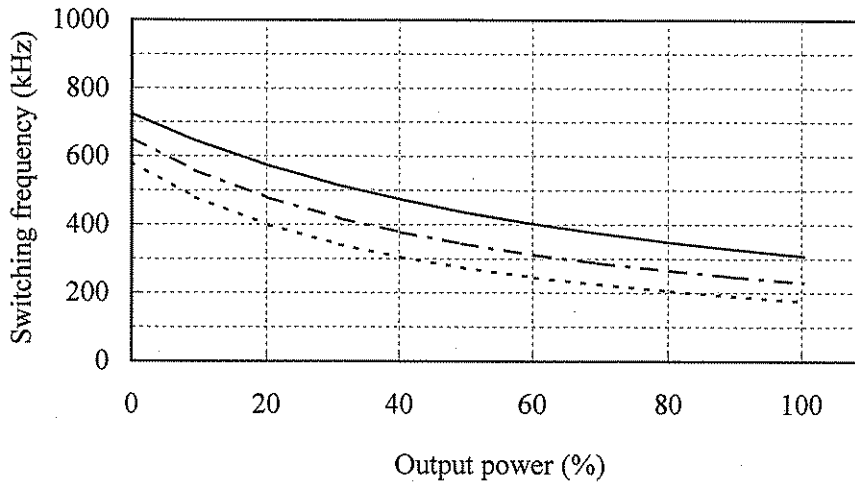
Conditions Vin : 4.5 VDC -----
5 VDC -----
9 VDC -----
Ta : 25 °C

PSD1R5-5-1212



Conditions Vin : 9 VDC -----
12 VDC -----
18 VDC -----
Ta : 25 °C

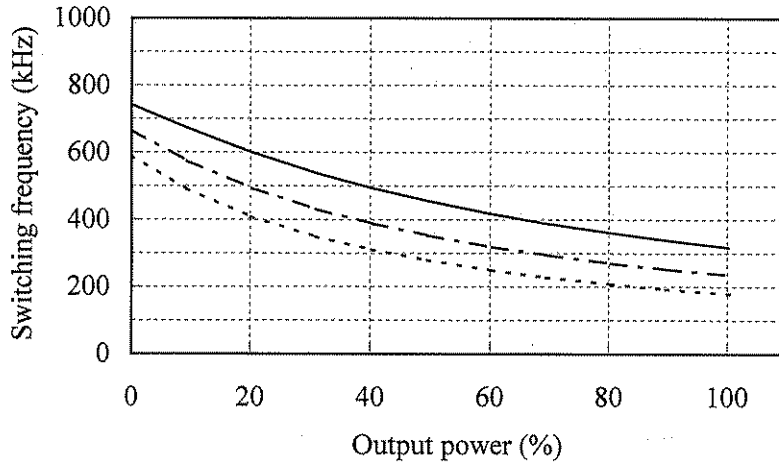
PSD1R5-12-1212



2.9 スイッチング周波数対出力電力
Switching frequency v.s. output power

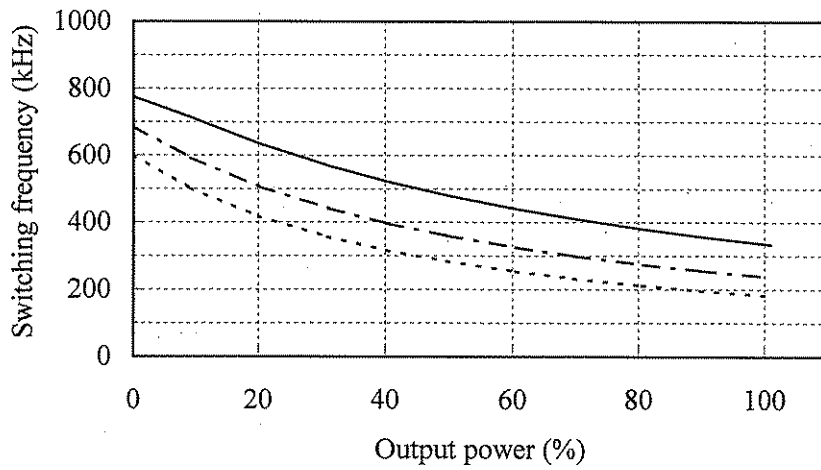
Conditions Vin : 18 VDC -----
 24 VDC -----
 36 VDC -----
 Ta : 25 °C

PSD1R5-24-1212



Conditions Vin : 36 VDC -----
 48 VDC -----
 76 VDC -----
 Ta : 25 °C

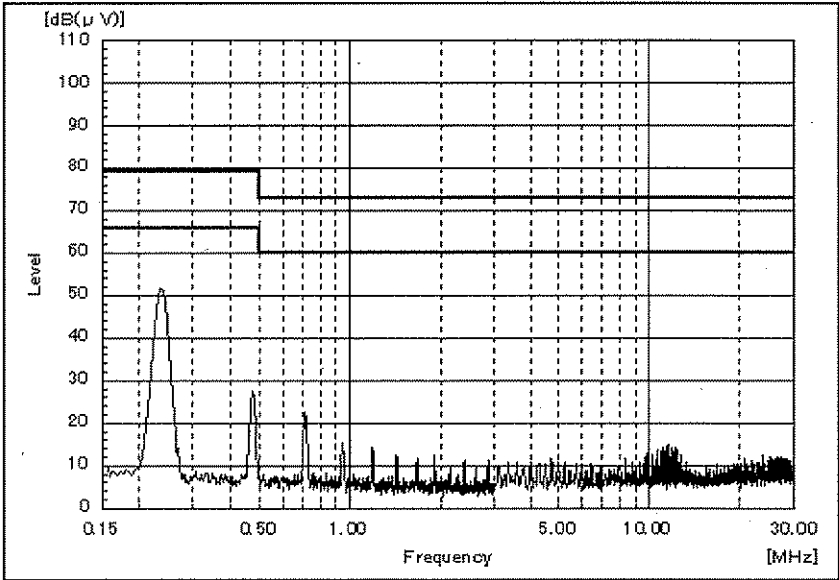
PSD1R5-48-1212



2.10 EMI特性
 Electro-Magnetic Interference characteristics
 (a) 雑音端子電圧 (帰還ノイズ)
 Conducted Emission
 VCCI class A 対応アプリケーションシステム
 VCCI class A application system

Conditions Vin : 5 VDC
 Iout : 100 %
 Ta : 25 °C

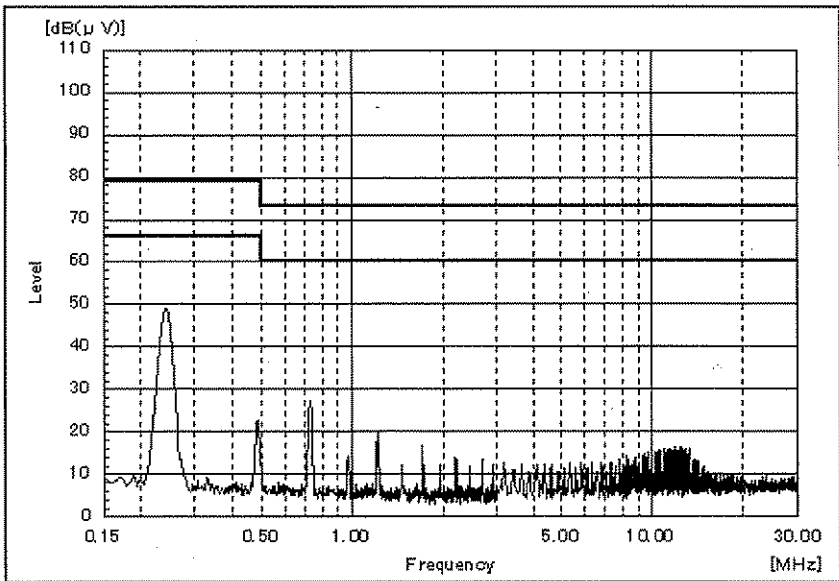
PSD1R5-5-1212



←QP Limit
 ←AV Limit

Conditions Vin : 12 VDC
 Iout : 100 %
 Ta : 25 °C

PSD1R5-12-1212



←QP Limit
 ←AV Limit

2.10 EMI特性

Electro-Magnetic Interference characteristics

(a) 雑音端子電圧 (帰還ノイズ)

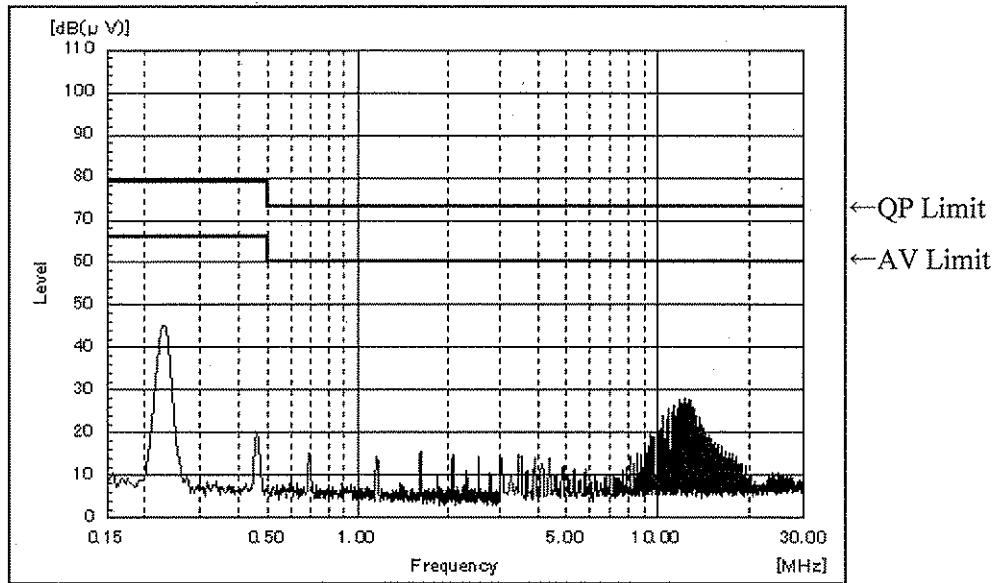
Conducted Emission

VCCI class A 対応アプリケーションシステム

VCCI class A application system

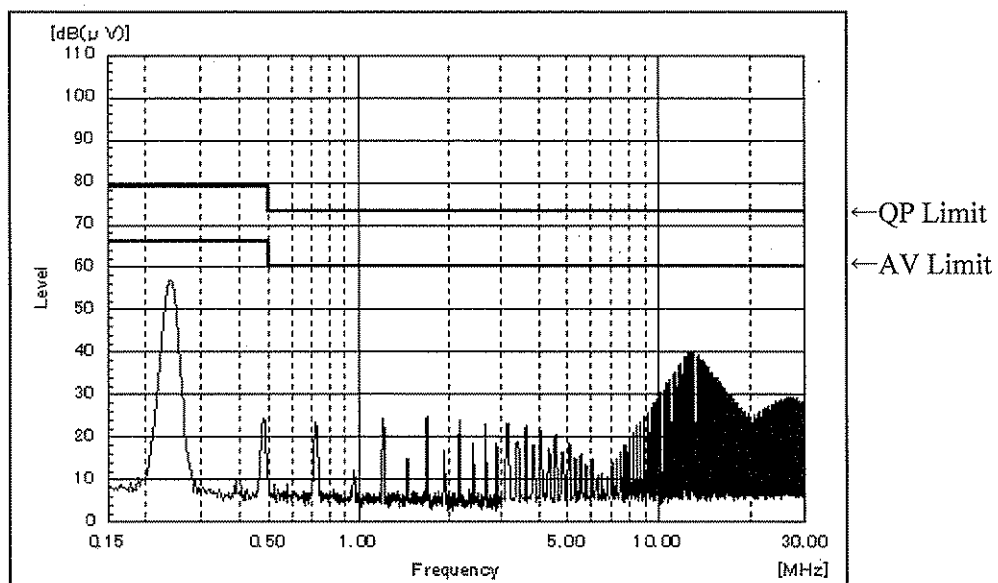
Conditions Vin : 24 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-24-1212



Conditions Vin : 48 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-48-1212



2.10 EMI特性

Electro-Magnetic Interference characteristics

(b) 雑音電界強度 (輻射ノイズ)

Radiated Emission

VCCI class A 対応アプリケーションシステム

VCCI class A application system

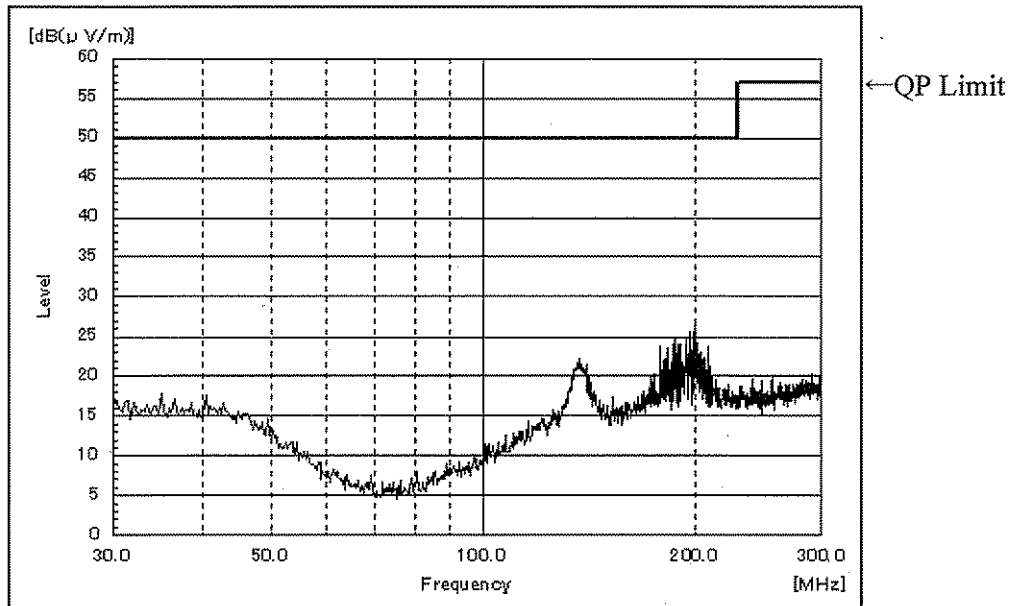
Conditions Vin : 5 VDC

Iout : 100 %

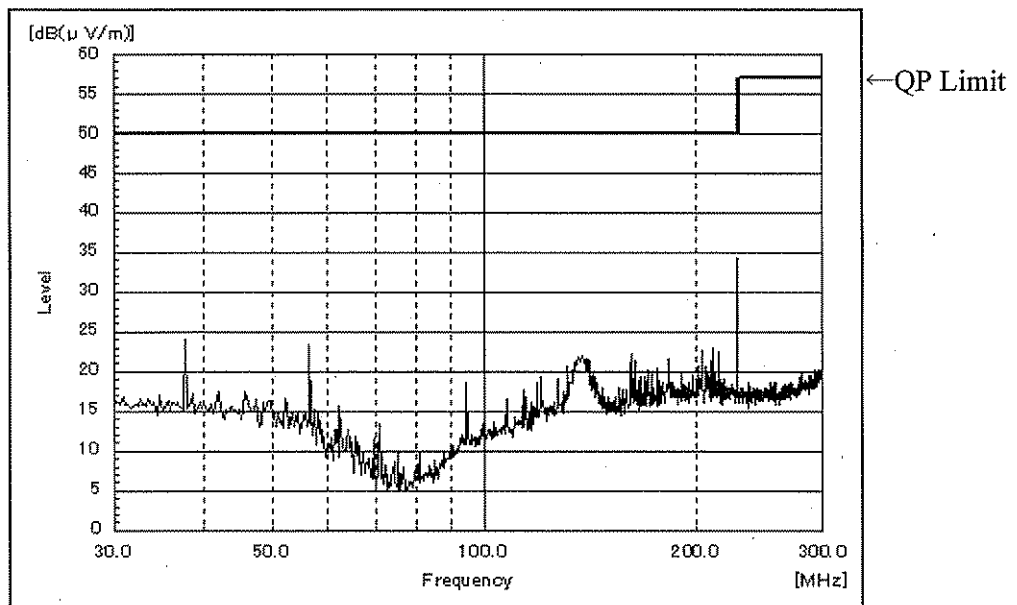
Ta : 25 °C

PSD1R5-5-1212

HORIZONTAL:



VERTICAL:



2.10 EMI特性

Electro-Magnetic Interference characteristics

(b) 雑音電界強度 (輻射ノイズ)

Radiated Emission

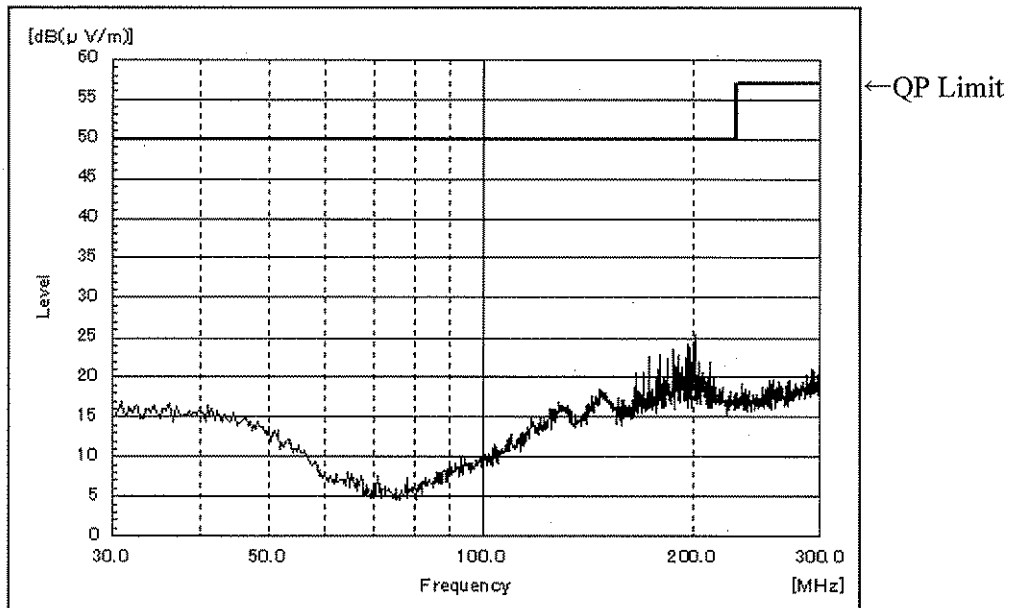
VCCI class A 対応アプリケーションシステム

VCCI class A application system

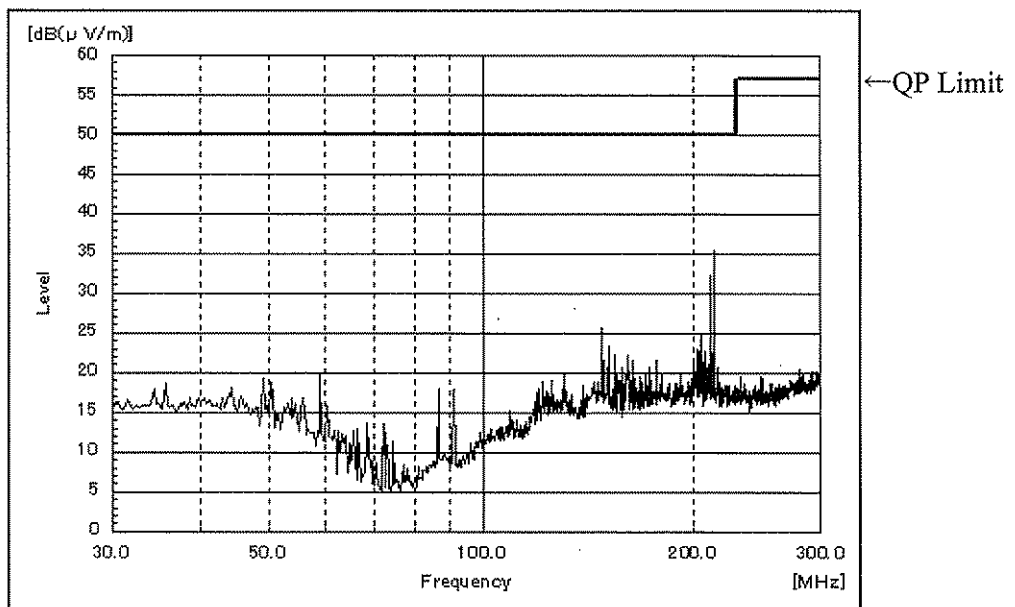
Conditions Vin : 12 VDC
Iout : 100 %
Ta : 25 °C

PSD1R5-12-1212

HORIZONTAL:



VERTICAL:



2.10 EMI特性

Electro-Magnetic Interference characteristics

(b) 雑音電界強度 (輻射ノイズ)

Radiated Emission

VCCI class A 対応アプリケーションシステム

VCCI class A application system

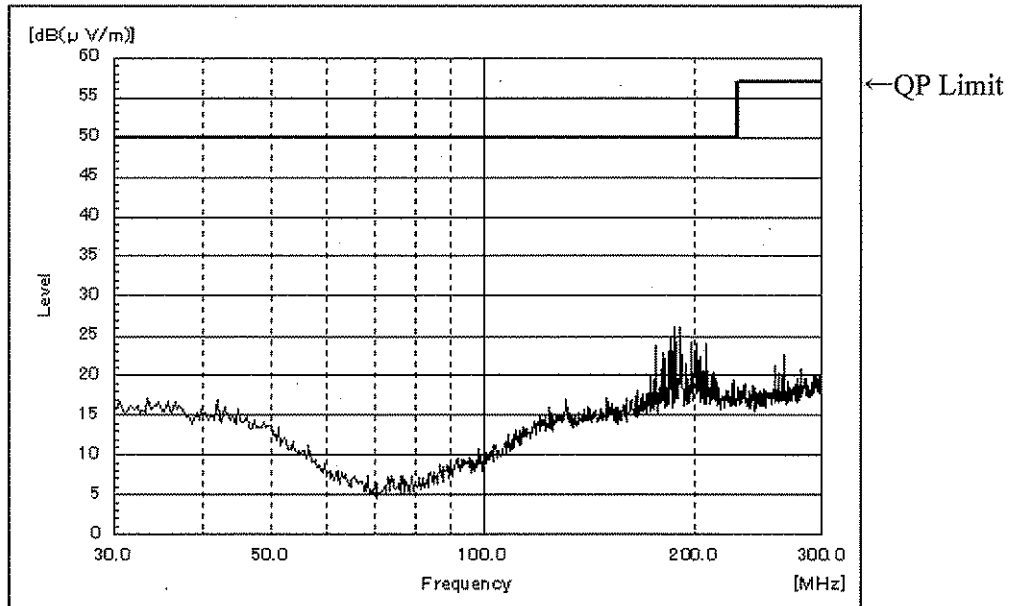
Conditions Vin : 24 VDC

Iout : 100 %

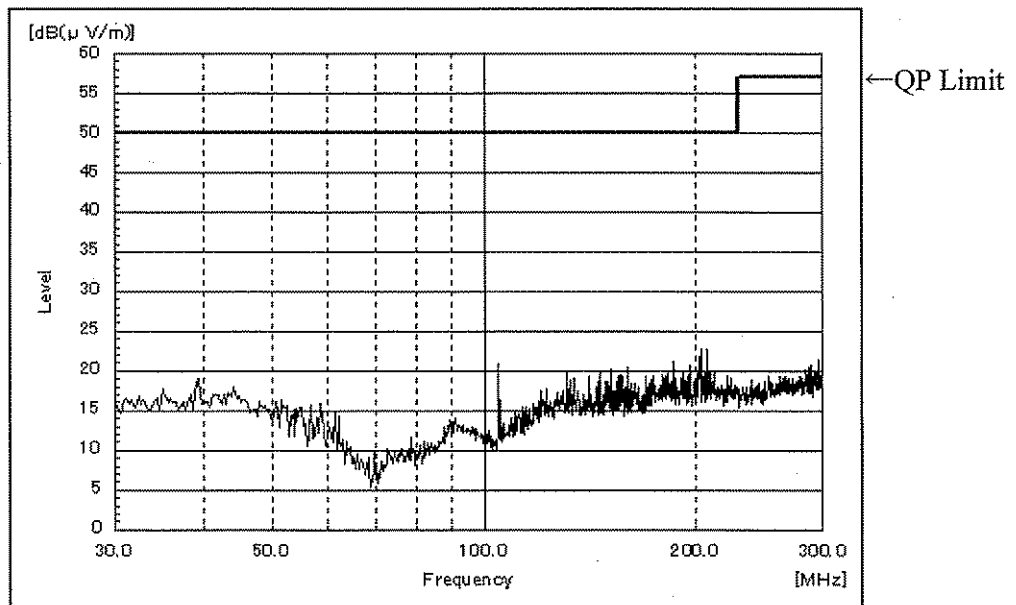
Ta : 25 °C

PSD1R5-24-1212

HORIZONTAL:



VERTICAL:



2.10 EMI特性

Electro-Magnetic Interference characteristics

(b) 雑音電界強度 (輻射ノイズ)

Radiated Emission

VCCI class A 対応アプリケーションシステム

VCCI class A application system

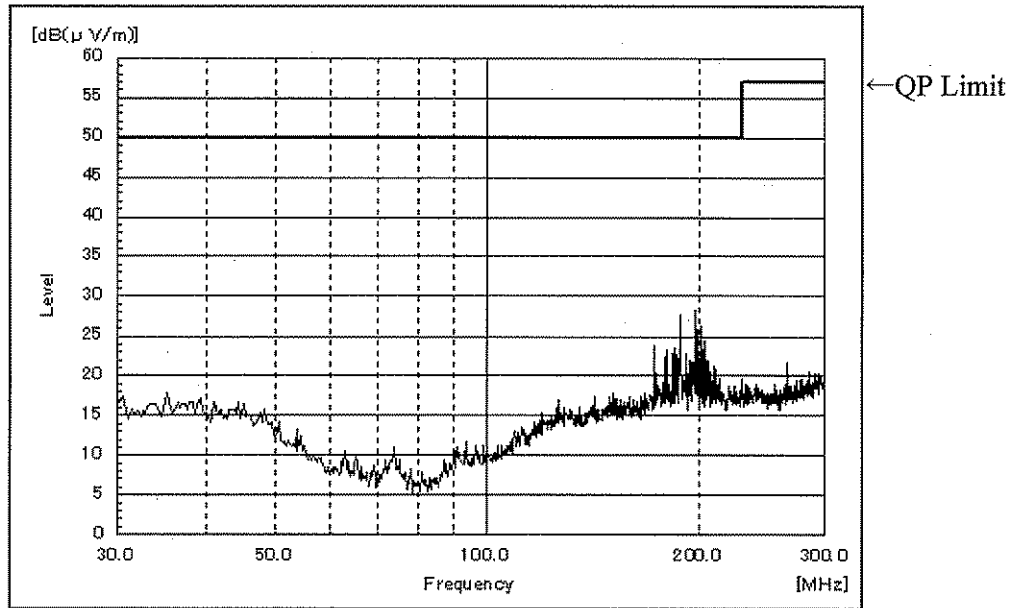
Conditions Vin : 48 VDC

Iout : 100 %

Ta : 25 °C

PSD1R5-48-1212

HORIZONTAL:



VERTICAL:

