

REGISTER MAP

Register	Name	Description
12	Unused[7:1]	
	Vout_high_byte[0]	Set up 9-bit DAC. Atomic write-protected. So both high byte and low byte have to be written in order to have the value take effect.
13	Vout_low_byte [7:0]	
14	Unused[7:4]	
	SS_rate[3]	0: 0.5mV/ μ s, 1: 1mV/ μ s
	SoftStopEnable[2]	0: disable; 1: enable
	Unused[1]	
	PGControl[0]	0: DAC-based, 1: threshold-based
15	Unused[7:3]	
	OCSet[2:0]	0: FS1403, 1: FS1404, 2: FS1406
16	Base_address[7:0]	
17	Unused[7:2]	
	OV threshold[1:0]	0: 105, 1: 110, 2: 115, 3: 120
18	Unused[7:2]	
	PG_threshold[1:0]	0: 80, 1: 85, 2: 90, 3: 95
19	Unused[7:2]	
	OT_threshold[1:0]	0: 75, 1: 85, 2: 125, 3: 145
1A	Unused [7:2]	
	Bus_voltage_sel[1]	0: 1.8–2.5V, 1: 3.3–5V
	Reserved[0]	
1B	Reserved[7:0]	
1C	Reserved[7:6]	
	Reserved[5]	
	Reserved[4]	
	Soft_Disable[3]	1: soft disable
	Reserved[2:0]	
1D	Reserved[7:5]	
	Reserved[4:2]	
	User_otp_on[1]	1: enable OTP burn (turn on OTP clock)
	Trim_OTP_on[0]	1: enable OTP burn (turn on OTP clock)
20	Unused[7:6]	
	User_pointer [5:3]	Read the index of last user bank burned
	Trim_pointer [2:0]	Read the index of last trim bank burned



Register	Name	Description
21	Status_pgood[7]	Not sticky, reflects real-time PG status
	Not used[6]	
	Not used[5]	
	Not used[4]	
	Status_enable[3]	Not sticky, reflects real-time Enable status
	Status_trim_burn[2]	1: success, 0: fail
	Status_user_burn[1]	1: success, 0: fail
	Unused[0]	

Notes:

For soft stop, set Reg 0x1C bit [3] to 1 and toggle Reg 0x14 bit [2]
 Modifying reserved bits may lead to erratic operation and/or damage

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