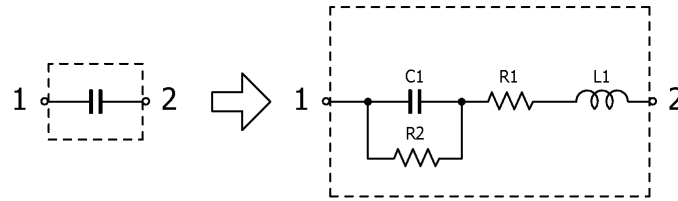


Multilayer Ceramic Chip Capacitors

Feb. 27, 2026
Simple Model

Commercial Grade Open Mode / C3225 series

Circuit Diagram



Circuit Parameters

Part No.	C1[pF]	L1[nH]	R1[ohm]	R2[Gohm]
C3225X7R1C335K200AM	3,300,000	0.600	0.0037	0.0300
C3225X7R1C475K250AM	4,700,000	0.600	0.0031	0.0210
C3225X7R1E105K115AM	1,000,000	0.600	0.0082	0.500
C3225X7R1E155K160AM	1,500,000	0.600	0.0053	0.333
C3225X7R1E225K200AM	2,200,000	0.600	0.0048	0.227
C3225X7R1H474K160AM	470,000	0.600	0.0116	1.06
C3225X7R1H684K200AM	680,000	0.600	0.0067	0.735
C3225X7R2A334K200AM	330,000	0.600	0.0112	1.52
C3225X7R2A105K200AM	1,000,000	0.600	0.0057	0.500
C3225X7R2A225K230AM	2,200,000	0.600	0.0029	0.227
C3225X7R2E104K200AM	100,000	0.600	0.0208	5.00
C3225X7R2E154K200AM	150,000	0.600	0.0132	3.33
C3225X7R2E224K200AM	220,000	0.600	0.0119	2.27
C3225X7R2J473K200AM	47,000	0.600	0.0324	10.0
C3225X7R2J683K200AM	68,000	0.600	0.0202	7.35